

# Model Name: GA-X58A-UD9

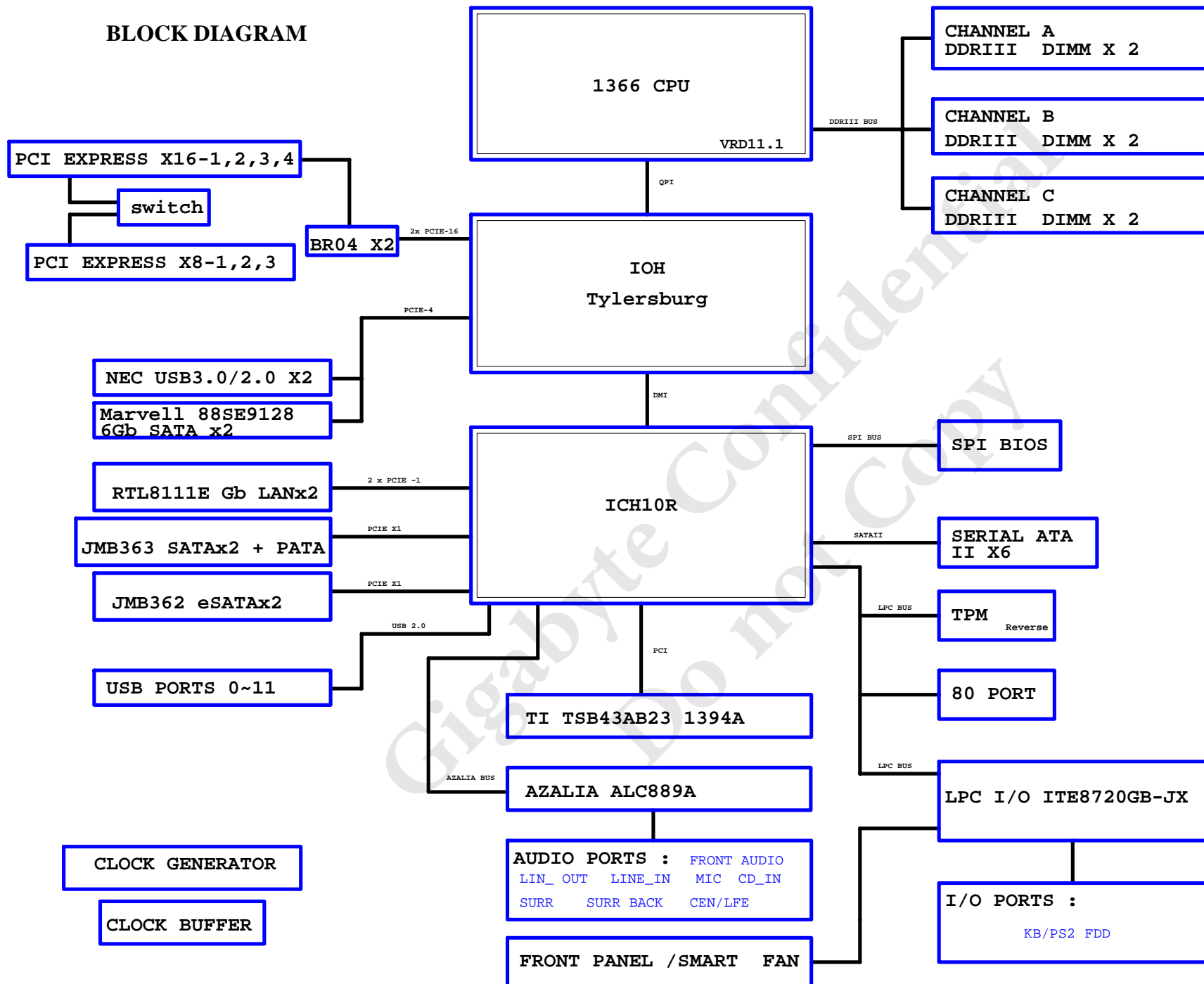
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02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
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06	LGA1366-C CPU_CSI
07	LGA1366-D CPU_GND
08	LGA1366-E CPU_PER
09-11	DDRIII CHANNEL A_B_C
12	IOH_CSI
13-14	IOH_PCIEx16 / PCIEx4
15-16	IOH_MISC_SRRAP
17-19	IOH_PWR_GND
21-29	PCIEX16_1,2,3,4 ; PCIEX8_1,2,3
30	ICH10 DMI, PCI, USB
31	ICH10 GPIO, CTRL
32	ICH10 SATA, FAN PWM
33	ICH10 VCC, GND
34	MARVELL 9128 SATAIII
35	NEC UP720200 USB3.0
36	JMB363 SATA+PATA
37	ESATA JMB362
38-39	DUAL REALTEK RTL8111E_1_2
40	TI TSB43AB23 1394
41	ITE 8720 JX(GB)
42	80 PORT
43	DUAL BIOS,TPM
44	FP,FUSB,FDD
45	HWM,KB/MS, FAN CTRL
46	COM, -PROHOT,DYNAMIC OC ,+12V PROTECT

SHEET	TITLE
47-48	CODEC & AUDIO JACK
49	ATX,OTHERS POWER
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58	ISL6312_VTTD
59	ISL6322_DDRIII
60	ISL6322_IOH_CORE
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65	ICS9LPRS914
66	CLOCK BUFFER
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<b>Gigabyte Technology</b>			
Title			
Cover Sheet			
Size	Document Number	Rev	
Custom	GA-X58A-UD9	1.0	
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# BLOCK DIAGRAM



LGA1366A

(9) DCLKA3 < E20  
(9) -DCLKA3 < E18  
(9) DCLKA2 < E18  
(9) -DCLKA2 < E18  
(9) DCLKA1 < C19  
(9) -DCLKA1 < C19  
(9) DCLKA0 < K19  
(9) -DCLKA0 < K19

-B8  
C11  
(9) -CSA5 < -CSA5  
(9) -CSA4 < -CSA4  
(9) -CSA1 < -CSA1  
(9) -CSA0 < -CSA0

-A31  
C32  
C31  
D31  
MODT\_A3  
MODT\_A2  
MODT\_A1  
MODT\_A0

(9) -SRASA < -SRASA  
(9) -SCASA < -SCASA  
(9) -SWEA < -SWEA

(9) SBA2 < SBA2  
(9) SBA1 < SBA1  
(9) SBA0 < SBA0

(9) CKEA3 < CKEA3  
(9) CKEA2 < CKEA2  
(9) CKEA1 < CKEA1  
(9) CKEA0 < CKEA0

MAAA15  
MAAA14  
MAAA13  
MAAA12  
MAAA11  
MAAA10  
MAAA9  
MAAA8  
MAAA7  
MAAA6  
MAAA5  
MAAA4  
MAAA3  
MAAA2  
MAAA1  
MAAA0

-B20

-B33  
-A22  
-B26  
-D26

DOSA0  
DOSA1  
DOSA2  
DOSA3  
DOSA4  
DOSA5  
DOSA6  
DOSA7  
DOSA8  
DOSA9

-V43  
-V42  
-M43  
-G43  
-D39  
-C39  
-D5  
-D4  
-J2  
-J1  
-F7  
-V2  
-V3  
-B36  
-B35

LGA1366A

DDR0\_CLK\_P3  
DDR0\_CLK\_N3  
DDR0\_CLK\_P2  
DDR0\_CLK\_N2  
DDR0\_CLK\_P1  
DDR0\_CLK\_N1  
DDR0\_CLK\_P0  
DDR0\_CLK\_N0

DDR0\_CS\_7  
DDR0\_CS\_6  
DDR0\_CS\_5  
DDR0\_CS\_4  
DDR0\_CS\_3  
DDR0\_CS\_2  
DDR0\_CS\_1  
DDR0\_CS\_0

DDR0\_ODT\_7  
DDR0\_ODT\_6  
DDR0\_ODT\_5  
DDR0\_ODT\_4  
DDR0\_ODT\_3  
DDR0\_ODT\_2  
DDR0\_ODT\_1  
DDR0\_ODT\_0

DDR0\_RAS\*  
DDR0\_CAS\*  
DDR0\_WE\*

DDR0\_BA2  
DDR0\_BA1  
DDR0\_BA0

DDR0\_CKE\_3  
DDR0\_CKE\_2  
DDR0\_CKE\_1  
DDR0\_CKE\_0

DDR0\_MA\_15  
DDR0\_MA\_14  
DDR0\_MA\_13  
DDR0\_MA\_12  
DDR0\_MA\_11  
DDR0\_MA\_10  
DDR0\_MA\_9  
DDR0\_MA\_8  
DDR0\_MA\_7  
DDR0\_MA\_6  
DDR0\_MA\_5  
DDR0\_MA\_4  
DDR0\_MA\_3  
DDR0\_MA\_2  
DDR0\_MA\_1  
DDR0\_MA\_0

DDR0\_MA\_PAR

DDR0\_PAR\_ERR\_3\*  
DDR0\_PAR\_ERR\_2\*  
DDR0\_PAR\_ERR\_1\*  
DDR0\_PAR\_ERR\_0\*

DDR0\_DQS\_P0  
DDR0\_DQS\_N0  
DDR0\_DQS\_P1  
DDR0\_DQS\_N1  
DDR0\_DQS\_P2  
DDR0\_DQS\_N2  
DDR0\_DQS\_P3  
DDR0\_DQS\_N3  
DDR0\_DQS\_P4  
DDR0\_DQS\_N4  
DDR0\_DQS\_P5  
DDR0\_DQS\_N5  
DDR0\_DQS\_P6  
DDR0\_DQS\_N6  
DDR0\_DQS\_P7  
DDR0\_DQS\_N7  
DDR0\_DQS\_P8  
DDR0\_DQS\_N8

DDR0\_DQS\_P9  
DDR0\_DQS\_N9  
DDR0\_DQS\_P10  
DDR0\_DQS\_N10  
DDR0\_DQS\_P11  
DDR0\_DQS\_N11  
DDR0\_DQS\_P12  
DDR0\_DQS\_N12  
DDR0\_DQS\_P13  
DDR0\_DQS\_N13  
DDR0\_DQS\_P14  
DDR0\_DQS\_N14  
DDR0\_DQS\_P15  
DDR0\_DQS\_N15  
DDR0\_DQS\_P16  
DDR0\_DQS\_N16  
DDR0\_DQS\_P17  
DDR0\_DQS\_N17

W4 MDA63  
V4 MDA62  
U3 MDA61  
U1 MDA60  
Y3 MDA59  
Y2 MDA58  
V1 MDA57  
U4 MDA56  
T3 MDA55  
R4 MDA54  
N3 MDA53  
M3 MDA52  
T2 MDA51  
T1 MDA50  
N2 MDA49  
N1 MDA48  
M2 MDA47  
H3 MDA46  
H2 MDA45  
G1 MDA44  
M1 MDA43  
L1 MDA42  
H1 MDA41  
H2 MDA40  
F2 MDA39  
F3 MDA38  
C6 MDA37  
B6 MDA36  
G3 MDA35  
F1 MDA34  
C4 MDA33  
B3 MDA32  
C38 MDA31  
D42 MDA30  
M41 MDA29  
D37 MDA28  
A38 MDA27  
C41 MDA26  
D40 MDA25  
F42 MDA24  
F43 MDA23  
J41 MDA22  
J42 MDA21  
E43 MDA20  
E42 MDA19  
H43 MDA18  
H41 MDA17  
L42 MDA16  
L43 MDA15  
P41 MDA14  
P42 MDA13  
K43 MDA12  
K42 MDA11  
N43 MDA10  
N41 MDA9  
T42 MDA8  
T41 MDA7  
U41 MDA6  
W42 MDA5  
W40 MDA4  
R42 MDA3  
R43 MDA2  
V41 MDA1  
W41 MDA0

SACB7  
SACB6  
SACB5  
SACB4  
SACB3  
SACB2  
SACB1  
SACB0

DDR\_COMP\_0  
DDR\_COMP\_1  
DDR\_COMP\_2  
DDR\_COMP\_3  
DDR\_COMP\_4  
DDR\_COMP\_5  
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DDR\_COMP\_9  
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DDR\_COMP\_48  
DDR\_COMP\_49  
DDR\_COMP\_50  
DDR\_COMP\_51  
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DDR\_COMP\_59  
DDR\_COMP\_60  
DDR\_COMP\_61  
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DDR\_COMP\_76  
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DDR\_COMP\_85  
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DDR\_COMP\_88  
DDR\_COMP\_89  
DDR\_COMP\_90  
DDR\_COMP\_91  
DDR\_COMP\_92  
DDR\_COMP\_93  
DDR\_COMP\_94  
DDR\_COMP\_95  
DDR\_COMP\_96  
DDR\_COMP\_97  
DDR\_COMP\_98  
DDR\_COMP\_99

DDR0\_RESET\*  
DDR0\_RST0 (9)

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LGA1366B

(10) DCLKB3 < H18  
(10) -DCLKB3 < H18  
(10) DCLKB2 < K18  
(10) -DCLKB2 < K18  
(10) DCLKB1 < G19  
(10) -DCLKB1 < G19  
(10) DCLKB0 < C21  
(10) -DCLKB0 < C21

E122  
C144  
E110  
C112  
E113  
C114  
-CSB1 < -CSB1  
-CSB0 < -CSB0

MODT\_B3  
MODT\_B2  
MODT\_B1  
MODT\_B0

(10) -SRASB < -SRASB  
(10) -SCASB < -SCASB  
(10) -SWEB < -SWEB

(10) SBAB2 < SBAB2  
(10) SBAB1 < SBAB1  
(10) SBAB0 < SBAB0

(10) CKEB3 < CKEB3  
(10) CKEB2 < CKEB2  
(10) CKEB1 < CKEB1  
(10) CKEB0 < CKEB0

MAAB15  
MAAB14  
MAAB13  
MAAB12  
MAAB11  
MAAB10  
MAAB9  
MAAB8  
MAAB7  
MAAB6  
MAAB5  
MAAB4  
MAAB3  
MAAB2  
MAAB1  
MAAB0

-D20  
-F27  
-F26  
-C22

DOSB0  
DOSB1  
DOSB2  
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DOSB4  
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DOSB8  
DOSB9  
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DOSB98  
DOSB99

DDR1\_CLK\_P3  
DDR1\_CLK\_N3  
DDR1\_CLK\_P2  
DDR1\_CLK\_N2  
DDR1\_CLK\_P1  
DDR1\_CLK\_N1  
DDR1\_CLK\_P0  
DDR1\_CLK\_N0

DDR1\_CS\_7  
DDR1\_CS\_6  
DDR1\_CS\_5  
DDR1\_CS\_4  
DDR1\_CS\_3  
DDR1\_CS\_2  
DDR1\_CS\_1  
DDR1\_CS\_0

DDR1\_ODT\_7  
DDR1\_ODT\_6  
DDR1\_ODT\_5  
DDR1\_ODT\_4  
DDR1\_ODT\_3  
DDR1\_ODT\_2  
DDR1\_ODT\_1  
DDR1\_ODT\_0

DDR1\_RAS\*  
DDR1\_CAS\*  
DDR1\_WE\*

DDR1\_BA\_2  
DDR1\_BA\_1  
DDR1\_BA\_0

DDR1\_CKE\_3  
DDR1\_CKE\_2  
DDR1\_CKE\_1  
DDR1\_CKE\_0

DDR1\_MA\_15  
DDR1\_MA\_14  
DDR1\_MA\_13  
DDR1\_MA\_12  
DDR1\_MA\_11  
DDR1\_MA\_10  
DDR1\_MA\_9  
DDR1\_MA\_8  
DDR1\_MA\_7  
DDR1\_MA\_6  
DDR1\_MA\_5  
DDR1\_MA\_4  
DDR1\_MA\_3  
DDR1\_MA\_2  
DDR1\_MA\_1  
DDR1\_MA\_0

DDR1\_MA\_PAR

DDR1\_PAR\_ERR\_3\*  
DDR1\_PAR\_ERR\_2\*  
DDR1\_PAR\_ERR\_1\*  
DDR1\_PAR\_ERR\_0\*

DDR1\_DQS\_P9  
DDR1\_DQS\_N9  
DDR1\_DQS\_P10  
DDR1\_DQS\_N10  
DDR1\_DQS\_P11  
DDR1\_DQS\_N11  
DDR1\_DQS\_P12  
DDR1\_DQS\_N12  
DDR1\_DQS\_P13  
DDR1\_DQS\_N13  
DDR1\_DQS\_P14  
DDR1\_DQS\_N14  
DDR1\_DQS\_P15  
DDR1\_DQS\_N15  
DDR1\_DQS\_P16  
DDR1\_DQS\_N16  
DDR1\_DQS\_P17  
DDR1\_DQS\_N17  
DDR1\_DQS\_P18  
DDR1\_DQS\_N18

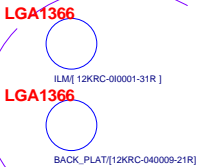
DDR1\_DQS\_P19  
DDR1\_DQS\_N19  
DDR1\_DQS\_P20  
DDR1\_DQS\_N20  
DDR1\_DQS\_P21  
DDR1\_DQS\_N21  
DDR1\_DQS\_P22  
DDR1\_DQS\_N22  
DDR1\_DQS\_P23  
DDR1\_DQS\_N23  
DDR1\_DQS\_P24  
DDR1\_DQS\_N24  
DDR1\_DQS\_P25  
DDR1\_DQS\_N25  
DDR1\_DQS\_P26  
DDR1\_DQS\_N26  
DDR1\_DQS\_P27  
DDR1\_DQS\_N27  
DDR1\_DQS\_P28  
DDR1\_DQS\_N28  
DDR1\_DQS\_P29  
DDR1\_DQS\_N29  
DDR1\_DQS\_P30  
DDR1\_DQS\_N30  
DDR1\_DQS\_P31  
DDR1\_DQS\_N31  
DDR1\_DQS\_P32  
DDR1\_DQS\_N32  
DDR1\_DQS\_P33  
DDR1\_DQS\_N33  
DDR1\_DQS\_P34  
DDR1\_DQS\_N34  
DDR1\_DQS\_P35  
DDR1\_DQS\_N35  
DDR1\_DQS\_P36  
DDR1\_DQS\_N36  
DDR1\_DQS\_P37  
DDR1\_DQS\_N37  
DDR1\_DQS\_P38  
DDR1\_DQS\_N38  
DDR1\_DQS\_P39  
DDR1\_DQS\_N39  
DDR1\_DQS\_P40  
DDR1\_DQS\_N40  
DDR1\_DQS\_P41  
DDR1\_DQS\_N41  
DDR1\_DQS\_P42  
DDR1\_DQS\_N42  
DDR1\_DQS\_P43  
DDR1\_DQS\_N43  
DDR1\_DQS\_P44  
DDR1\_DQS\_N44  
DDR1\_DQS\_P45  
DDR1\_DQS\_N45  
DDR1\_DQS\_P46  
DDR1\_DQS\_N46  
DDR1\_DQS\_P47  
DDR1\_DQS\_N47  
DDR1\_DQS\_P48  
DDR1\_DQS\_N48  
DDR1\_DQS\_P49  
DDR1\_DQS\_N49  
DDR1\_DQS\_P50  
DDR1\_DQS\_N50  
DDR1\_DQS\_P51  
DDR1\_DQS\_N51  
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DDR1\_DQS\_N52  
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DDR1\_DQS\_P55  
DDR1\_DQS\_N55  
DDR1\_DQS\_P56  
DDR1\_DQS\_N56  
DDR1\_DQS\_P57  
DDR1\_DQS\_N57  
DDR1\_DQS\_P58  
DDR1\_DQS\_N58  
DDR1\_DQS\_P59  
DDR1\_DQS\_N59  
DDR1\_DQS\_P60  
DDR1\_DQS\_N60  
DDR1\_DQS\_P61  
DDR1\_DQS\_N61  
DDR1\_DQS\_P62  
DDR1\_DQS\_N62  
DDR1\_DQS\_P63  
DDR1\_DQS\_N63  
DDR1\_DQS\_P64  
DDR1\_DQS\_N64  
DDR1\_DQS\_P65  
DDR1\_DQS\_N65  
DDR1\_DQS\_P66  
DDR1\_DQS\_N66  
DDR1\_DQS\_P67  
DDR1\_DQS\_N67  
DDR1\_DQS\_P68  
DDR1\_DQS\_N68  
DDR1\_DQS\_P69  
DDR1\_DQS\_N69  
DDR1\_DQS\_P70  
DDR1\_DQS\_N70  
DDR1\_DQS\_P71  
DDR1\_DQS\_N71  
DDR1\_DQS\_P72  
DDR1\_DQS\_N72  
DDR1\_DQS\_P73  
DDR1\_DQS\_N73  
DDR1\_DQS\_P74  
DDR1\_DQS\_N74  
DDR1\_DQS\_P75  
DDR1\_DQS\_N75  
DDR1\_DQS\_P76  
DDR1\_DQS\_N76  
DDR1\_DQS\_P77  
DDR1\_DQS\_N77  
DDR1\_DQS\_P78  
DDR1\_DQS\_N78  
DDR1\_DQS\_P79  
DDR1\_DQS\_N79  
DDR1\_DQS\_P80  
DDR1\_DQS\_N80  
DDR1\_DQS\_P81  
DDR1\_DQS\_N81  
DDR1\_DQS\_P82  
DDR1\_DQS\_N82  
DDR1\_DQS\_P83  
DDR1\_DQS\_N83  
DDR1\_DQS\_P84  
DDR1\_DQS\_N84  
DDR1\_DQS\_P85  
DDR1\_DQS\_N85  
DDR1\_DQS\_P86  
DDR1\_DQS\_N86  
DDR1\_DQS\_P87  
DDR1\_DQS\_N87  
DDR1\_DQS\_P88  
DDR1\_DQS\_N88  
DDR1\_DQS\_P89  
DDR1\_DQS\_N89  
DDR1\_DQS\_P90  
DDR1\_DQS\_N90  
DDR1\_DQS\_P91  
DDR1\_DQS\_N91  
DDR1\_DQS\_P92  
DDR1\_DQS\_N92  
DDR1\_DQS\_P93  
DDR1\_DQS\_N93  
DDR1\_DQS\_P94  
DDR1\_DQS\_N94  
DDR1\_DQS\_P95  
DDR1\_DQS\_N95  
DDR1\_DQS\_P96  
DDR1\_DQS\_N96  
DDR1\_DQS\_P97  
DDR1\_DQS\_N97  
DDR1\_DQS\_P98  
DDR1\_DQS\_N98  
DDR1\_DQS\_P99  
DDR1\_DQS\_N99

DDR1\_COMP\_1  
DDR1\_COMP\_2  
DDR1\_COMP\_3  
DDR1\_COMP\_4  
DDR1\_COMP\_5  
DDR1\_COMP\_6  
DDR1\_COMP\_7  
DDR1\_COMP\_8  
DDR1\_COMP\_9  
DDR1\_COMP\_10  
DDR1\_COMP\_11  
DDR1\_COMP\_12  
DDR1\_COMP\_13  
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DDR1\_COMP\_15  
DDR1\_COMP\_16  
DDR1\_COMP\_17  
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DDR1\_COMP\_20  
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DDR1\_COMP\_28  
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DDR1\_COMP\_30  
DDR1\_COMP\_31  
DDR1\_COMP\_32  
DDR1\_COMP\_33  
DDR1\_COMP\_34  
DDR1\_COMP\_35  
DDR1\_COMP\_36  
DDR1\_COMP\_37  
DDR1\_COMP\_38  
DDR1\_COMP\_39  
DDR1\_COMP\_40  
DDR1\_COMP\_41  
DDR1\_COMP\_42  
DDR1\_COMP\_43  
DDR1\_COMP\_44  
DDR1\_COMP\_45  
DDR1\_COMP\_46  
DDR1\_COMP\_47  
DDR1\_COMP\_48  
DDR1\_COMP\_49  
DDR1\_COMP\_50  
DDR1\_COMP\_51  
DDR1\_COMP\_52  
DDR1\_COMP\_53  
DDR1\_COMP\_54  
DDR1\_COMP\_55  
DDR1\_COMP\_56  
DDR1\_COMP\_57  
DDR1\_COMP\_58  
DDR1\_COMP\_59  
DDR1\_COMP\_60  
DDR1\_COMP\_61  
DDR1\_COMP\_62  
DDR1\_COMP\_63  
DDR1\_COMP\_64  
DDR1\_COMP\_65  
DDR1\_COMP\_66  
DDR1\_COMP\_67  
DDR1\_COMP\_68  
DDR1\_COMP\_69  
DDR1\_COMP\_70  
DDR1\_COMP\_71  
DDR1\_COMP\_72  
DDR1\_COMP\_73  
DDR1\_COMP\_74  
DDR1\_COMP\_75  
DDR1\_COMP\_76  
DDR1\_COMP\_77  
DDR1\_COMP\_78  
DDR1\_COMP\_79  
DDR1\_COMP\_80  
DDR1\_COMP\_81  
DDR1\_COMP\_82  
DDR1\_COMP\_83  
DDR1\_COMP\_84  
DDR1\_COMP\_85  
DDR1\_COMP\_86  
DDR1\_COMP\_87  
DDR1\_COMP\_88  
DDR1\_COMP\_89  
DDR1\_COMP\_90  
DDR1\_COMP\_91  
DDR1\_COMP\_92  
DDR1\_COMP\_93  
DDR1\_COMP\_94  
DDR1\_COMP\_95  
DDR1\_COMP\_96  
DDR1\_COMP\_97  
DDR1\_COMP\_98  
DDR1\_COMP\_99

DDR1\_RESET\*  
DDR1\_RST1 (10)

2 OF 10

CPU-SK/1366P/S/15



CPU-SK/1366P/S/15

DDR\_COMP1 R3873 24.9/4/1

Gigabyte Technology			
Title			
LGA1366-A CPU_DDRA_B			
Size			
Document Number			
GA-X58A-UD9			
Rev			
1.0			
Date			
Sheet 4 of 68			

(11) DCLKC3 ↔ L22  
 (11) -DCLKC3 ↔ L21  
 (11) DCLKC2 ↔ H21  
 (11) -DCLKC2 ↔ G21  
 (11) DCLKC1 ↔ L20  
 (11) -DCLKC1 ↔ K20  
 (11) DCLKC0 ↔ J22  
 (11) -DCLKC0 ↔ J21

(11) -CSC5 ↔ -CSC5  
 (11) -CSC4 ↔ -CSC4  
 (11) -CSC1 ↔ -CSC1  
 (11) -CSC0 ↔ -CSC0

(11) -SRASC ↔ -SRASC  
 (11) -SCASC ↔ -SCASC  
 (11) -SWEC ↔ -SWEC

(11) SBAC2 ↔ SBAC2  
 (11) SBAC1 ↔ SBAC1  
 (11) SBAC0 ↔ SBAC0

(11) CKEC3 ↔ CKEC3  
 (11) CKEC2 ↔ CKEC2  
 (11) CKEC1 ↔ CKEC1  
 (11) CKEC0 ↔ CKEC0

(11) MAAC15 ↔ G25  
 (11) MAAC14 ↔ H24  
 (11) MAAC13 ↔ F15  
 (11) MAAC12 ↔ G23  
 (11) MAAC11 ↔ H23  
 (11) MAAC10 ↔ H17  
 (11) MAAC9 ↔ H22  
 (11) MAAC8 ↔ L25  
 (11) MAAC7 ↔ J24  
 (11) MAAC6 ↔ K22  
 (11) MAAC5 ↔ K23  
 (11) MAAC4 ↔ F20  
 (11) MAAC3 ↔ J20  
 (11) MAAC2 ↔ G18  
 (11) MAAC1 ↔ K17  
 (11) MAAC0 ↔ A18

(11) -B18 ↔ DDR2\_MA\_PAR  
 (11) -K25 ↔ DDR2\_PAR\_ERR\_3\*  
 (11) -F24 ↔ DDR2\_PAR\_ERR\_2\*  
 (11) -J25 ↔ DDR2\_PAR\_ERR\_1\*  
 (11) -F21 ↔ DDR2\_PAR\_ERR\_0\*

(11) -DQSC0 ↔ W37  
 (11) -DQSC1 ↔ W36  
 (11) -DQSC2 ↔ T37  
 (11) -DQSC3 ↔ T38  
 (11) -DQSC4 ↔ K40  
 (11) -DQSC5 ↔ K39  
 (11) -DQSC6 ↔ E38  
 (11) -DQSC7 ↔ E40  
 (11) -DQSC8 ↔ J10  
 (11) -DQSC9 ↔ J9  
 (11) -DQSC10 ↔ K7  
 (11) -DQSC11 ↔ P6  
 (11) -DQSC12 ↔ P5  
 (11) -DQSC13 ↔ U8  
 (11) -DQSC14 ↔ T8  
 (11) -DQSC15 ↔ G29  
 (11) -DQSC16 ↔ G30

(11) -DQSC17 ↔ L7  
 (11) -DQSC18 ↔ K7  
 (11) -DQSC19 ↔ P6  
 (11) -DQSC20 ↔ P5  
 (11) -DQSC21 ↔ U8  
 (11) -DQSC22 ↔ T8  
 (11) -DQSC23 ↔ G29  
 (11) -DQSC24 ↔ G30

(11) -U35 ↔ DDR2\_DQS\_P9  
 (11) -T35 ↔ DDR2\_DQS\_N9  
 (11) -T40 ↔ DDR2\_DQS\_P10  
 (11) -M38 ↔ DDR2\_DQS\_N10  
 (11) -L38 ↔ DDR2\_DQS\_P11  
 (11) -G38 ↔ DDR2\_DQS\_N11  
 (11) -H11 ↔ DDR2\_DQS\_P12  
 (11) -J11 ↔ DDR2\_DQS\_N12  
 (11) -K9 ↔ DDR2\_DQS\_P13  
 (11) -N4 ↔ DDR2\_DQS\_N14  
 (11) -P4 ↔ DDR2\_DQS\_P15  
 (11) -V6 ↔ DDR2\_DQS\_N16  
 (11) -H31 ↔ DDR2\_DQS\_P17  
 (11) -G31 ↔ DDR2\_DQS\_N17

DDR2\_DQ\_63 U9 MDC63  
 DDR2\_DQ\_62 V8 MDC62  
 DDR2\_DQ\_61 T7 MDC61  
 DDR2\_DQ\_60 T6 MDC60  
 DDR2\_DQ\_59 U10 MDC59  
 DDR2\_DQ\_58 T10 MDC58  
 DDR2\_DQ\_57 U6 MDC57  
 DDR2\_DQ\_56 U5 MDC56  
 DDR2\_DQ\_55 R9 MDC55  
 DDR2\_DQ\_54 R10 MDC54  
 DDR2\_DQ\_53 N8 MDC53  
 DDR2\_DQ\_52 P10 MDC52  
 DDR2\_DQ\_51 P9 MDC51  
 DDR2\_DQ\_50 N6 MDC50  
 DDR2\_DQ\_49 P7 MDC49  
 DDR2\_DQ\_48 M8 MDC48  
 DDR2\_DQ\_47 L8 MDC47  
 DDR2\_DQ\_46 M10 MDC46  
 DDR2\_DQ\_45 L11 MDC45  
 DDR2\_DQ\_44 N9 MDC44  
 DDR2\_DQ\_43 M9 MDC43  
 DDR2\_DQ\_42 K10 MDC42  
 DDR2\_DQ\_41 L12 MDC41  
 DDR2\_DQ\_40 H12 MDC40  
 DDR2\_DQ\_39 G10 MDC39  
 DDR2\_DQ\_38 G11 MDC38  
 DDR2\_DQ\_37 G11 MDC37  
 DDR2\_DQ\_36 H13 MDC36  
 DDR2\_DQ\_35 H12 MDC35  
 DDR2\_DQ\_34 K12 MDC34  
 DDR2\_DQ\_33 E38 MDC33  
 DDR2\_DQ\_32 F38 MDC32  
 DDR2\_DQ\_31 G39 MDC31  
 DDR2\_DQ\_30 H39 MDC30  
 DDR2\_DQ\_29 H37 MDC29  
 DDR2\_DQ\_28 F40 MDC28  
 DDR2\_DQ\_27 F40 MDC27  
 DDR2\_DQ\_26 G40 MDC26  
 DDR2\_DQ\_25 K38 MDC25  
 DDR2\_DQ\_24 L40 MDC24  
 DDR2\_DQ\_23 N36 MDC23  
 DDR2\_DQ\_22 P40 MDC22  
 DDR2\_DQ\_21 J39 MDC21  
 DDR2\_DQ\_20 M40 MDC20  
 DDR2\_DQ\_19 M39 MDC19  
 DDR2\_DQ\_18 R40 MDC18  
 DDR2\_DQ\_17 T41 MDC17  
 DDR2\_DQ\_16 V39 MDC16  
 DDR2\_DQ\_15 T36 MDC15  
 DDR2\_DQ\_14 R39 MDC14  
 DDR2\_DQ\_13 U38 MDC13  
 DDR2\_DQ\_12 V38 MDC12  
 DDR2\_DQ\_11 V37 MDC11  
 DDR2\_DQ\_10 V34 MDC10  
 DDR2\_DQ\_9 U34 MDC9  
 DDR2\_DQ\_8 U36 MDC8  
 DDR2\_DQ\_7 V36 MDC7  
 DDR2\_DQ\_6 W35 MDC6  
 DDR2\_DQ\_5 W34 MDC5  
 DDR2\_DQ\_4 W34 MDC4  
 DDR2\_DQ\_3 W34 MDC3  
 DDR2\_DQ\_2 W34 MDC2  
 DDR2\_DQ\_1 W34 MDC1  
 DDR2\_DQ\_0 W34 MDC0

DDR2\_ECC\_7 F30 SCBC7  
 DDR2\_ECC\_6 F31 SCBC6  
 DDR2\_ECC\_5 J30 SCBC5  
 DDR2\_ECC\_4 J31 SCBC4  
 DDR2\_ECC\_3 E30 SCBC3  
 DDR2\_ECC\_2 E29 SCBC2  
 DDR2\_ECC\_1 F32 SCBC1  
 DDR2\_ECC\_0 H32 SCBC0

DDR\_COMP\_2 AC1 DDR\_COMP2 R3874 130/4/1  
 DDR2\_RESET\* E32 → -DDR3\_RST2 (11)

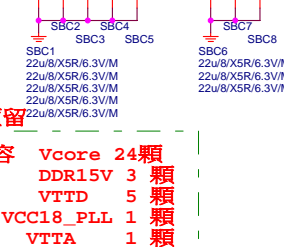
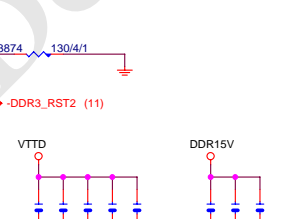
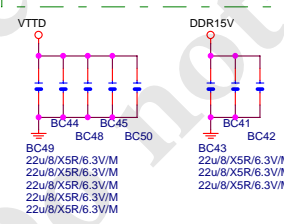
DDR2\_DQS\_P9 U35  
 DDR2\_DQS\_N9 T35  
 DDR2\_DQS\_P10 T40  
 DDR2\_DQS\_N10 M38  
 DDR2\_DQS\_P11 L38  
 DDR2\_DQS\_N11 G38  
 DDR2\_DQS\_P12 H11  
 DDR2\_DQS\_N12 J11  
 DDR2\_DQS\_P13 K9  
 DDR2\_DQS\_N14 N4  
 DDR2\_DQS\_P15 P4  
 DDR2\_DQS\_N16 V6  
 DDR2\_DQS\_P17 H31  
 DDR2\_DQS\_N17 G31

## BOTTOM 預留

CPU下電容 Vcore 24顆  
 DDR15V 3顆  
 VTTD 5顆  
 VCC18\_PLL 1顆  
 VTTA 1顆

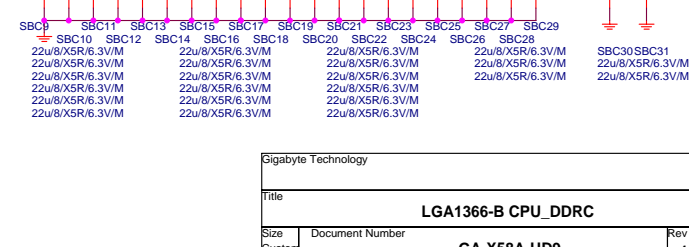
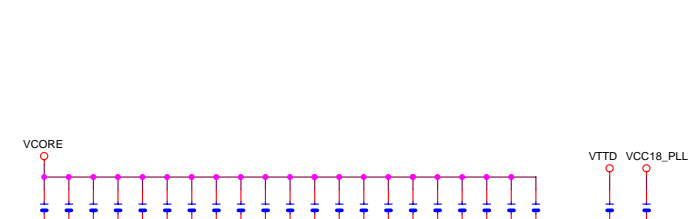
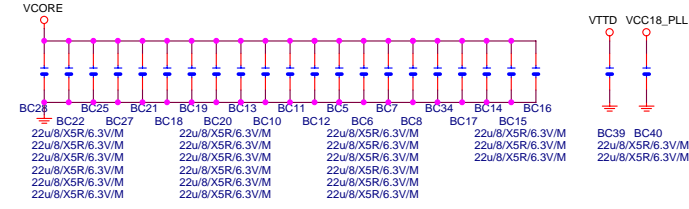
TOP

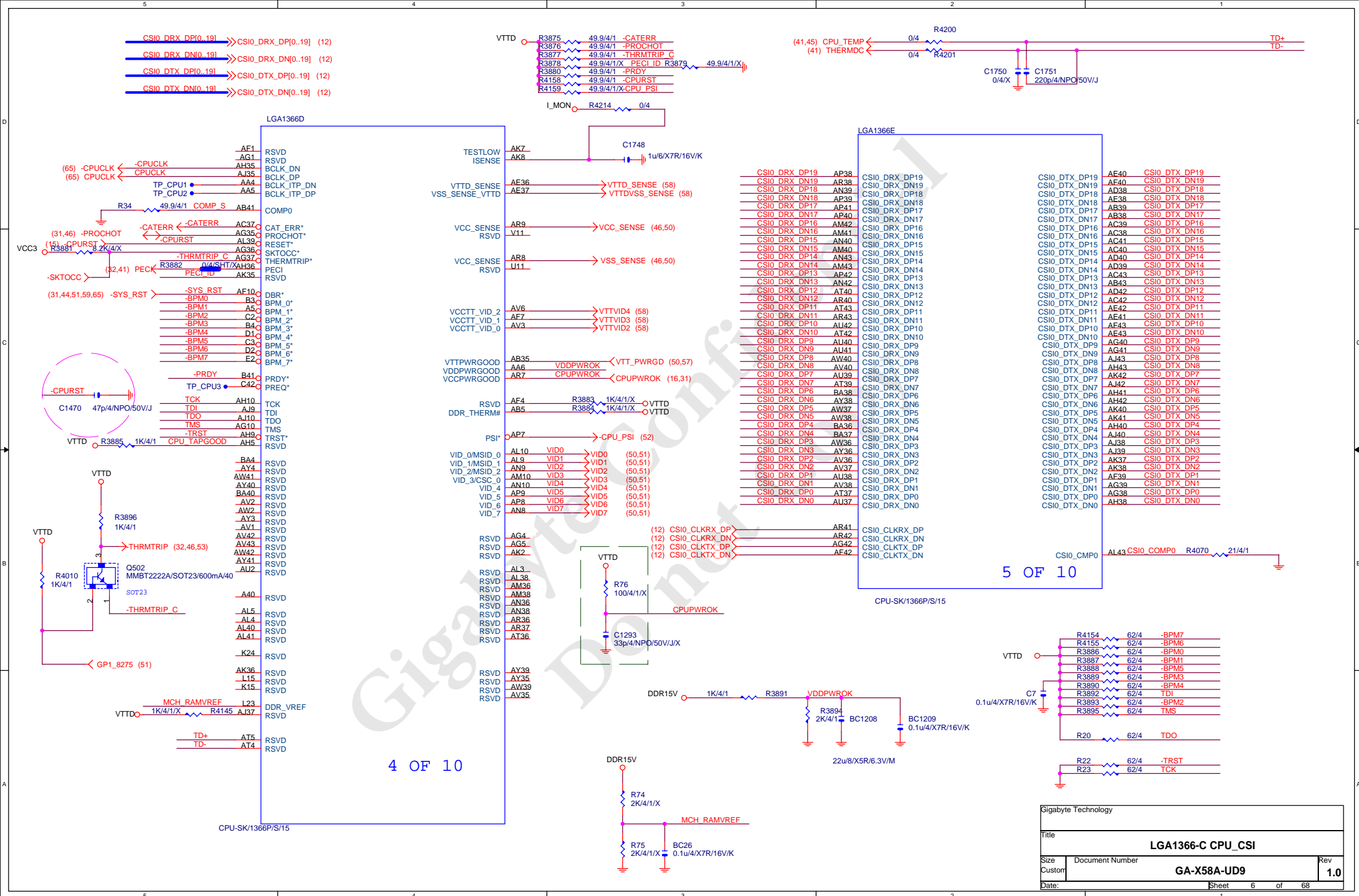
CPU下電容 Vcore 24顆  
 DDR15V 3顆  
 VTTD 5顆  
 VCC18\_PLL 1顆  
 VTTA 1顆



AM8 RSVD  
 AL8 RSVD  
 AM7 RSVD  
 AN7 RSVD  
 AN6 RSVD  
 AN5 RSVD  
 AN4 RSVD  
 AN3 RSVD  
 AP3 RSVD  
 AP4 RSVD  
 AM2 RSVD  
 AM3 RSVD  
 AN1 RSVD  
 AM1 RSVD  
 AP2 RSVD  
 AN2 RSVD  
 AR4 RSVD  
 AR5 RSVD  
 AT1 RSVD  
 AR1 RSVD  
 AT3 RSVD  
 AT2 RSVD  
 AU4 RSVD  
 AU3 RSVD  
 AU2 RSVD  
 AW4 RSVD  
 AW5 RSVD  
 AU7 RSVD  
 AU6 RSVD  
 AY6 RSVD  
 AY5 RSVD  
 BA7 RSVD  
 BA6 RSVD  
 AV5 RSVD  
 AW5 RSVD  
 AY8 RSVD  
 BA8 RSVD  
 AV7 RSVD  
 AW7 RSVD  
 AU8 RSVD  
 AV8 RSVD

AT6 RSVD  
 AR6 RSVD  
 AF6 RSVD  
 AE6 RSVD





LGA1366I

B42	VSS	AV23	VSS
B37	VSS	AV22	VSS
B2	VSS	AV20	VSS
A41	VSS	AV17	VSS
A39	VSS	AV14	VSS
A35	VSS	AV11	VSS
A6	VSS	AV4	VSS
A4	VSS	AU43	VSS
C5	VSS	AU36	VSS
E6	VSS	AU35	VSS
E1	VSS	AU32	VSS
D43	VSS	AU29	VSS
D38	VSS	AU26	VSS
D33	VSS	AU23	VSS
D8	VSS	AU22	VSS
D3	VSS	AU20	VSS
C43	VSS	AU17	VSS
C40	VSS	AU11	VSS
C35	VSS	AU14	VSS
E36	VSS	AU5	VSS
F41	VSS	AU23	VSS
F4	VSS	AT41	VSS
F9	VSS	AT38	VSS
F29	VSS	AT35	VSS
F34	VSS	AT32	VSS
F39	VSS	AT29	VSS
G2	VSS	AT26	VSS
G7	VSS	AT23	VSS
G12	VSS	AT22	VSS
G32	VSS	AT20	VSS
G37	VSS	AT17	VSS
G42	VSS	AT14	VSS
H5	VSS	AT11	VSS
H10	VSS	AT8	VSS
H30	VSS	AT7	VSS
H35	VSS	AK39	VSS
BA39	VSS	AK23	VSS
BA35	VSS	AK35	VSS
BA29	VSS	AK32	VSS
BA26	VSS	AK29	VSS
BA20	VSS	AK27	VSS
BA17	VSS	AK26	VSS
BA14	VSS	AK23	VSS
BA11	VSS	AK22	VSS
BA5	VSS	AK20	VSS
BA3	VSS	AK17	VSS
AY42	VSS	AK14	VSS
AY37	VSS	AK10	VSS
AY29	VSS	AK9	VSS
AY26	VSS	AK3	VSS
AY23	VSS	AJ41	VSS
AY32	VSS	AJ36	VSS
AY22	VSS	AJ34	VSS
AY20	VSS	AJ5	VSS
AY17	VSS	AH39	VSS
AY14	VSS	AH37	VSS
AY11	VSS	AH34	VSS
AY7	VSS	AH7	VSS
AY2	VSS	AP36	VSS
AW35	VSS	AP35	VSS
AW32	VSS	AP32	VSS
AW29	VSS	AP29	VSS
AW26	VSS	AP26	VSS
AW23	VSS	AP23	VSS
AW22	VSS	AP22	VSS
AW20	VSS	AP20	VSS
AW17	VSS	AP17	VSS
AW14	VSS	AP14	VSS
AW11	VSS	AP11	VSS
AW8	VSS	AP10	VSS
AW6	VSS	AP6	VSS
AW1	VSS	AP5	VSS
AV41	VSS	AD43	VSS
AV39	VSS	AD41	VSS
AV32	VSS	AD37	VSS
AV29	VSS	AD33	VSS
AV26	VSS	AD11	VSS
		AC36	VSS
		AC9	VSS
		AC7	VSS
		AC5	VSS
		AC2	VSS
		AN17	VSS
		AN14	VSS
		AN11	VSS

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CPU-SK/1366P/S/15

LGA1366J

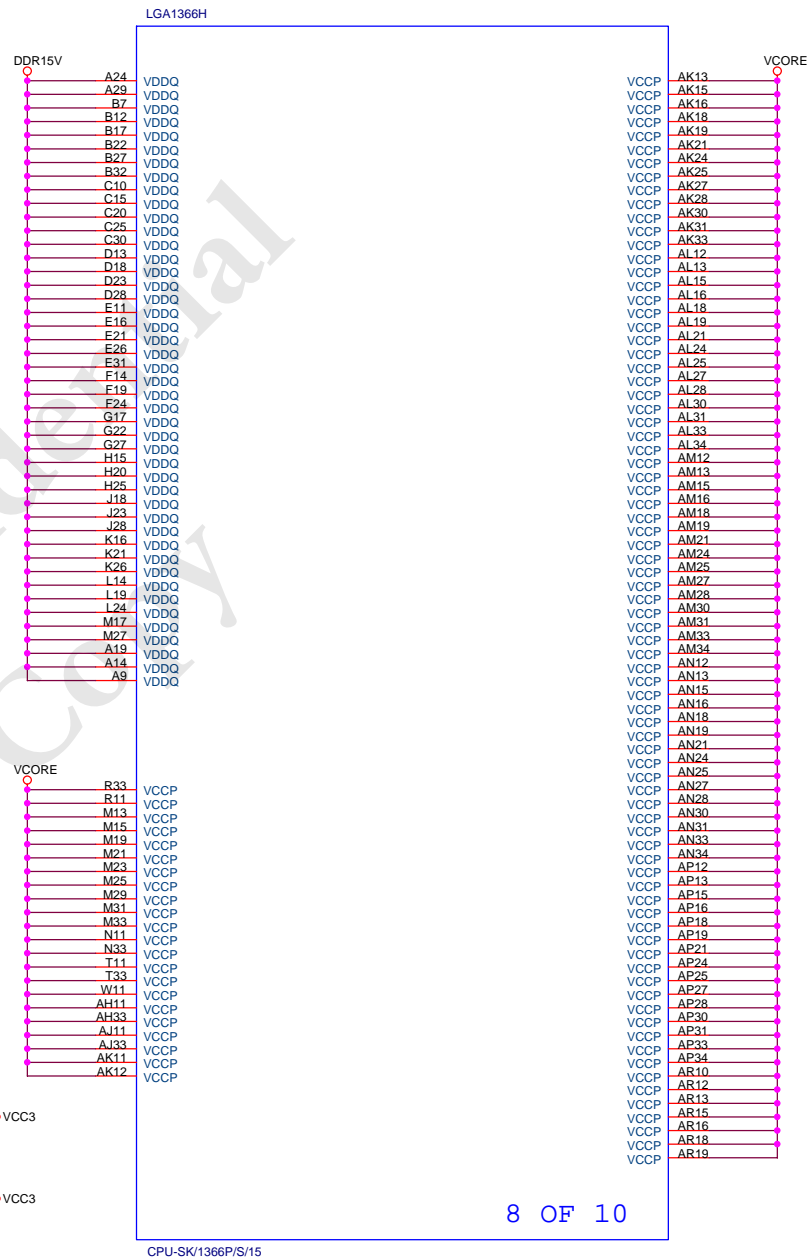
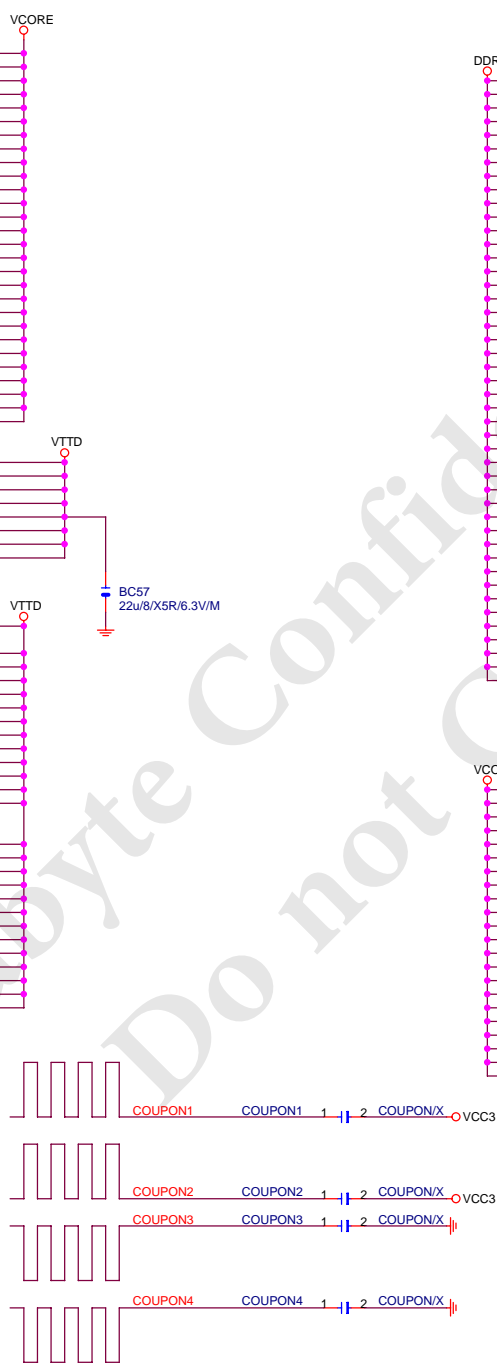
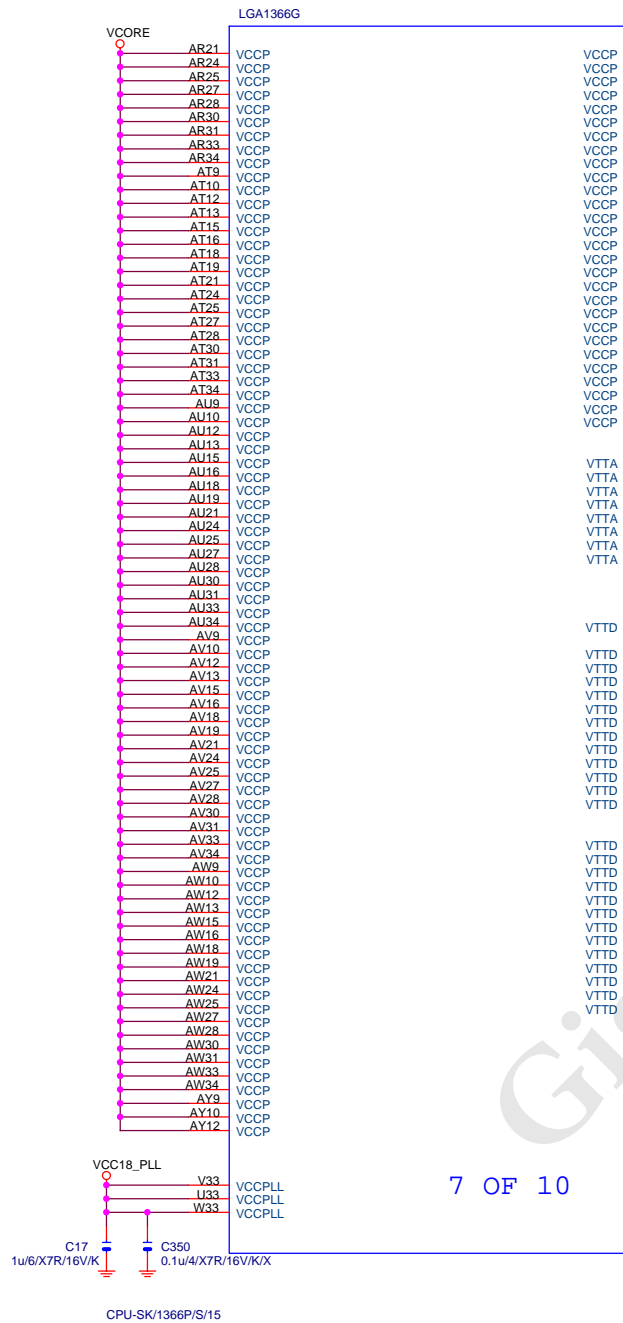
AN7	VSS	AB40	VSS
AN3	VSS	AB37	VSS
AM39	VSS	AB7	VSS
AM37	VSS	AB4	VSS
AM35	VSS	AA39	VSS
AM32	VSS	AA38	VSS
AM29	VSS	AA34	VSS
AM26	VSS	AA9	VSS
AM23	VSS	AA3	VSS
AM22	VSS	Y41	VSS
AM20	VSS	Y36	VSS
AM17	VSS	Y33	VSS
AM14	VSS	Y11	VSS
AM11	VSS	Y6	VSS
AM9	VSS	Y1	VSS
AM5	VSS	W43	VSS
AL42	VSS	W38	VSS
AL37	VSS	W8	VSS
AL36	VSS	W3	VSS
AL35	VSS	V40	VSS
AL32	VSS	V35	VSS
AL29	VSS	V10	VSS
AL26	VSS	V5	VSS
AL23	VSS	U45	VSS
AL22	VSS	U42	VSS
AL20	VSS	U37	VSS
AL17	VSS	U7	VSS
AL14	VSS	U2	VSS
AL11	VSS	T39	VSS
AL7	VSS	T34	VSS
AL2	VSS	T9	VSS
AL1	VSS	T4	VSS
AK43	VSS	R41	VSS
AK39	VSS	R36	VSS
AK34	VSS	R6	VSS
AK32	VSS	R1	VSS
AK29	VSS	P43	VSS
AK27	VSS	P38	VSS
AK26	VSS	P33	VSS
AK23	VSS	P11	VSS
AK35	VSS	P8	VSS
AK32	VSS	P3	VSS
AK29	VSS	N40	VSS
AK27	VSS	N35	VSS
AK20	VSS	N10	VSS
AK17	VSS	N5	VSS
AK14	VSS	M42	VSS
AK10	VSS	M37	VSS
AK9	VSS	M32	VSS
AK3	VSS	M30	VSS
AJ41	VSS	M28	VSS
AJ36	VSS	M26	VSS
AJ34	VSS	M24	VSS
AJ5	VSS	M22	VSS
AH39	VSS	M20	VSS
AH37	VSS	M18	VSS
AH34	VSS	M16	VSS
AH7	VSS	M14	VSS
AP36	VSS	M12	VSS
AP35	VSS	M7	VSS
AP32	VSS	M2	VSS
AP29	VSS	L39	VSS
AP26	VSS	L34	VSS
AP23	VSS	L29	VSS
AP22	VSS	L9	VSS
AP20	VSS	L4	VSS
AP17	VSS	K41	VSS
AP14	VSS	K36	VSS
AP11	VSS	K31	VSS
AP10	VSS	K11	VSS
AP6	VSS	K6	VSS
AP5	VSS	K1	VSS
AD43	VSS	J43	VSS
AD41	VSS	J38	VSS
AD37	VSS	J33	VSS
AD33	VSS	J13	VSS
AD11	VSS	J8	VSS
AC36	VSS	J3	VSS
AC9	VSS	H40	VSS
AC7	VSS		
AC5	VSS		
AC2	VSS		
AN17	VSS		
AN14	VSS		
AN11	VSS		

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CPU-SK/1366P/S/15

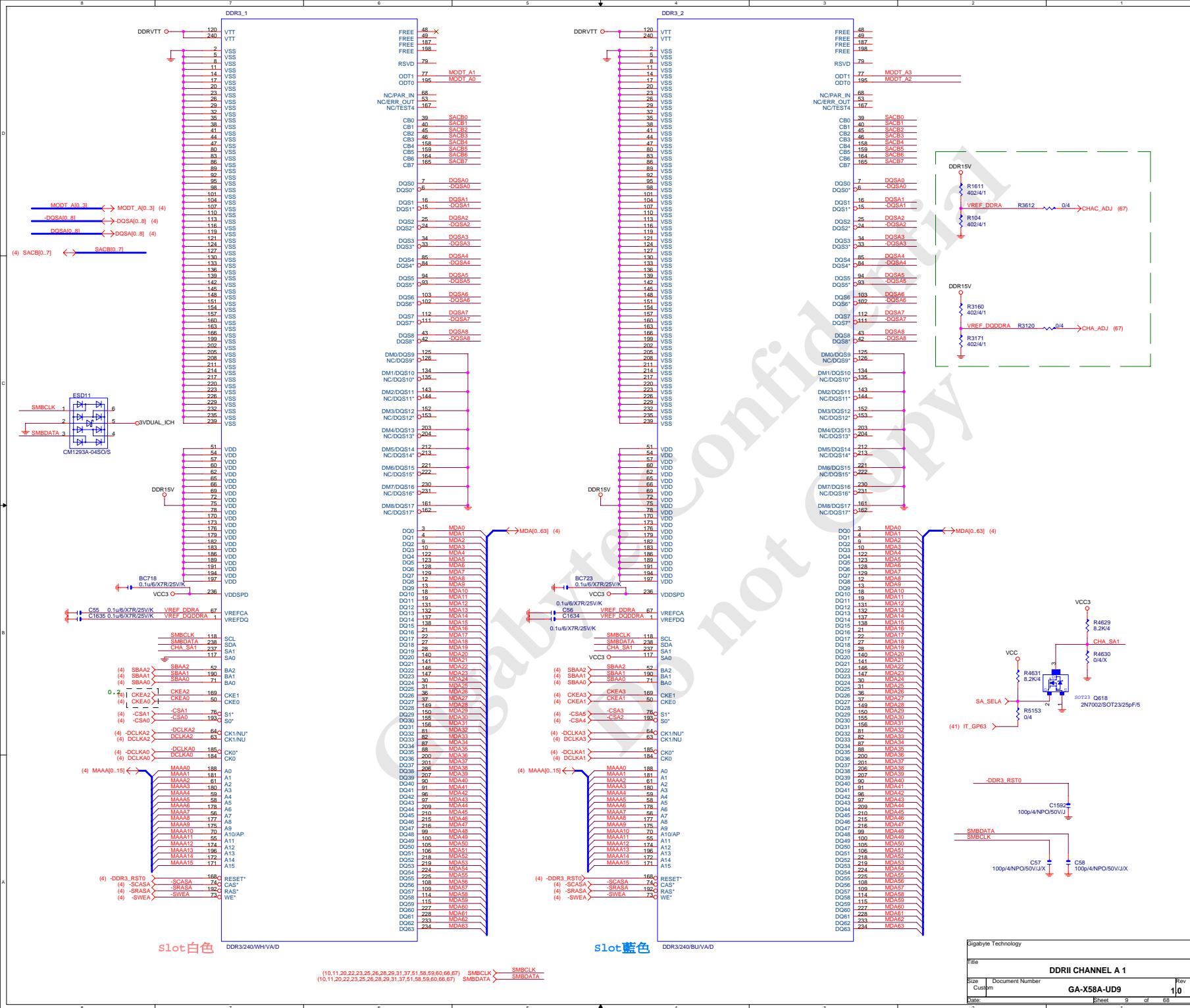
Gigabyte Technology		
Title		
LGA1366-D GND		
Size	Document Number	Rev
Custom	GA-X58A-UD9	1.0
Date:	Sheet 7 of 68	

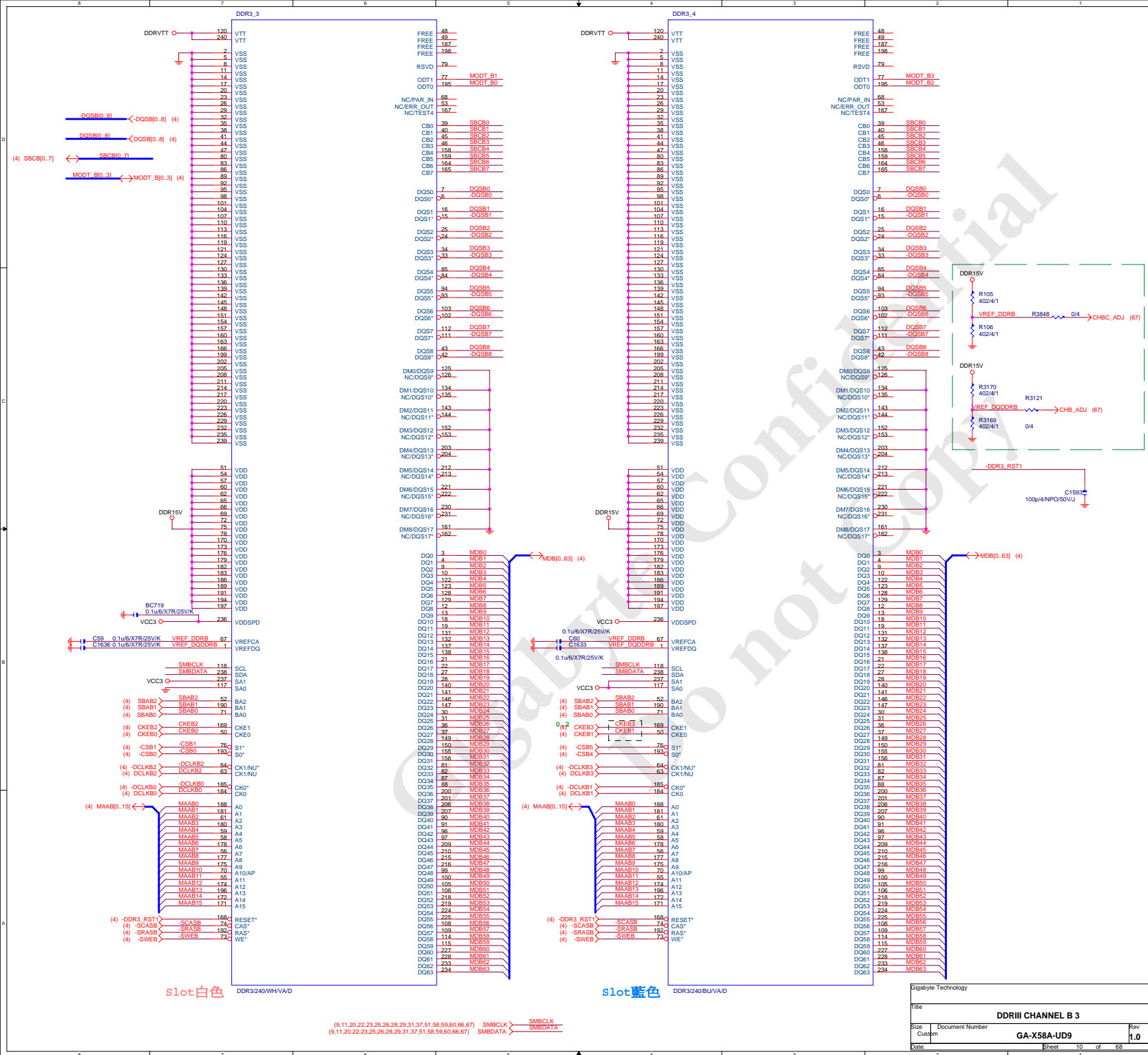


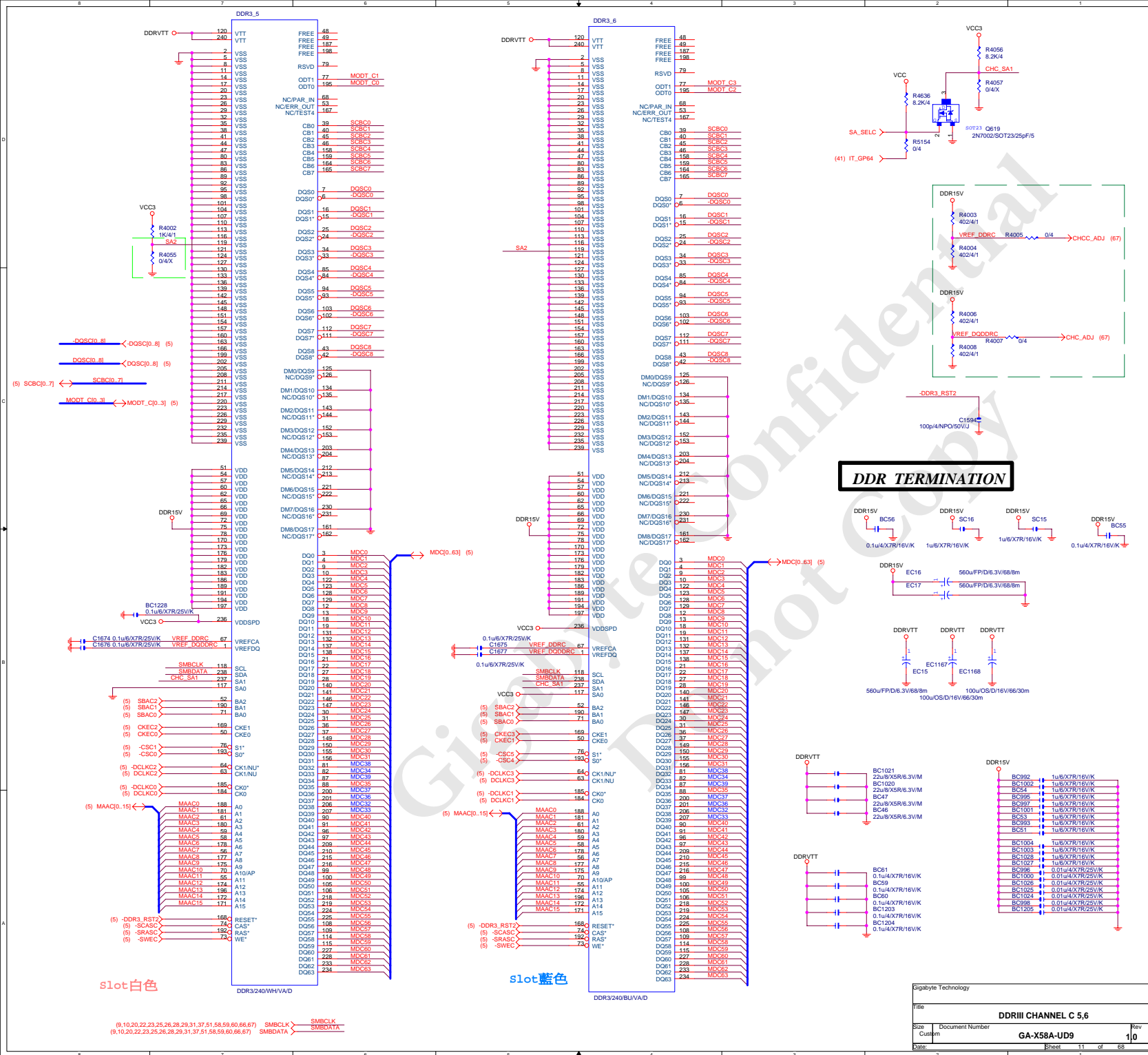


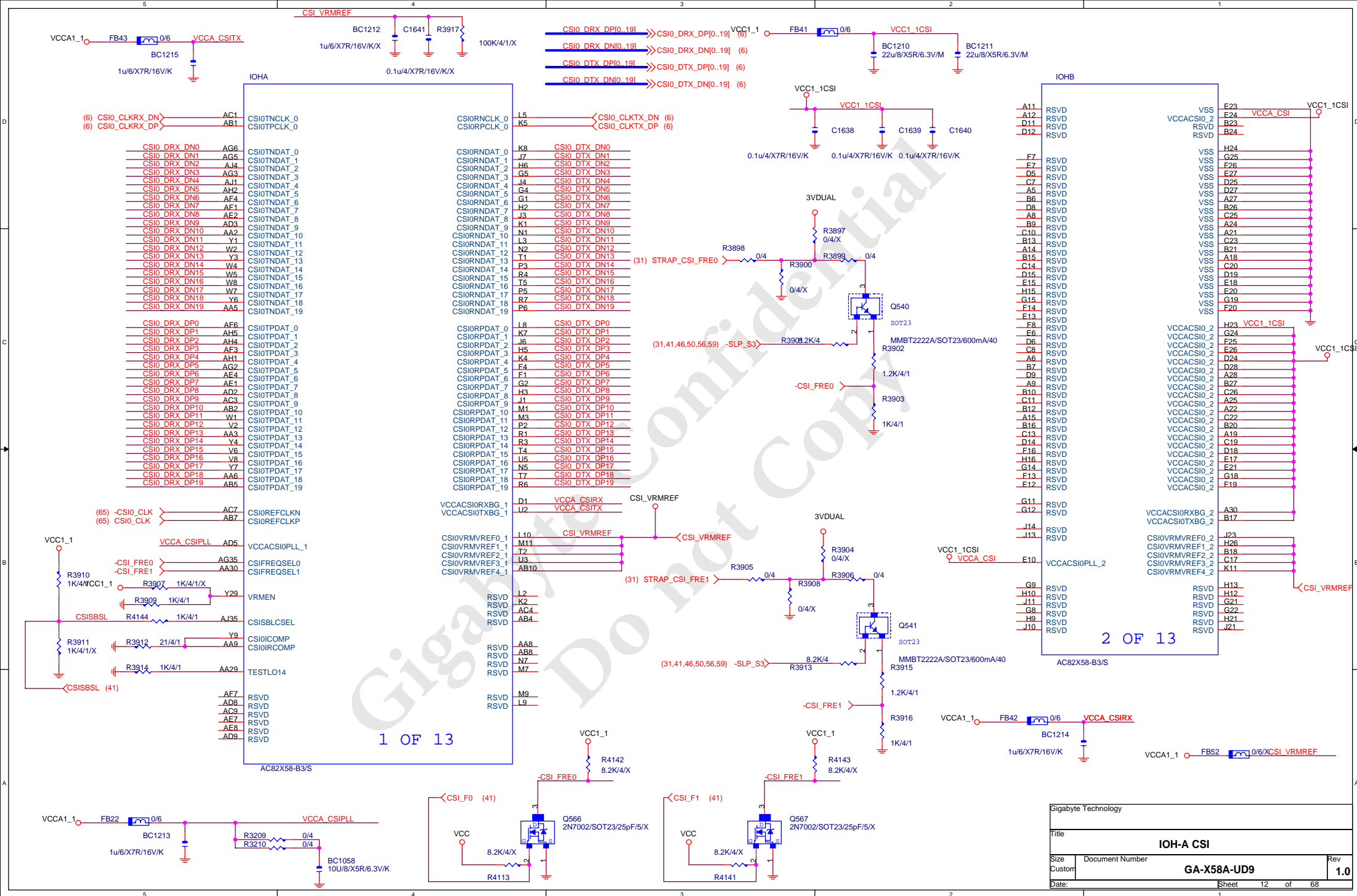
Gigabyte Technology		
Title		
LGA1366-E CPU_PWR		
Size	Document Number	Rev
Custom	GA-X58A-UD9	1.0
Date:	Sheet	8 of 68

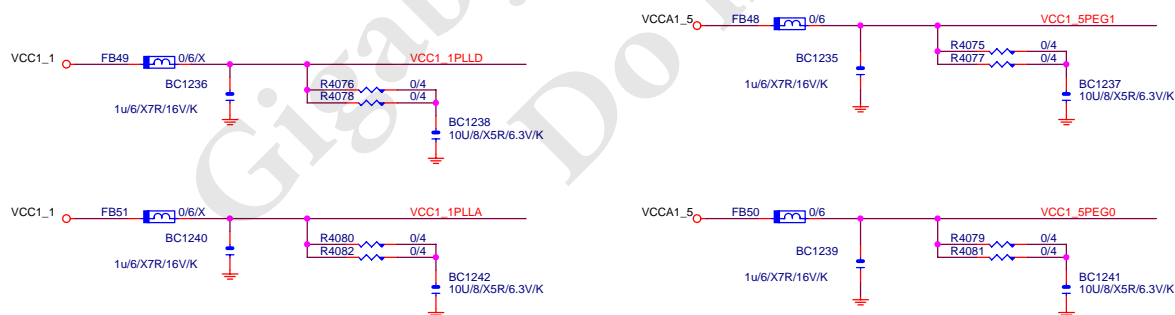
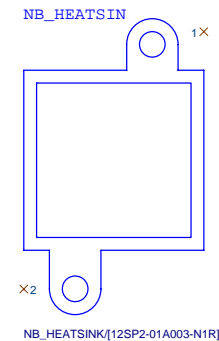
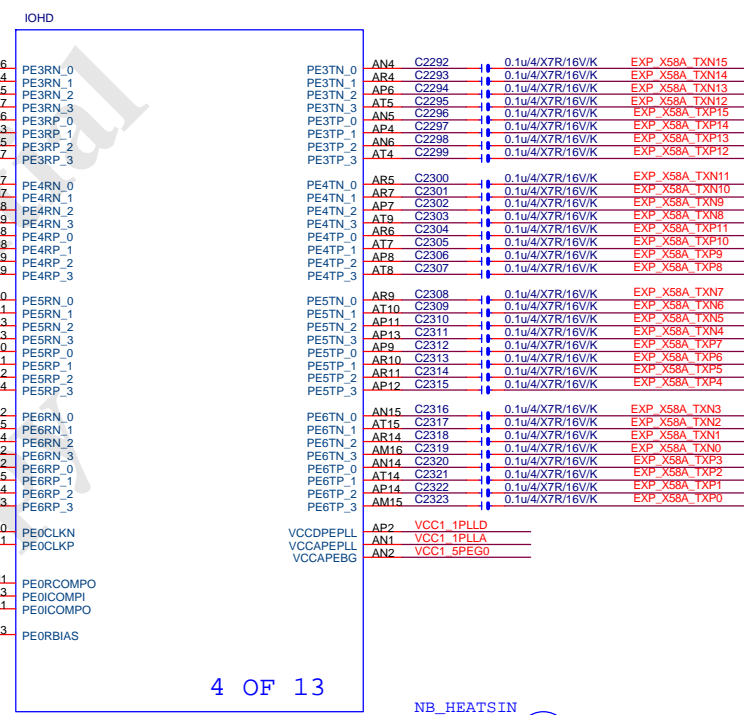
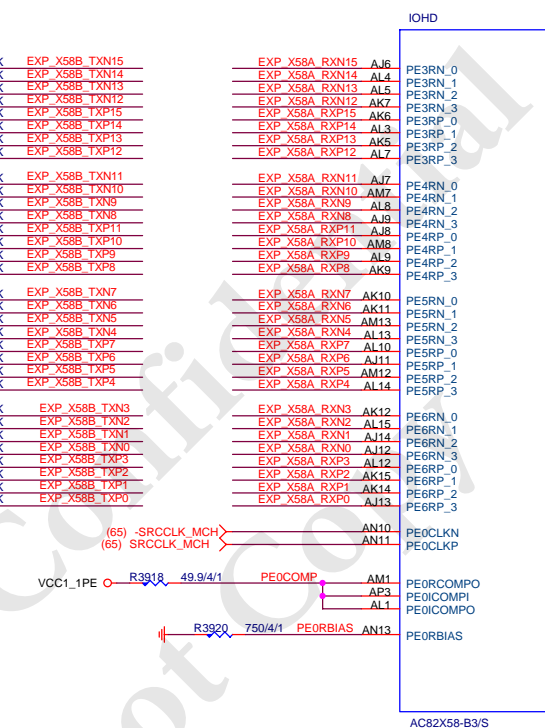
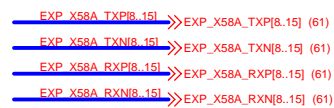


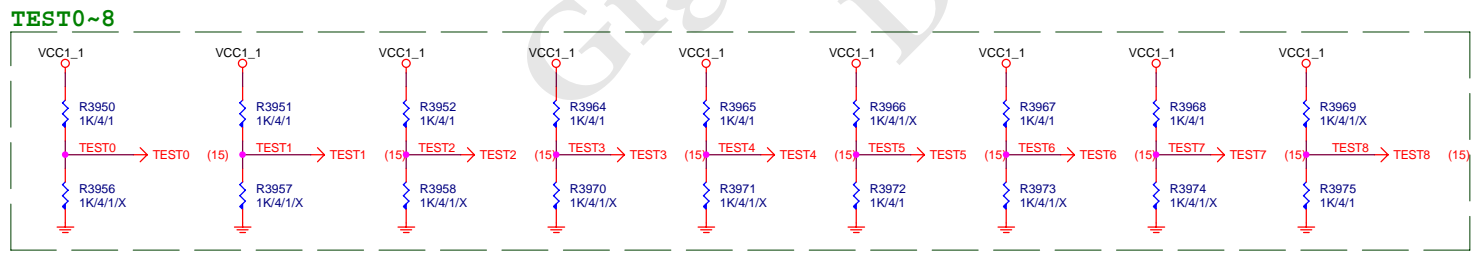
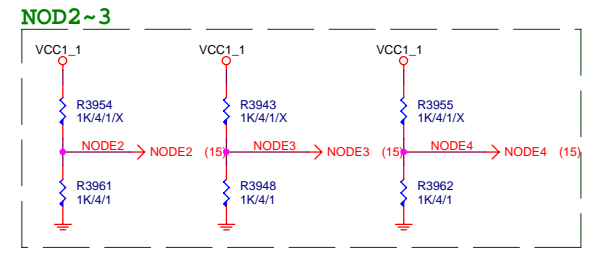
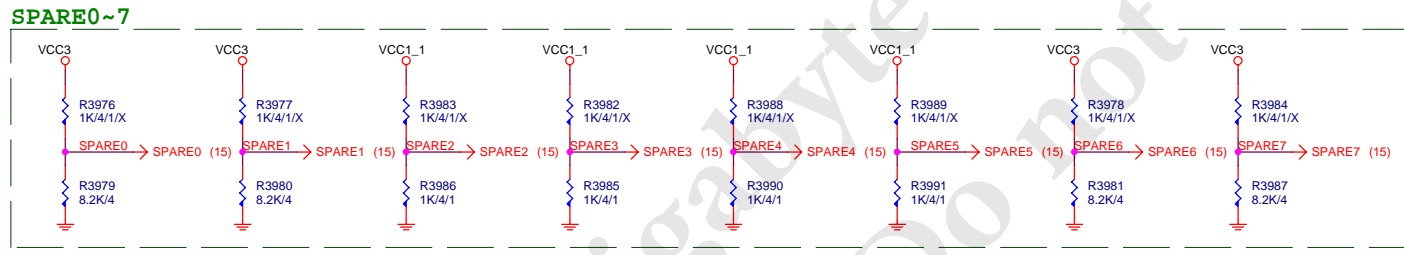
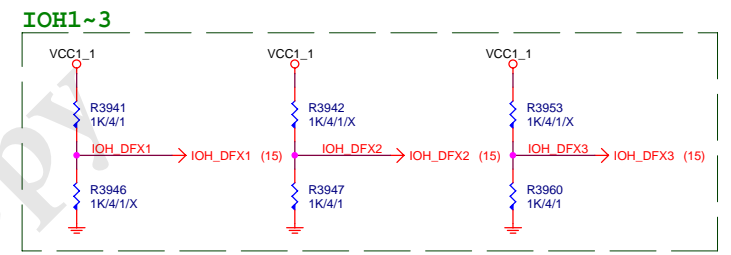
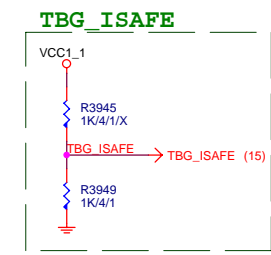
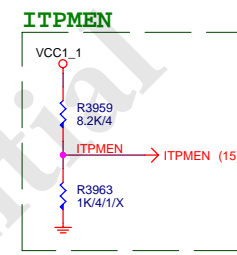
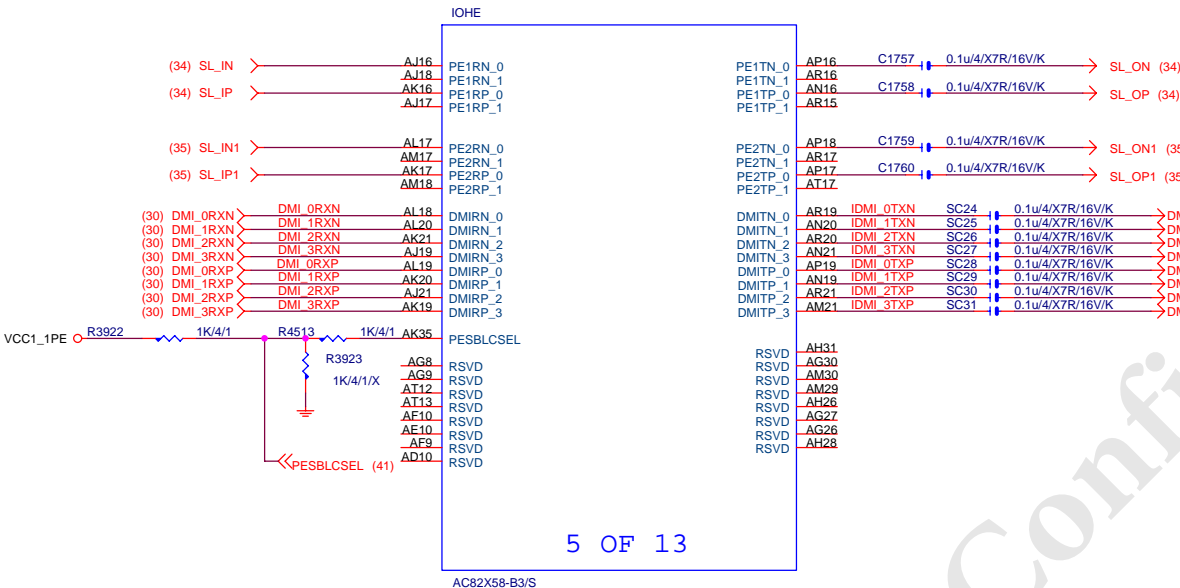






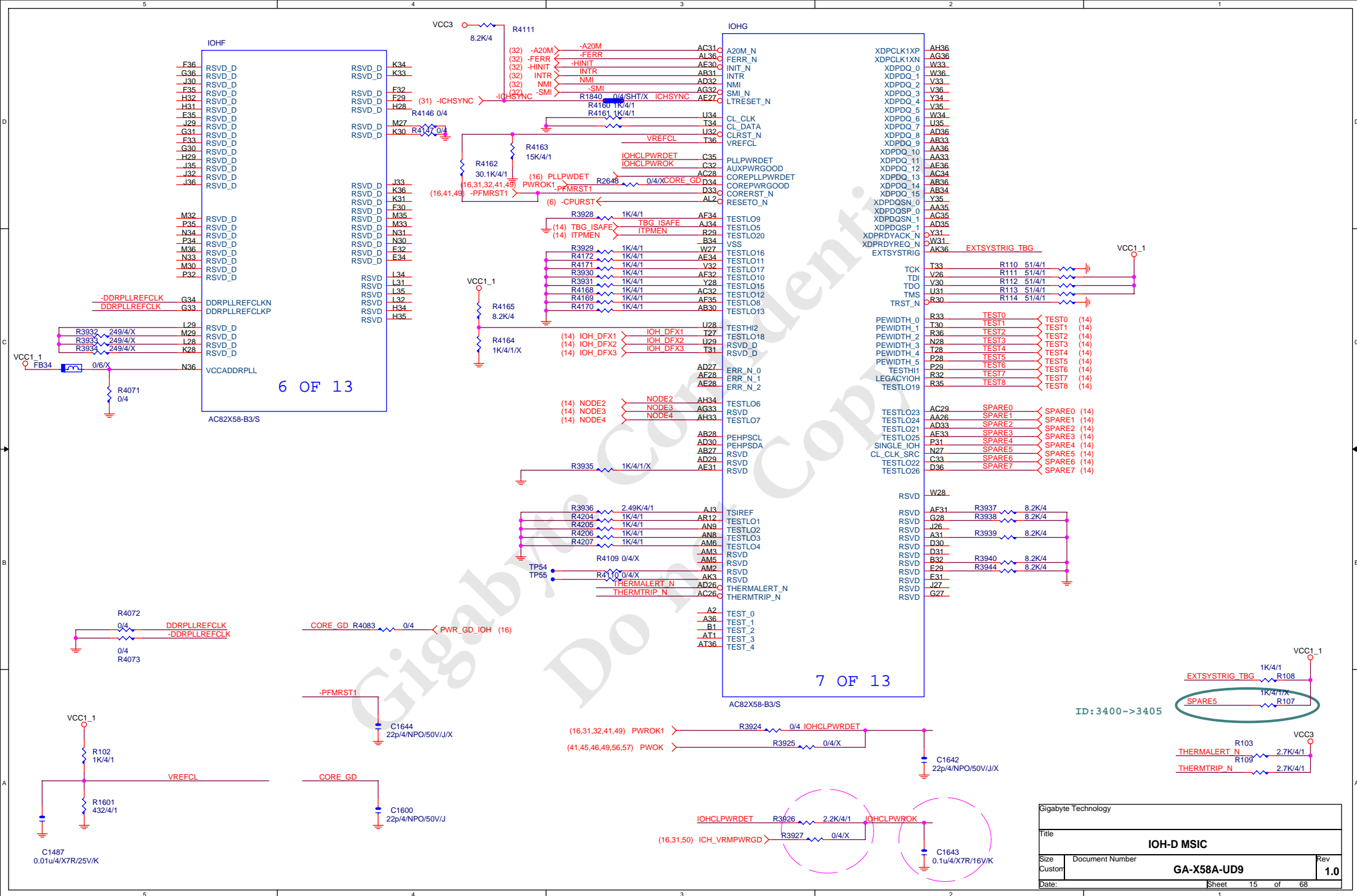




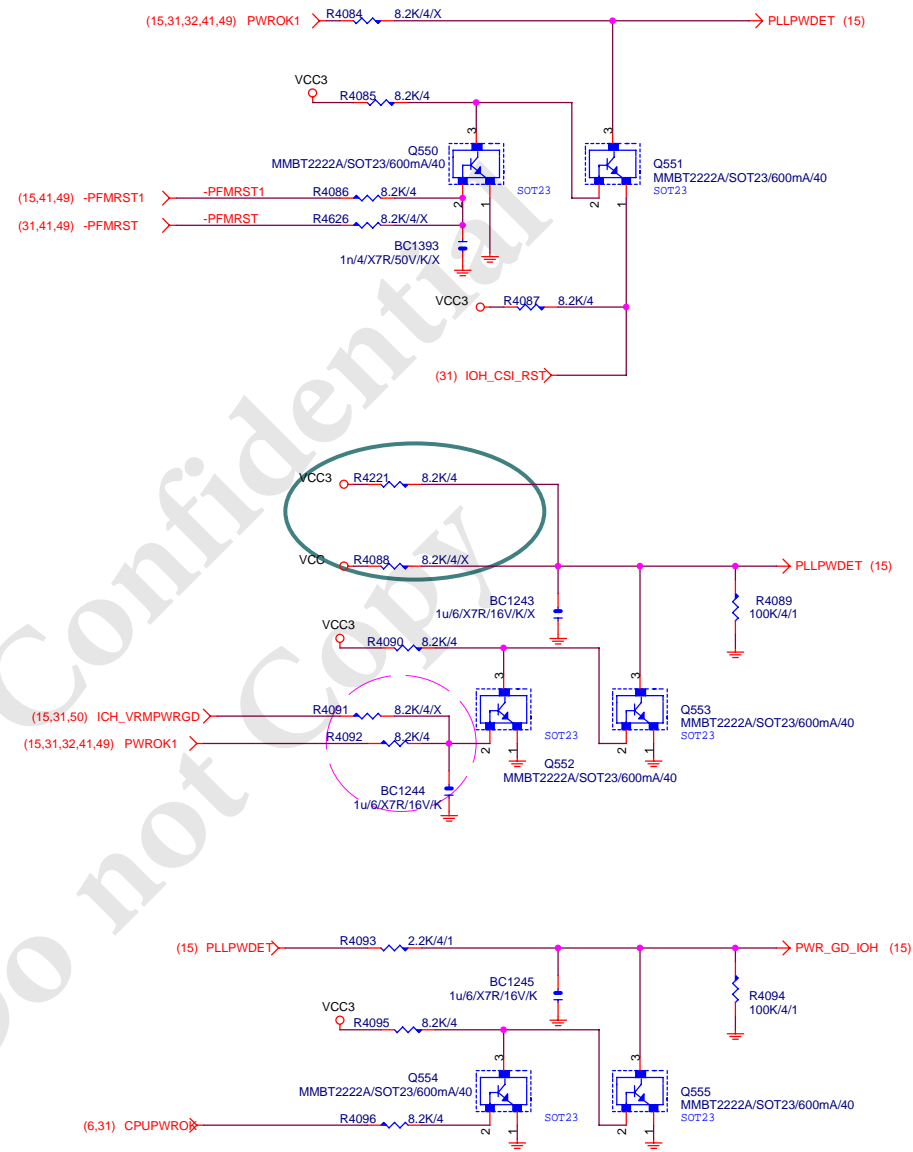


Gigabyte Technology			
Title			
IOH-C PCIEX4			
Size	Document Number		Rev
Custom	GA-X58A-UD9		1.0
Date:	Sheet		14 of 68

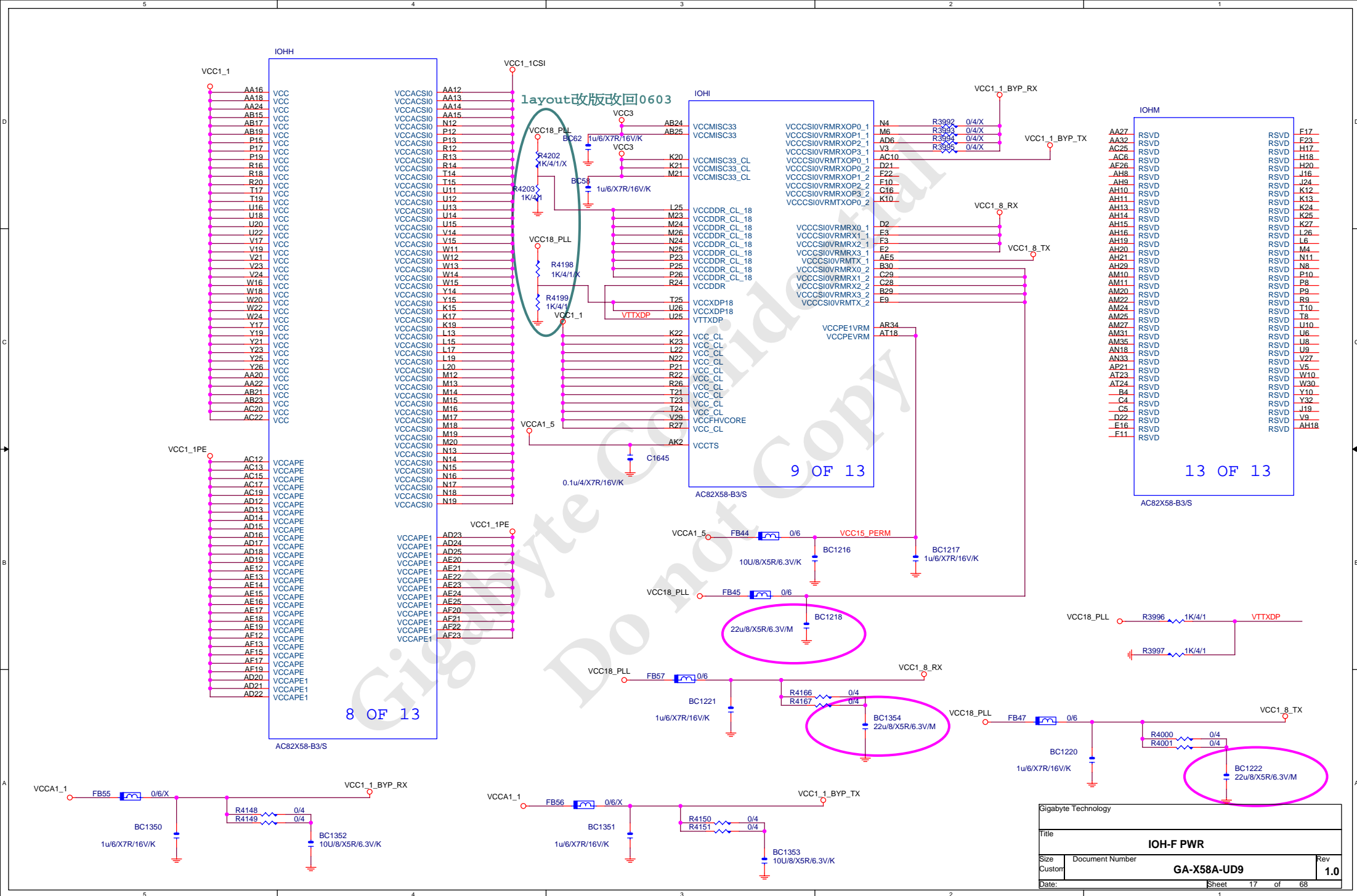






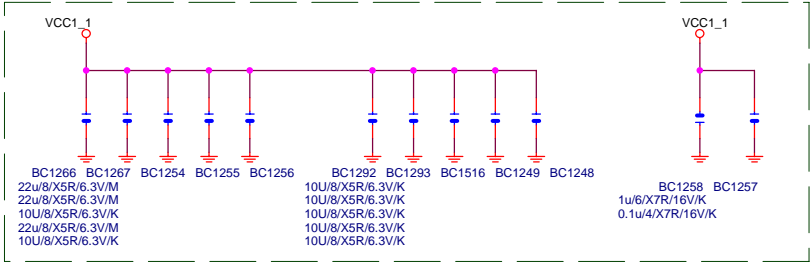


Gigabyte Technology			
Title			
IOH-E_MISC_STRAP			
Size	Document Number		Rev
Custom	GA-X58A-UD9		1.0
Date:	Sheet 16 of 68		

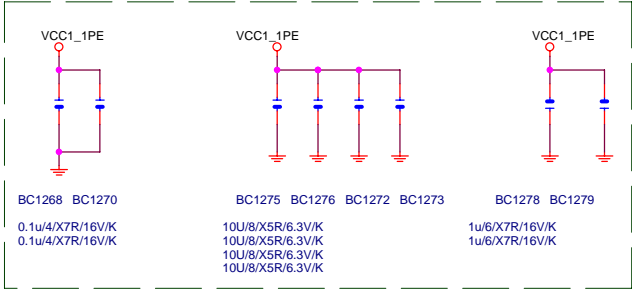


TOP side

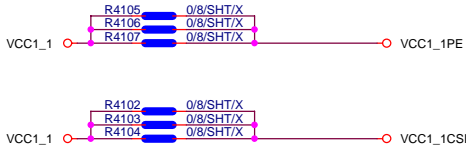
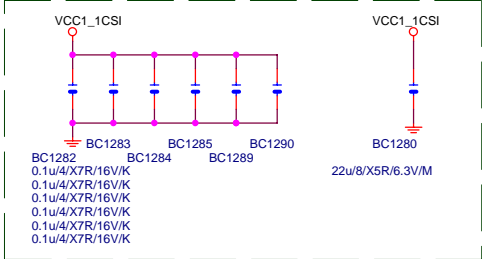
VCC1\_1



VCC1\_1PE

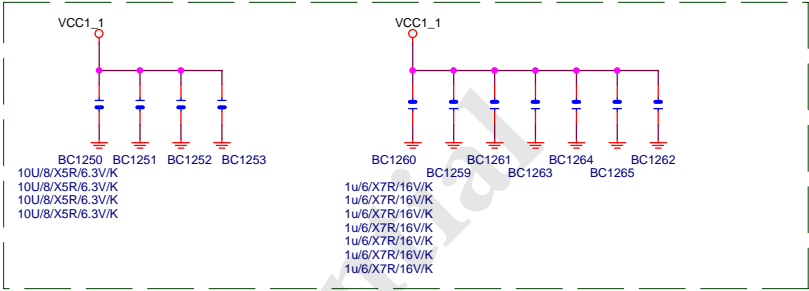


VCC1\_1CSI

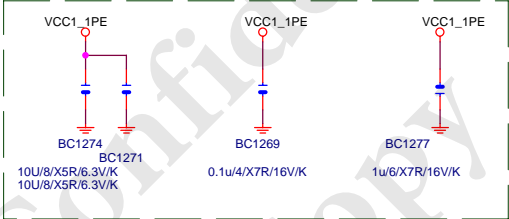


Bottom Side

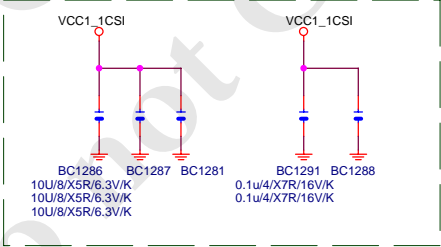
VCC1\_1



VCC1\_1PE



VCC1\_1CSI



Gigabyte Technology			
Title			
IOH-G PWR_1			
Size	Document Number		Rev
Custom	GA-X58A-UD9		1.0
Date:	Sheet 18 of 68		

IOHJ

<del>A16</del>	RSVD_SP	VSS	AM9
<del>A23</del>	RSVD_SP	VSS	AM14
<del>B14</del>	RSVD_SP	VSS	AM23
<del>C12</del>	RSVD_SP	VSS	AM32
<del>C18</del>	RSVD_SP	VSS	AM36
<del>D16</del>	RSVD_SP	VSS	AN17
<del>F12</del>	RSVD_SP	VSS	AN26
<del>G13</del>	RSVD_SP	VSS	AN31
<del>G17</del>	RSVD_SP	VSS	AP1
<del>H14</del>	RSVD_SP	VSS	AP15
		VSS	AP20
		VSS	AP22
		VSS	AP29
		VSS	AP36
A7	VSS	VSS	AR1
A10	VSS	VSS	AR3
A29	VSS	VSS	AR8
A32	VSS	VSS	AR18
A34	VSS	VSS	AR22
AA4	VSS	VSS	AR35
AA10	VSS	VSS	AT2
AA17	VSS	VSS	AT11
AA21	VSS	VSS	AT25
AA25	VSS	VSS	AT30
AA31	VSS	VSS	AT34
AB3	VSS	VSS	B2
AB9	VSS	VSS	B3
AB11	VSS	VSS	B8
AB13	VSS	VSS	B25
AB16	VSS	VSS	B33
AB20	VSS	VSS	B36
AB26	VSS	VSS	C6
AB35	VSS	VSS	C21
AC8	VSS	VSS	C27
AC14	VSS	VSS	C30
AC18	VSS	VSS	C34
AC21	VSS	VSS	D3
AC24	VSS	VSS	D7
AC30	VSS	VSS	D10
AC36	VSS	VSS	D20
AD4	VSS	VSS	D26
AD11	VSS	VSS	D35
AD31	VSS	VSS	E5
AE6	VSS	VSS	E11
AE11	VSS	VSS	E19
AE29	VSS	VSS	E25
AE32	VSS	VSS	E30
AF2	VSS	VSS	E36
AF8	VSS	VSS	F5
AF11	VSS	VSS	F9
AF16	VSS	VSS	F18
AF25	VSS	VSS	F21
AF30	VSS	VSS	F27
AF36	VSS	VSS	F31
AG7	VSS	VSS	G6
AG10	VSS	VSS	G23
AG12	VSS	VSS	G29
AG14	VSS	VSS	G32
AG16	VSS	VSS	H1
AG18	VSS	VSS	H4
AG20	VSS	VSS	H8
AG22	VSS	VSS	H22
AG24	VSS	VSS	H27
AG28	VSS	VSS	H33
AG34	VSS	VSS	
AH6	VSS	VSS	
AH17	VSS	VSS	
AH27	VSS	VSS	
AH32	VSS	VSS	
AJ2	VSS	VSS	
AJ5	VSS	VSS	
AJ10	VSS	VSS	
AJ22	VSS	VSS	
AJ29	VSS	VSS	
AJ33	VSS	VSS	
AK4	VSS	VSS	
AK13	VSS	VSS	
AK22	VSS	VSS	
AK27	VSS	VSS	
AL11	VSS	VSS	
AL21	VSS	VSS	
AL25	VSS	VSS	
AL35	VSS	VSS	

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AC82X58-B3/S

IOHK

<del>J15</del>	RSVD_SP	VSS	R2
<del>J18</del>	RSVD_SP	VSS	R5
		VSS	R8
		VSS	R21
J2	VSS	VSS	R23
J5	VSS	VSS	R25
J8	VSS	VSS	R28
J9	VSS	VSS	R31
J20	VSS	VSS	R34
J22	VSS	VSS	T3
J25	VSS	VSS	T6
J28	VSS	VSS	T9
J31	VSS	VSS	T11
J34	VSS	VSS	T12
K3	VSS	VSS	T13
K6	VSS	VSS	T16
K9	VSS	VSS	T18
K14	VSS	VSS	T20
K16	VSS	VSS	T22
K18	VSS	VSS	T26
K26	VSS	VSS	T29
K29	VSS	VSS	T32
K32	VSS	VSS	T35
K35	VSS	VSS	U1
L1	VSS	VSS	U4
L4	VSS	VSS	U7
L7	VSS	VSS	U17
L11	VSS	VSS	U19
L12	VSS	VSS	U21
L14	VSS	VSS	U23
L16	VSS	VSS	U24
L18	VSS	VSS	U27
L21	VSS	VSS	U30
L23	VSS	VSS	U33
L24	VSS	VSS	U36
L27	VSS	VSS	V1
L30	VSS	VSS	V4
L33	VSS	VSS	V7
L36	VSS	VSS	V10
M2	VSS	VSS	V11
M5	VSS	VSS	V12
M8	VSS	VSS	V13
M10	VSS	VSS	V16
M22	VSS	VSS	V18
M25	VSS	VSS	V20
M28	VSS	VSS	V22
M31	VSS	VSS	V25
M34	VSS	VSS	V28
N3	VSS	VSS	V31
N6	VSS	VSS	V34
N9	VSS	VSS	W3
N10	VSS	VSS	W6
N20	VSS	VSS	W9
N21	VSS	VSS	W17
N23	VSS	VSS	W19
N26	VSS	VSS	W21
N29	VSS	VSS	W23
N32	VSS	VSS	W25
N35	VSS	VSS	W26
P1	VSS	VSS	W29
P4	VSS	VSS	W32
P7	VSS	VSS	W35
P11	VSS	VSS	Y2
P14	VSS	VSS	Y5
P16	VSS	VSS	Y11
P18	VSS	VSS	Y12
P20	VSS	VSS	Y13
P22	VSS	VSS	Y16
P24	VSS	VSS	Y18
P27	VSS	VSS	Y20
P30	VSS	VSS	Y22
P33	VSS	VSS	Y24
P36	VSS	VSS	Y27
R10	VSS	VSS	Y30
R11	VSS	VSS	Y33
R15	VSS	VSS	Y36
R17	VSS	VSS	
R19	VSS	VSS	

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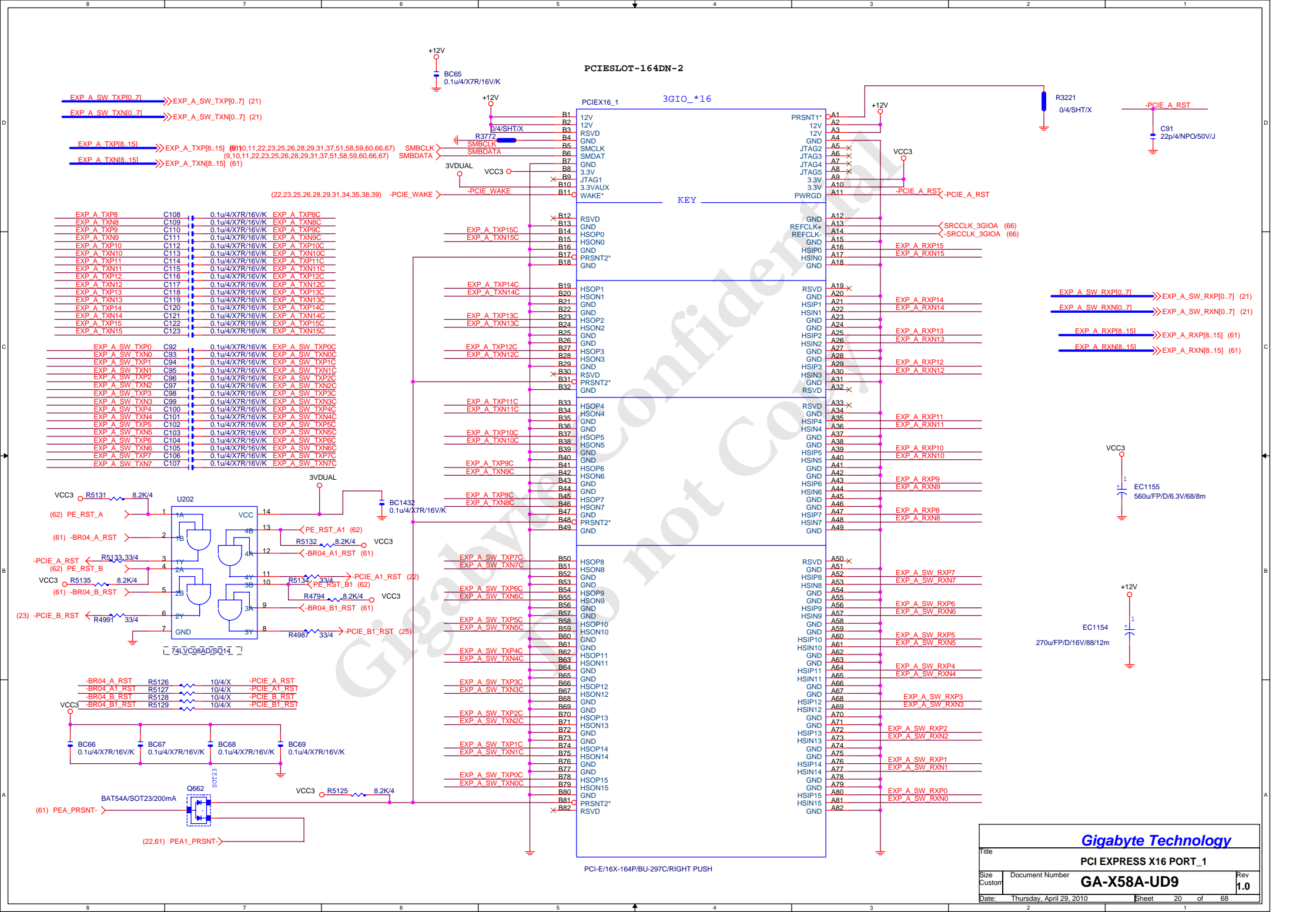
AC82X58-B3/S

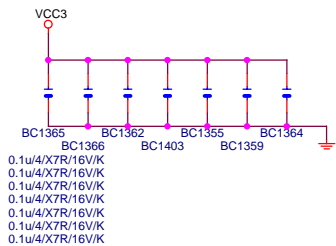
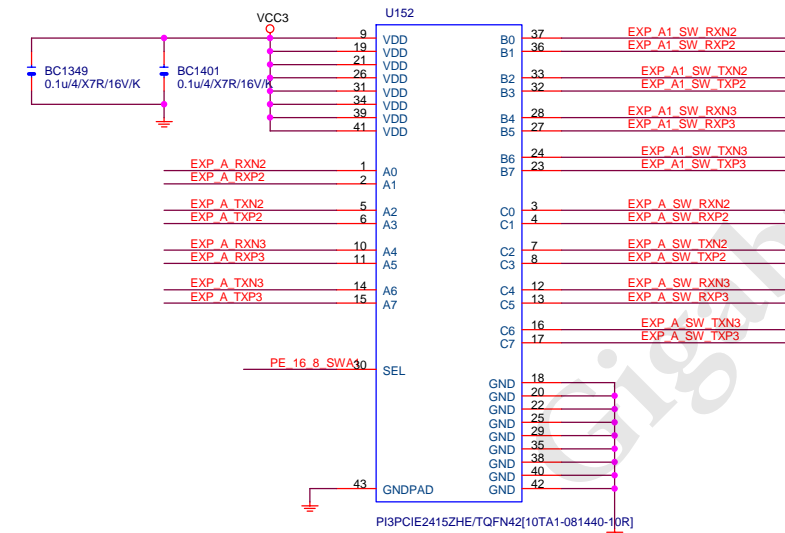
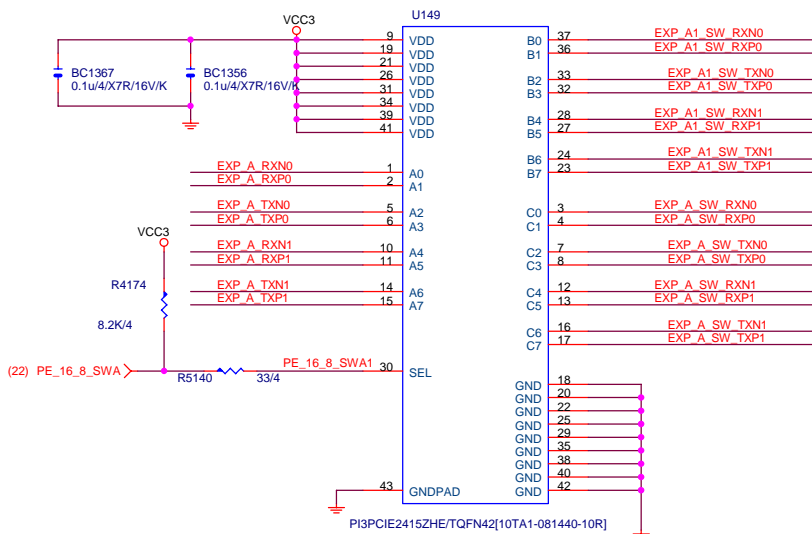
IOHL

<del>A13</del>	RSVD_SP	VSS	AM4
<del>A17</del>	RSVD_SP	VSS	AM19
<del>A20</del>	RSVD_SP	VSS	AM28
<del>C15</del>	RSVD_SP	VSS	AM33
<del>D13</del>	RSVD_SP	VSS	AN3
<del>D17</del>	RSVD_SP	VSS	AN7
<del>E14</del>	RSVD_SP	VSS	AN12
<del>F15</del>	RSVD_SP	VSS	AP5
<del>G16</del>	RSVD_SP	VSS	AP10
<del>J12</del>	RSVD_SP	VSS	AP20
<del>J17</del>	RSVD_SP	VSS	AP24
		VSS	AP34
A3	VSS	VSS	AR2
A4	VSS	VSS	AR13
A33	VSS	VSS	AR27
A35	VSS	VSS	AR32
AA1	VSS	VSS	AR36
AA7	VSS	VSS	AT3
AA11	VSS	VSS	AT6
AA19	VSS	VSS	AT16
AA23	VSS	VSS	AT21
AA28	VSS	VSS	AT33
AA34	VSS	VSS	AT35
AB6	VSS	VSS	B5
AB12	VSS	VSS	B11
AB14	VSS	VSS	B19
AB18	VSS	VSS	B22
AB22	VSS	VSS	B28
AB29	VSS	VSS	B31
AB32	VSS	VSS	B35
AC2	VSS	VSS	C1
AC5	VSS	VSS	C2
AC11	VSS	VSS	C3
AC16	VSS	VSS	C9
AC23	VSS	VSS	C24
AC27	VSS	VSS	C31
AC33	VSS	VSS	C36
AD1	VSS	VSS	D4
AD7	VSS	VSS	D23
AD28	VSS	VSS	D29
AD34	VSS	VSS	D32
AE3	VSS	VSS	E1
AE9	VSS	VSS	E4
AE26	VSS	VSS	E8
AE35	VSS	VSS	E22
AF5	VSS	VSS	E28
AF14	VSS	VSS	E33
AF18	VSS	VSS	F2
AF24	VSS	VSS	F6
AF27	VSS	VSS	F24
AF33	VSS	VSS	F28
AG1	VSS	VSS	F34
AG4	VSS	VSS	G3
AG11	VSS	VSS	G7
AG13	VSS	VSS	G10
AG15	VSS	VSS	G20
AG17	VSS	VSS	G26
AG19	VSS	VSS	G35
AG21	VSS	VSS	H7
AG23	VSS	VSS	H11
AG25	VSS	VSS	H19
AG31	VSS	VSS	H25
AH3	VSS	VSS	H30
AH7	VSS	VSS	H36
AH12	VSS	VSS	Y8
AH22	VSS	VSS	
AH35	VSS	VSS	
AJ15	VSS	VSS	
AJ20	VSS	VSS	
AJ24	VSS	VSS	
AJ31	VSS	VSS	
AJ36	VSS	VSS	
AK1	VSS	VSS	
AK8	VSS	VSS	
AK18	VSS	VSS	
AK26	VSS	VSS	
AK34	VSS	VSS	
AL6	VSS	VSS	
AL16	VSS	VSS	
AL22	VSS	VSS	
AL30	VSS	VSS	

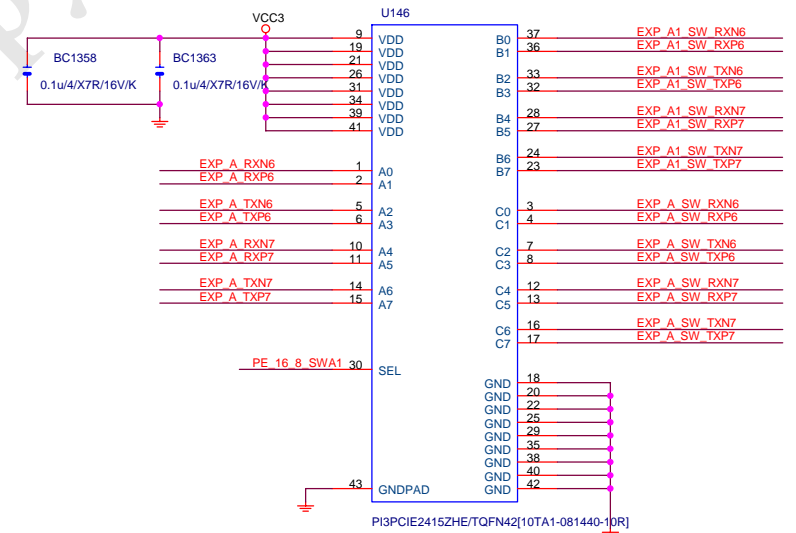
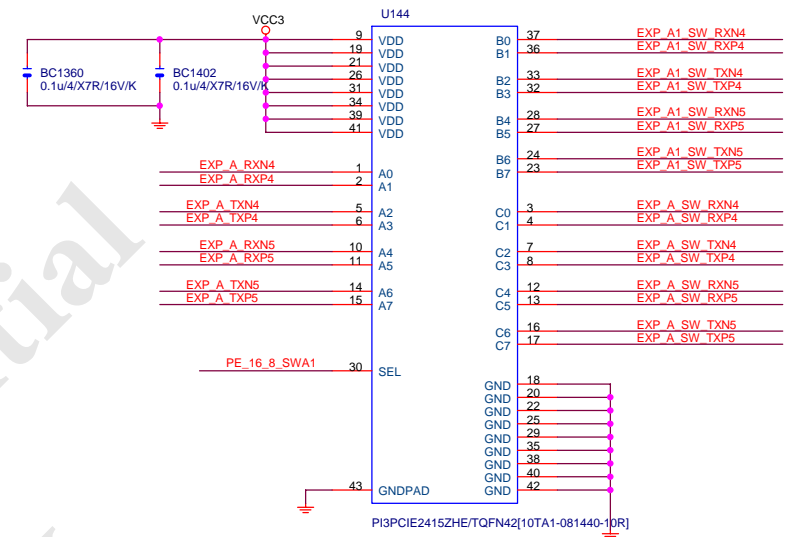
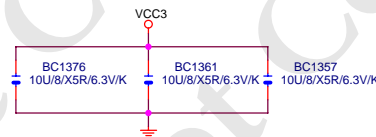
AC82X58-B3/S

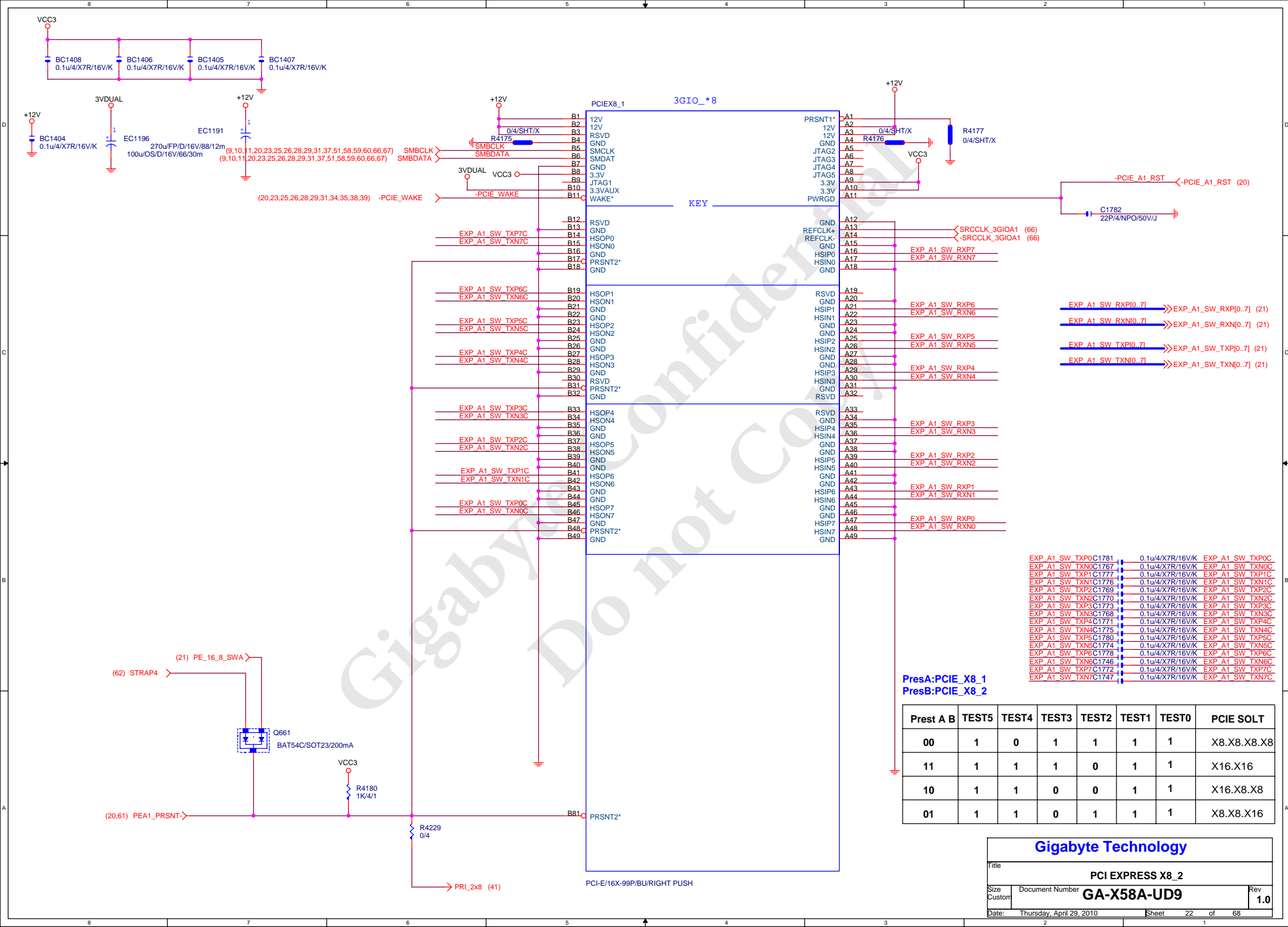
Gigabyte Technology		
Title		
IOH-H GND		
Size	Document Number	Rev
Custom	GA-X58A-UD9	1.0
Date:	Sheet 19 of 68	



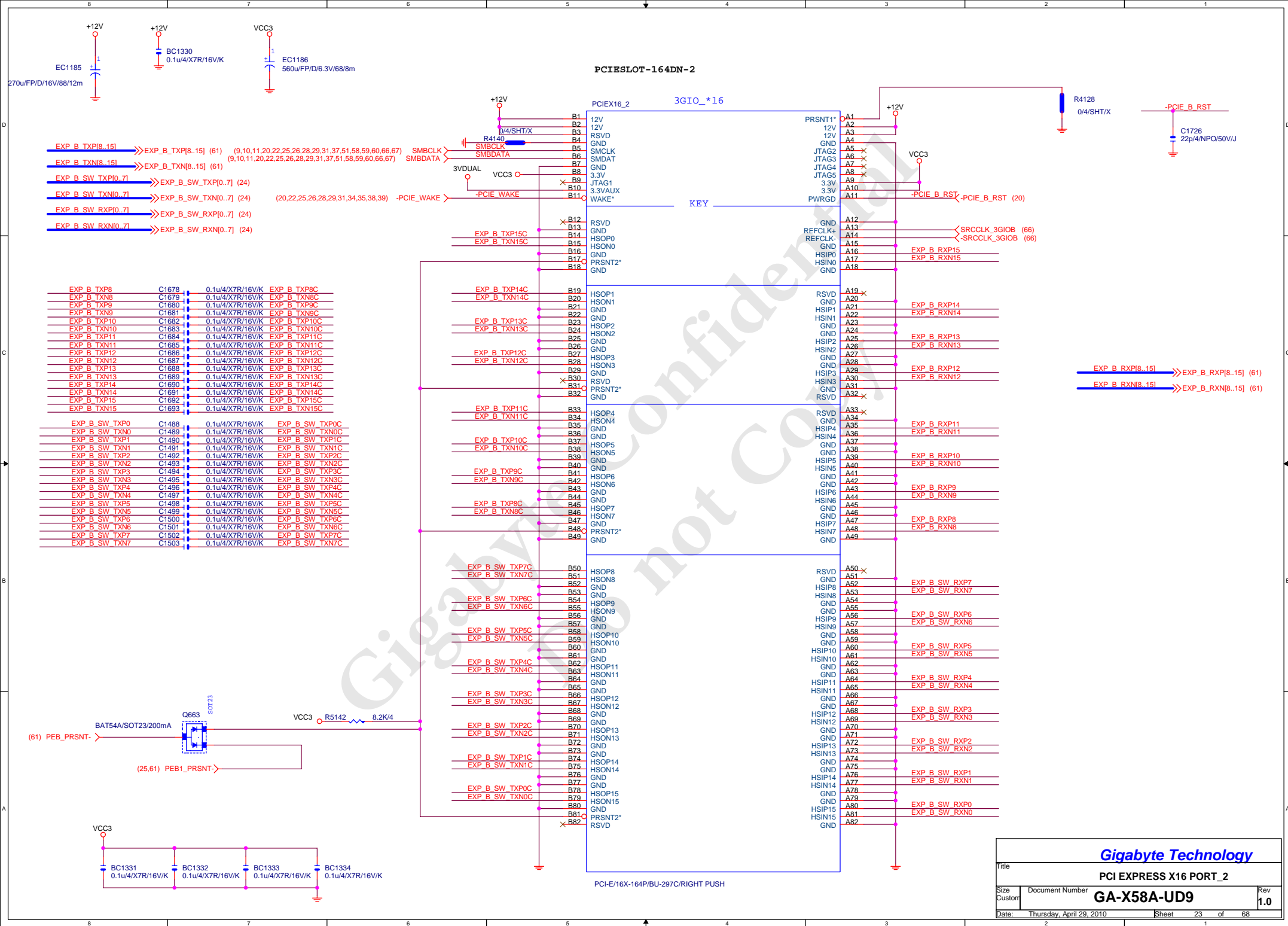


EXP A SW RXPI0..7] >>> EXP\_A\_SW\_RXPI[0..7] (20)  
EXP A SW RXNI0..7] >>> EXP\_A\_SW\_RXNI[0..7] (20)  
EXP A SW TXPI0..7] >>> EXP\_A\_SW\_TXPI[0..7] (20)  
EXP A SW TXNI0..7] >>> EXP\_A\_SW\_TXNI[0..7] (20)  
EXP A SW RXN1 >>> EXP\_A\_SW\_RXN1 (22)  
EXP A SW RXN0..7] >>> EXP\_A\_SW\_RXN[0..7] (22)  
EXP A SW TXN1 >>> EXP\_A\_SW\_TXN1 (22)  
EXP A SW TXN0..7] >>> EXP\_A\_SW\_TXN[0..7] (22)  
EXP A TXPI0..7] >>> EXP\_A\_TXPI[0..7] (61)  
EXP A TXNI0..7] >>> EXP\_A\_TXNI[0..7] (61)  
EXP A RXPI0..7] >>> EXP\_A\_RXPI[0..7] (61)  
EXP A RXNI0..7] >>> EXP\_A\_RXNI[0..7] (61)

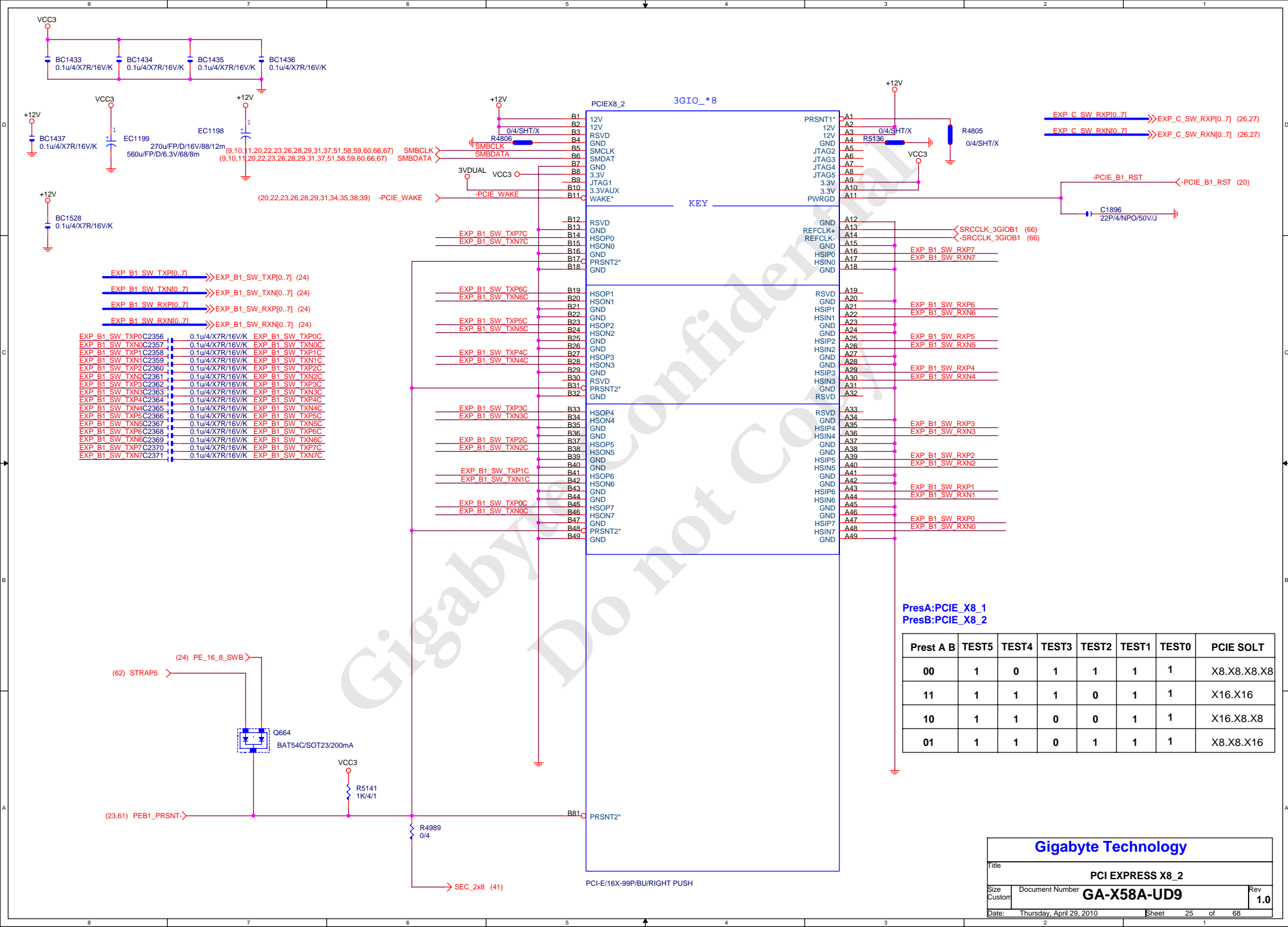












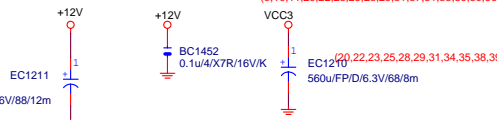
PresA:PCIE\_X8\_1  
PresB:PCIE\_X8\_2

Prest A B	TEST5	TEST4	TEST3	TEST2	TEST1	TEST0	PCIE SOLT
00	1	0	1	1	1	1	X8.X8.X8.X8
11	1	1	1	0	1	1	X16.X16
10	1	1	0	0	1	1	X16.X8.X8
01	1	1	0	1	1	1	X8.X8.X16

Gigabyte Technology

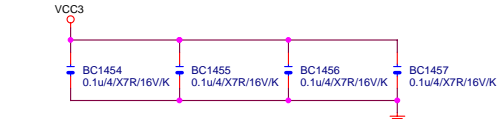
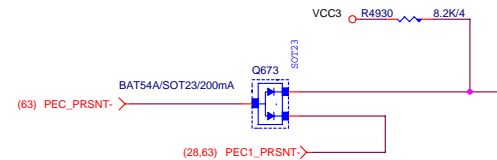
Title				PCI EXPRESS X8_2			
Size	Custom	Document Number			GA-X58A-UD9		Rev
							1.0
Date:		Thursday, April 29, 2010			Sheet	25	of 68

EXP\_C\_SW\_RXP[0..7] >> EXP\_C\_SW\_RXP[0..7] (27)  
EXP\_C\_SW\_RXN[0..7] >> EXP\_C\_SW\_RXN[0..7] (27)  
EXP\_C\_TXP[8..15] >> EXP\_C\_TXP[8..15] (63)  
EXP\_C\_TXN[8..15] >> EXP\_C\_TXN[8..15] (63)

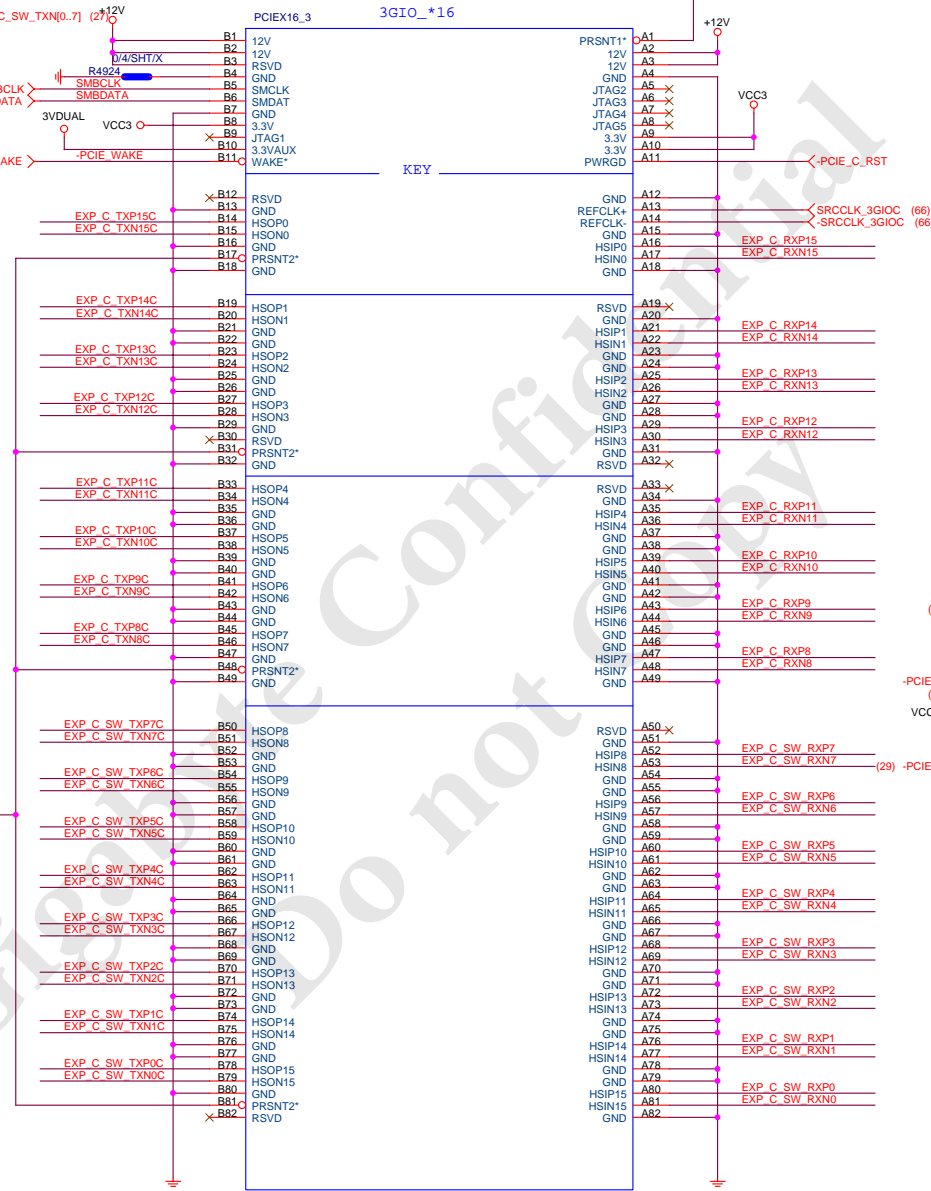


EXP_C_TXP8	C2062	0.1u/4/X7R/16V/K	EXP_C_TXP8C
EXP_C_TXN8	C2063	0.1u/4/X7R/16V/K	EXP_C_TXN8C
EXP_C_TXP9	C2064	0.1u/4/X7R/16V/K	EXP_C_TXP9C
EXP_C_TXN9	C2065	0.1u/4/X7R/16V/K	EXP_C_TXN9C
EXP_C_TXP10	C2066	0.1u/4/X7R/16V/K	EXP_C_TXP10C
EXP_C_TXN10	C2067	0.1u/4/X7R/16V/K	EXP_C_TXN10C
EXP_C_TXP11	C2068	0.1u/4/X7R/16V/K	EXP_C_TXP11C
EXP_C_TXN11	C2069	0.1u/4/X7R/16V/K	EXP_C_TXN11C
EXP_C_TXP12	C2070	0.1u/4/X7R/16V/K	EXP_C_TXP12C
EXP_C_TXN12	C2071	0.1u/4/X7R/16V/K	EXP_C_TXN12C
EXP_C_TXP13	C2072	0.1u/4/X7R/16V/K	EXP_C_TXP13C
EXP_C_TXN13	C2073	0.1u/4/X7R/16V/K	EXP_C_TXN13C
EXP_C_TXP14	C2074	0.1u/4/X7R/16V/K	EXP_C_TXP14C
EXP_C_TXN14	C2075	0.1u/4/X7R/16V/K	EXP_C_TXN14C
EXP_C_TXP15	C2076	0.1u/4/X7R/16V/K	EXP_C_TXP15C
EXP_C_TXN15	C2077	0.1u/4/X7R/16V/K	EXP_C_TXN15C

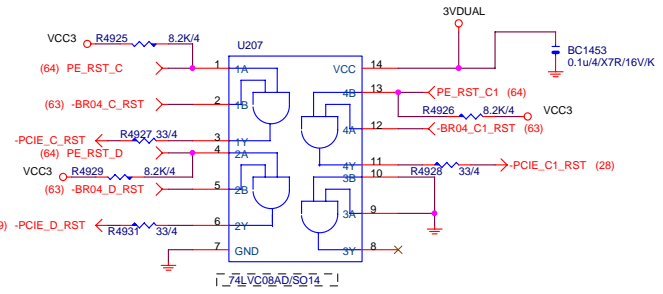
EXP_C_SW_TXP0	C2078	0.1u/4/X7R/16V/K	EXP_C_SW_TXP0C
EXP_C_SW_TXN0	C2079	0.1u/4/X7R/16V/K	EXP_C_SW_TXN0C
EXP_C_SW_TXP1	C2080	0.1u/4/X7R/16V/K	EXP_C_SW_TXP1C
EXP_C_SW_TXN1	C2081	0.1u/4/X7R/16V/K	EXP_C_SW_TXN1C
EXP_C_SW_TXP2	C2082	0.1u/4/X7R/16V/K	EXP_C_SW_TXP2C
EXP_C_SW_TXN2	C2083	0.1u/4/X7R/16V/K	EXP_C_SW_TXN2C
EXP_C_SW_TXP3	C2084	0.1u/4/X7R/16V/K	EXP_C_SW_TXP3C
EXP_C_SW_TXN3	C2085	0.1u/4/X7R/16V/K	EXP_C_SW_TXN3C
EXP_C_SW_TXP4	C2086	0.1u/4/X7R/16V/K	EXP_C_SW_TXP4C
EXP_C_SW_TXN4	C2087	0.1u/4/X7R/16V/K	EXP_C_SW_TXN4C
EXP_C_SW_TXP5	C2088	0.1u/4/X7R/16V/K	EXP_C_SW_TXP5C
EXP_C_SW_TXN5	C2089	0.1u/4/X7R/16V/K	EXP_C_SW_TXN5C
EXP_C_SW_TXP6	C2090	0.1u/4/X7R/16V/K	EXP_C_SW_TXP6C
EXP_C_SW_TXN6	C2091	0.1u/4/X7R/16V/K	EXP_C_SW_TXN6C
EXP_C_SW_TXP7	C2092	0.1u/4/X7R/16V/K	EXP_C_SW_TXP7C
EXP_C_SW_TXN7	C2093	0.1u/4/X7R/16V/K	EXP_C_SW_TXN7C



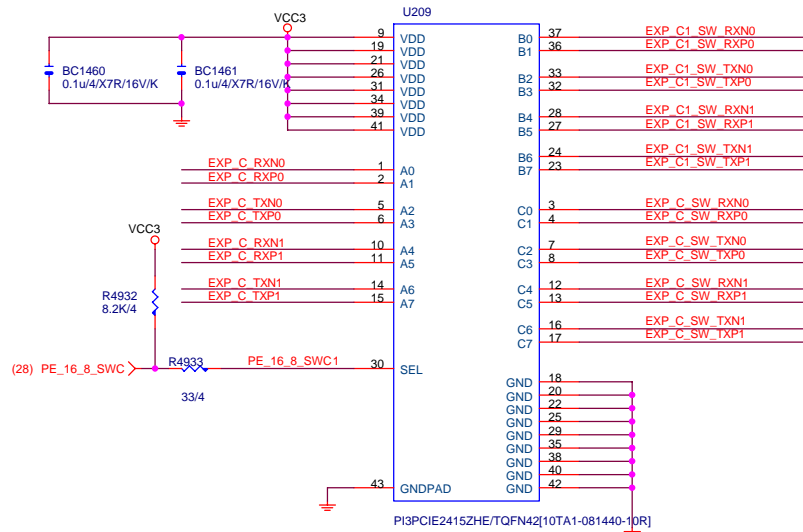
# PCIESLOT-164DN-2



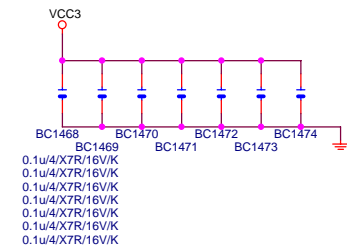
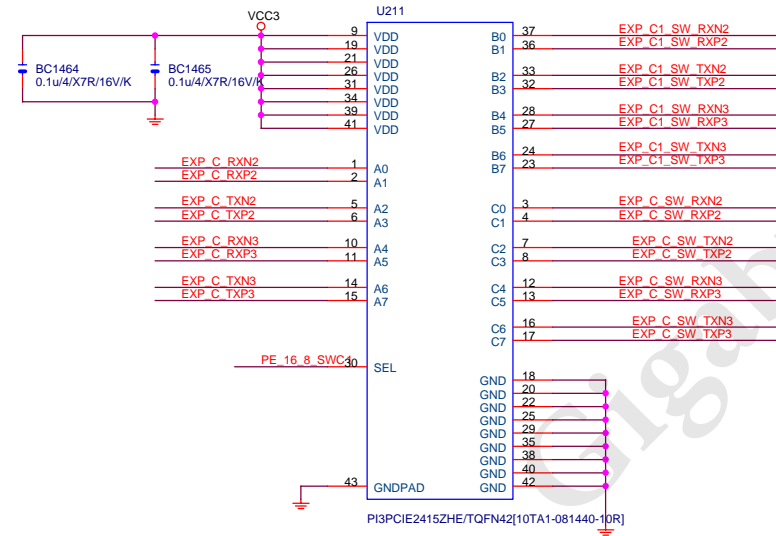
EXP\_C\_RXP[0..7] >> EXP\_C\_RXP[0..7] (27,63)  
EXP\_C\_RXN[0..7] >> EXP\_C\_RXN[0..7] (27,63)  
EXP\_C\_RXP[8..15] >> EXP\_C\_RXP[8..15] (63)  
EXP\_C\_RXN[8..15] >> EXP\_C\_RXN[8..15] (63)



-BR04\_C\_RST R4984 10/4/X -PCIE\_C\_RST  
-BR04\_CT\_RST R4985 10/4/X -PCIE\_CT\_RST  
-BR04\_D\_RST R4986 10/4/X -PCIE\_D\_RST



Function	SEL
A--> B	L
A--> C	H

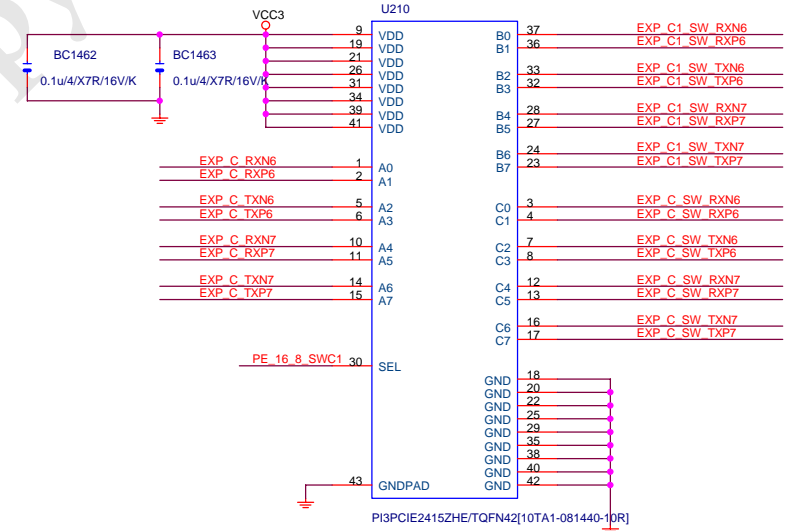
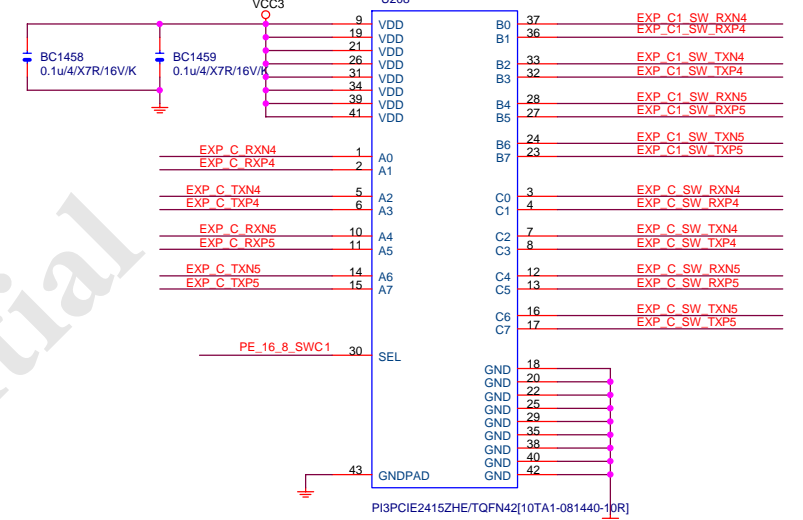
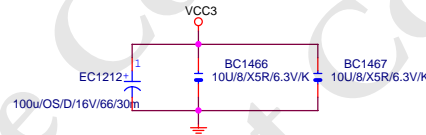


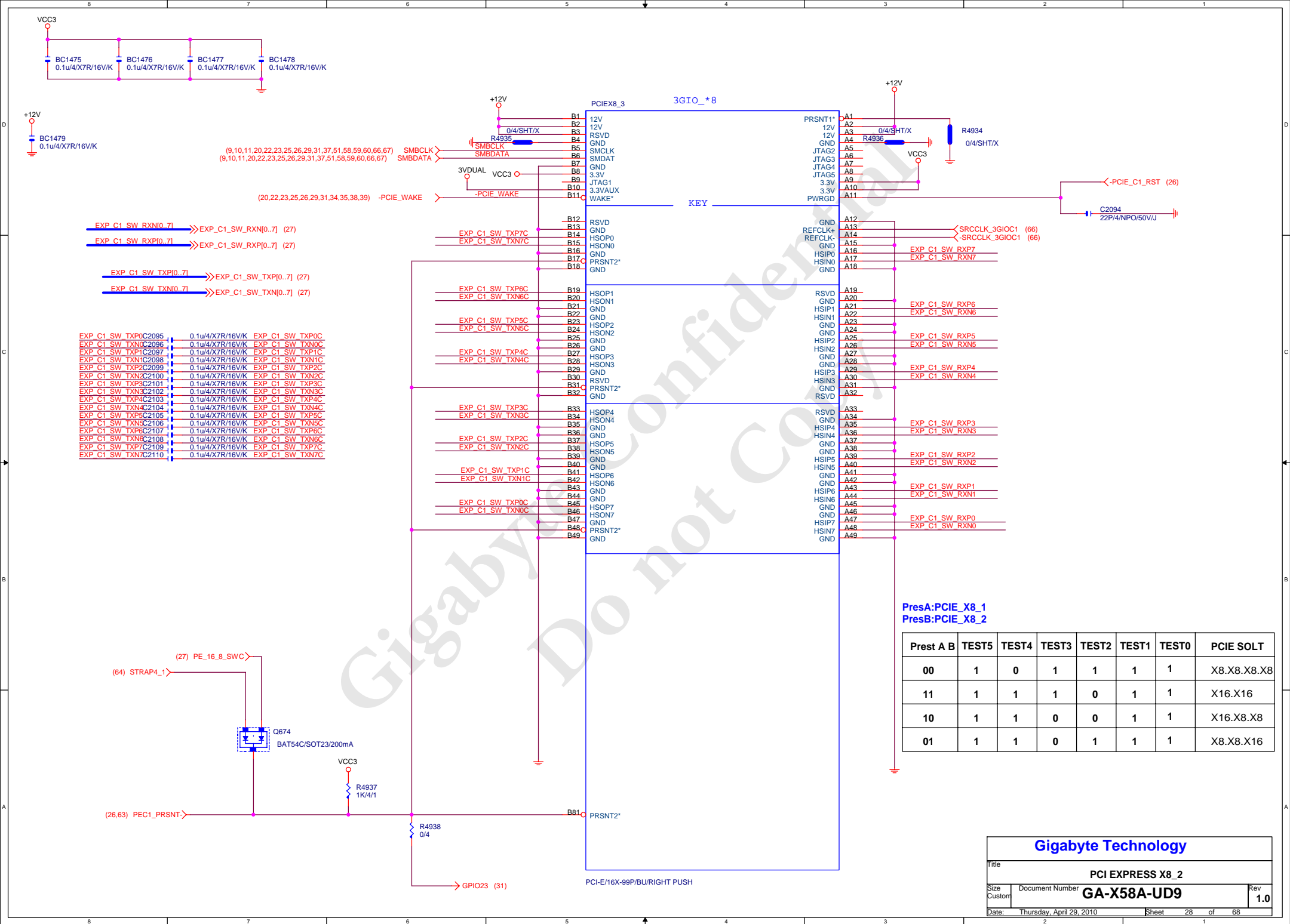
EXP C1 SW RXN0.7I >>> EXP\_C1\_SW\_RXN[0..7] (28)  
 EXP C1 SW RXN0.7I >>> EXP\_C1\_SW\_RXN[0..7] (28)  
 EXP C1 SW TXN0.7I >>> EXP\_C1\_SW\_TXN[0..7] (28)  
 EXP C1 SW TXN0.7I >>> EXP\_C1\_SW\_TXN[0..7] (28)

EXP C SW RXN0.7I >>> EXP\_C\_SW\_RXN[0..7] (26)  
 EXP C SW RXN0.7I >>> EXP\_C\_SW\_RXN[0..7] (26)  
 EXP C SW TXN0.7I >>> EXP\_C\_SW\_TXN[0..7] (26)  
 EXP C SW TXN0.7I >>> EXP\_C\_SW\_TXN[0..7] (26)

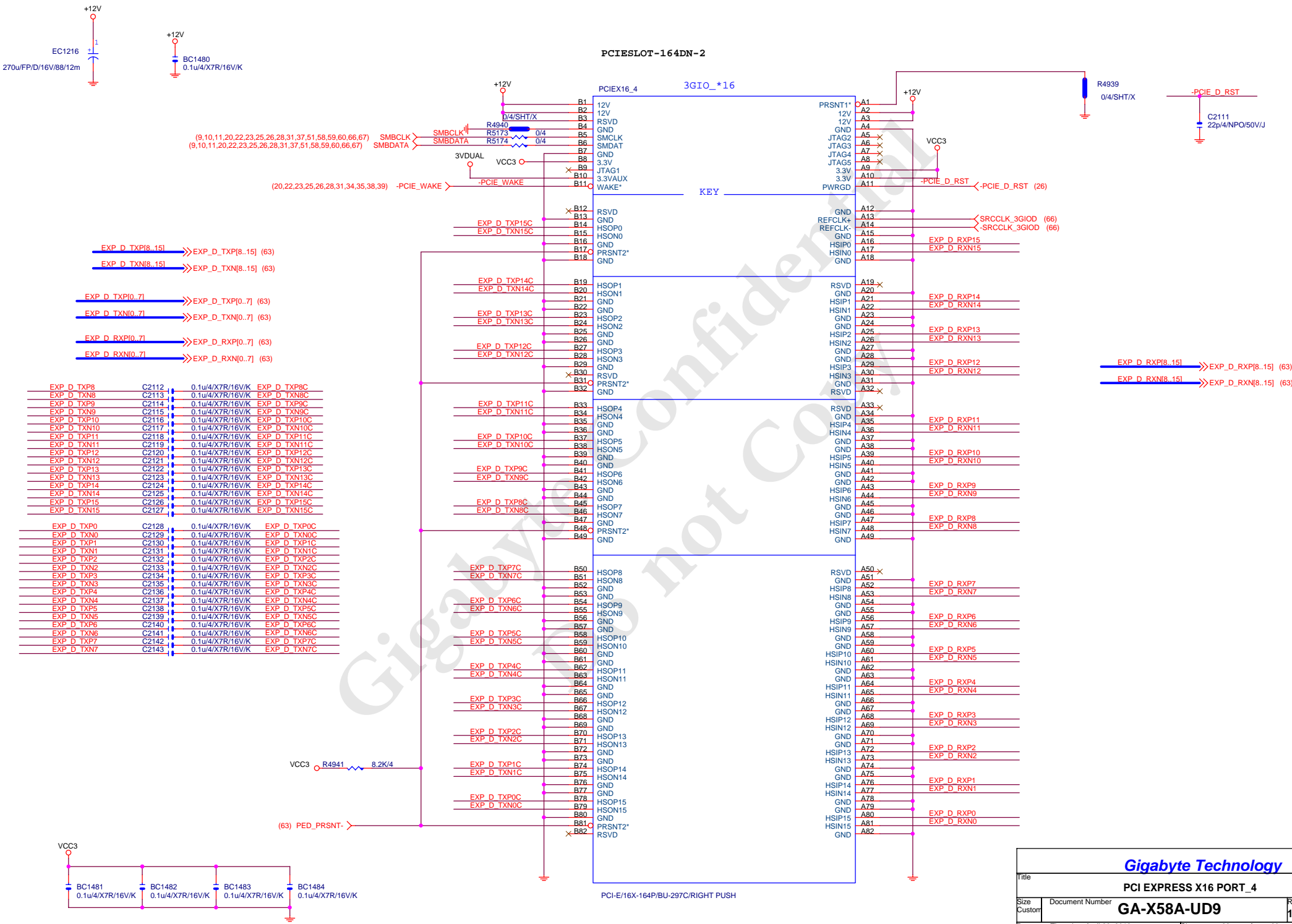
EXP C RXN0.7I >>> EXP\_C\_RXN[0..7] (63)  
 EXP C RXN0.7I >>> EXP\_C\_RXN[0..7] (63)

EXP C TXN0.7I >>> EXP\_C\_TXN[0..7] (63)  
 EXP C TXN0.7I >>> EXP\_C\_TXN[0..7] (63)











# ICH GPIO Table

PIN NAME	USAGE	NOTE
GP9_WOL_EN(GPIO9)	8268_P18	
GP20(GPIO20)	8268_P18	
GP0	-PECI_REQ	
GP8	STRAP_CSI_FRE1	
GP12	STRAP_CSI_FRE0	
GP27_QRT_STATE0	3VDUAL_ICH	原ISOLATEB_1
GP26_S4_STATEB	3VDUAL_ICH	原ISOLATEB_2
CLGPIO5_GP57	F_LED1_C	
GP1_TACH1	F_LED2_C	
GP22_SCLOCK	F_LED3_C	
GP28_SLOAD	F_LED4_C	
GP21_SATA0GP	F_LED5_C	
GP6_TACH2	NBT_LED2_C	
GP39_SDATAOUT0	-CPU_PSI_DIS	
GP34(GPIO34)	-SPI_WP0	
GP48_SDATAOUT1	-EN_PWM	
GP19_SATA1GP	-ACZ_DET	
GP25	-CPU_STOP	
GP36_SATA2GP	GPIO36(FS)	
GP37_SATA3GP	SATA3GP	
SMBALERTB_GP11	-SMBALRT	
GP10_ALERTB	ICH_GP10(-CATERR)	原-LAN1_DSM
GP13	-LPCPME	

ICH9

PCI

1 OF 6

ICH10R[10HB1-038280-F0R]

ICH9

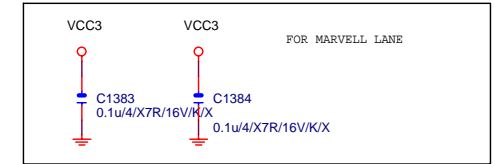
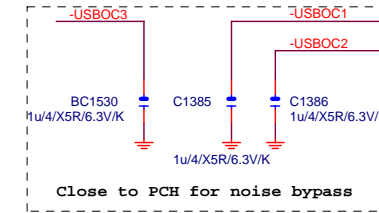
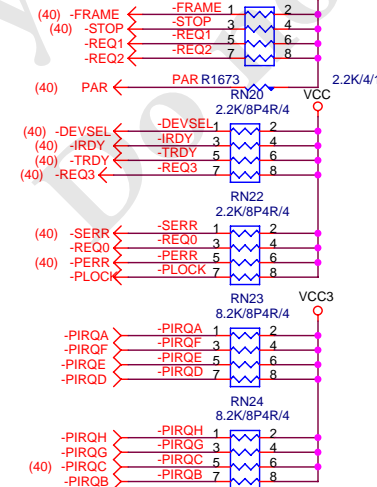
DMI

USB

PCI-E

2 OF 6

(40) A\_D[0..31] <-> A\_D[0..31] (40)



**Gigabyte Technology**

Title: ICH10 DMI, PCI, USB

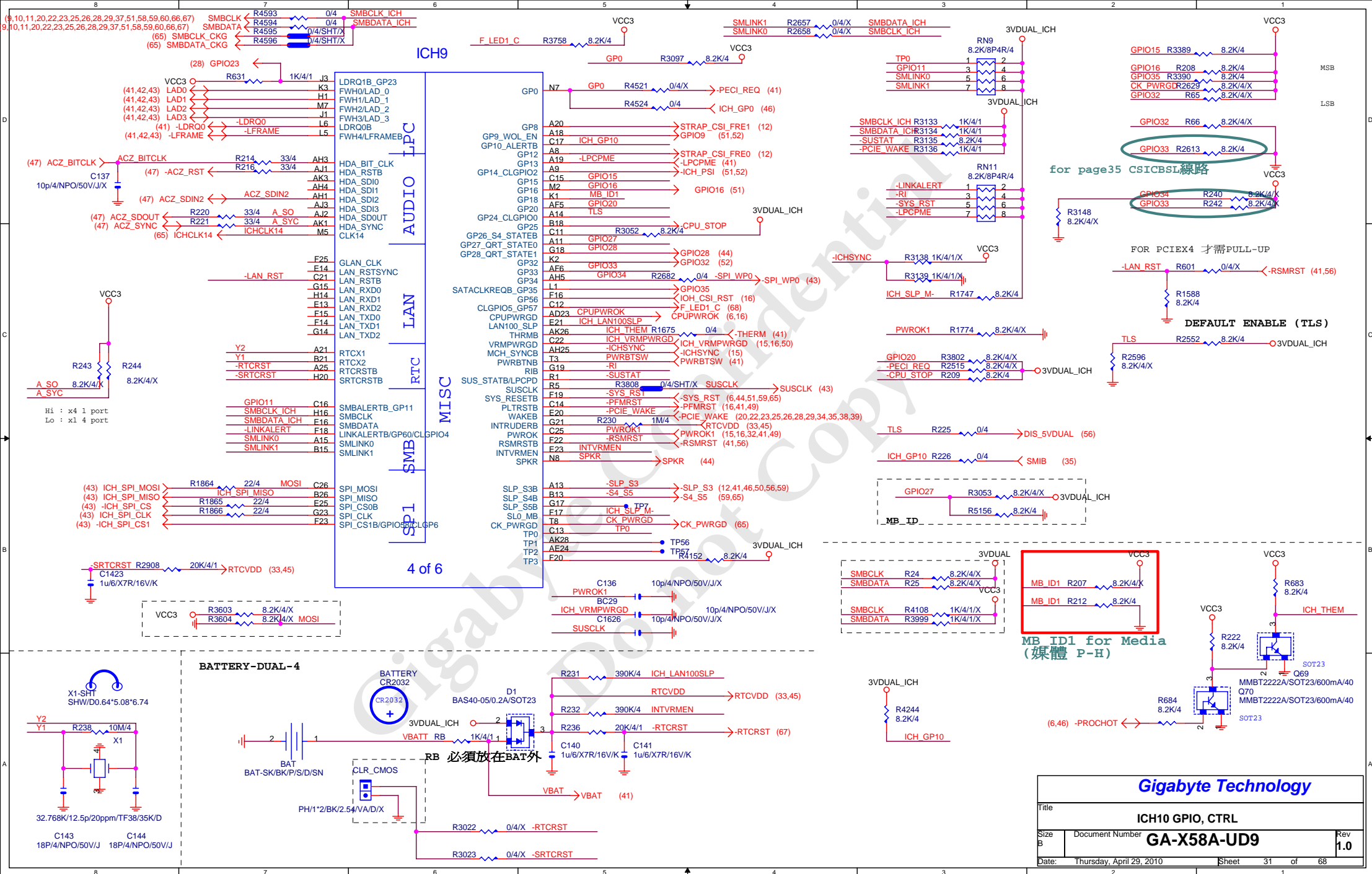
Size: B

Document Number: GA-X58A-UD9

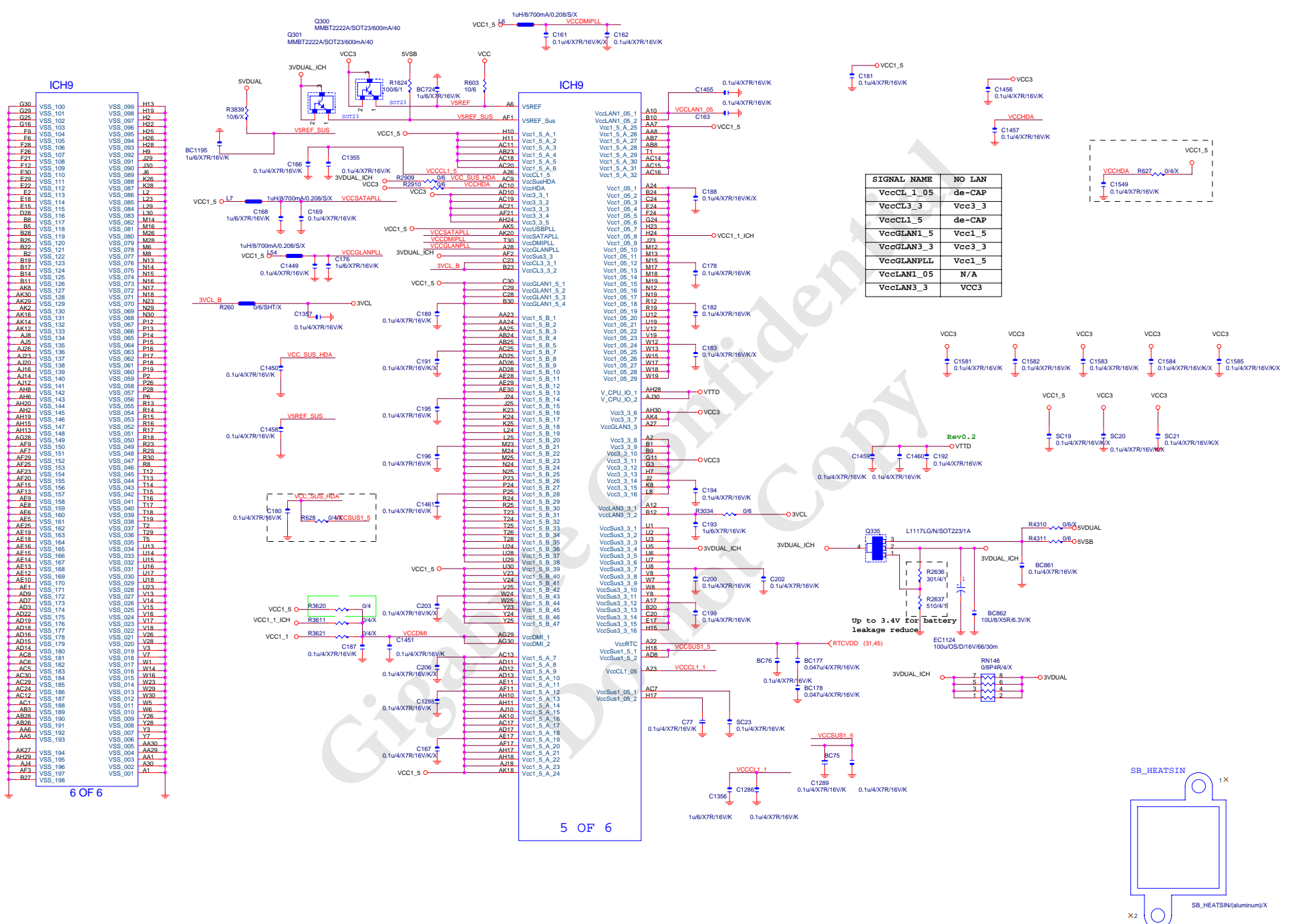
Date: Thursday, April 29, 2010

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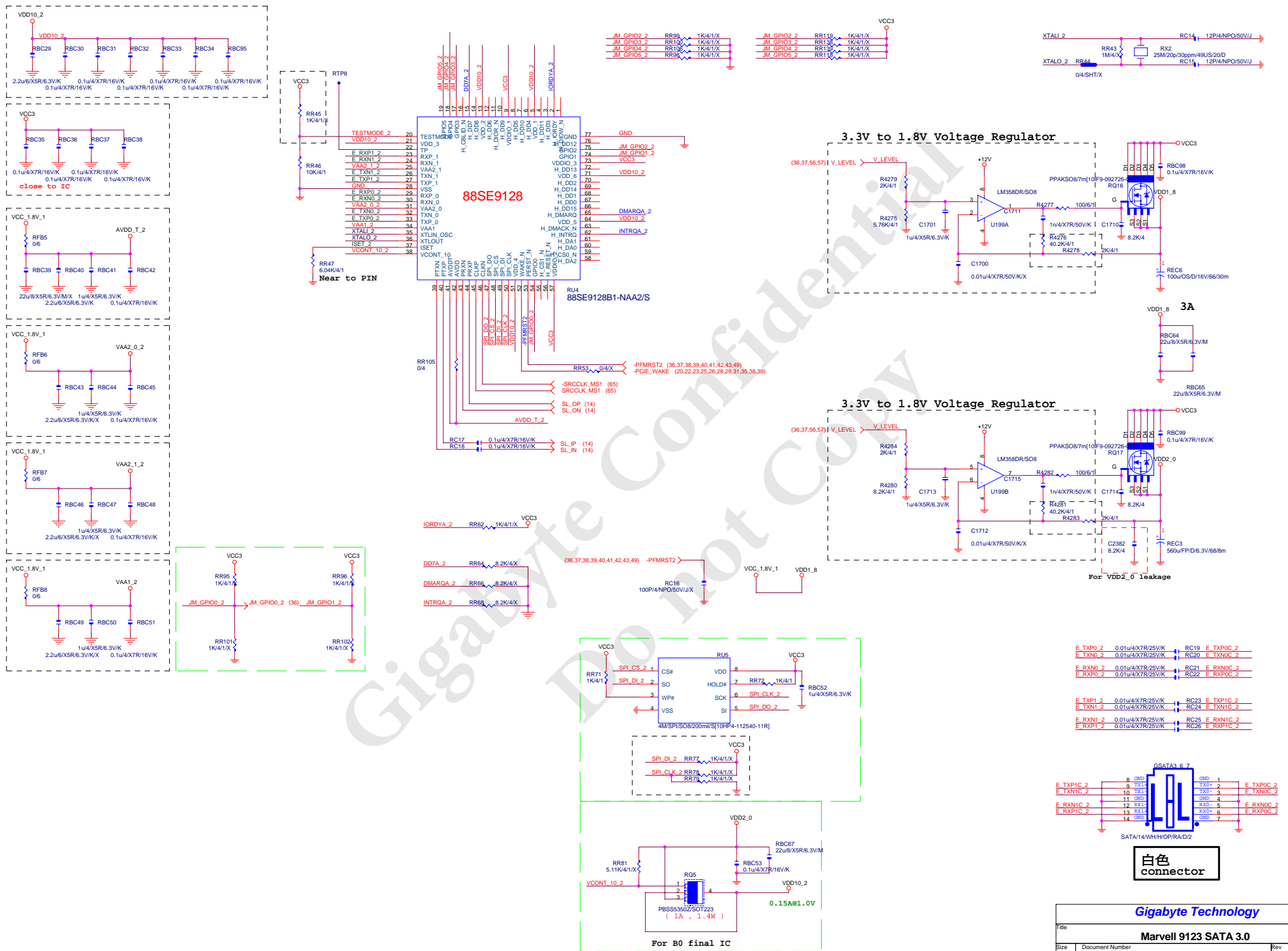
Rev: 1.0



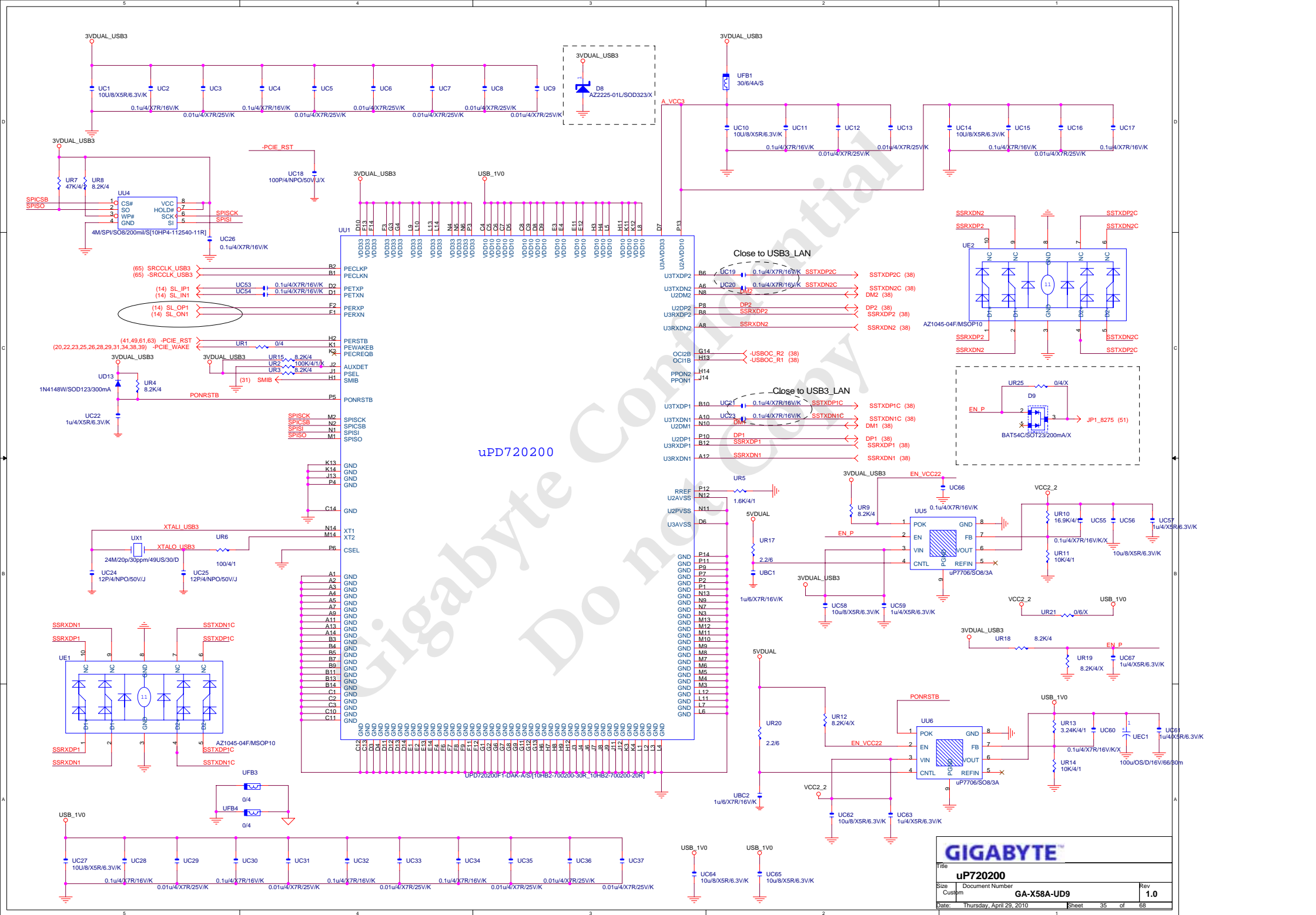




SIGNAL NAME	NO LAN
VccCL1_05	de-CAP
VccCL3_3	Vcc3_3
VccCL1_5	de-CAP
VccGLAN1_5	Vcc1_5
VccGLAN3_3	Vcc3_3
VccGLANPLL	Vcc1_5
VccLAN1_05	N/A
VccLAN3_3	VCC3



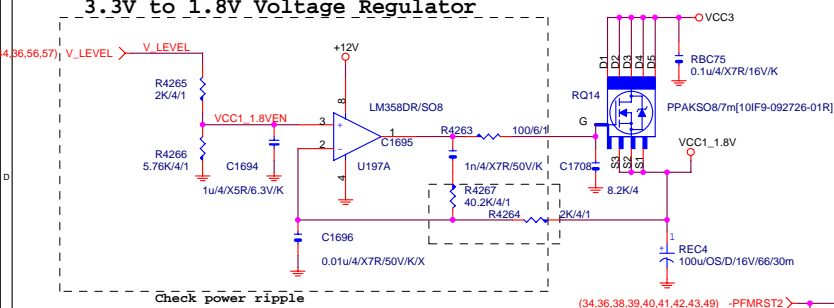




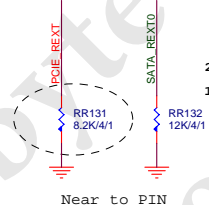
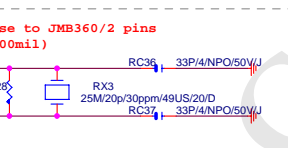
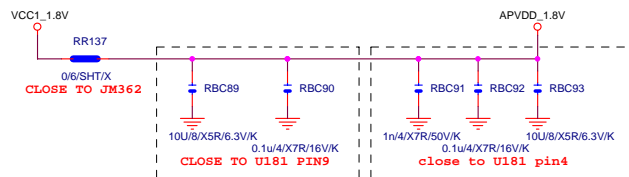
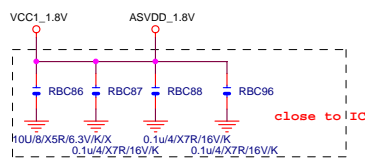
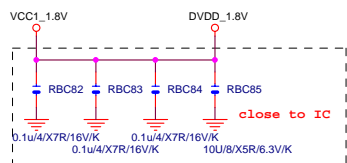
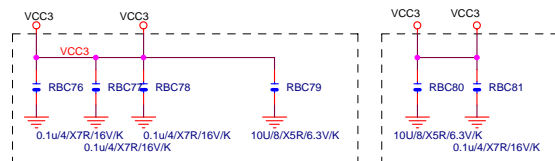
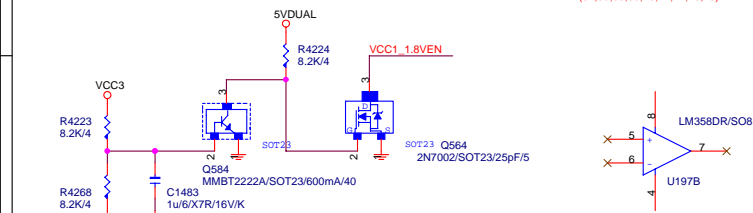




# 3.3V to 1.8V Voltage Regulator

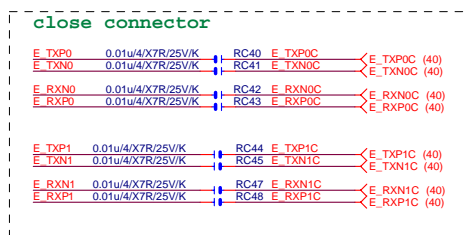


Check power ripple

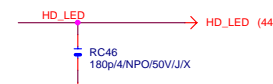
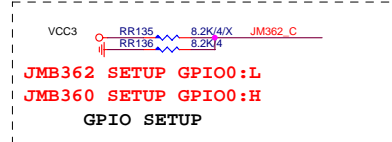
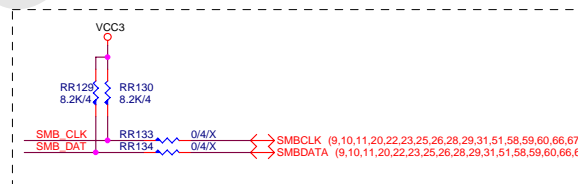


250MIL TRACE  
12MIL WIDTH

Near to PIN

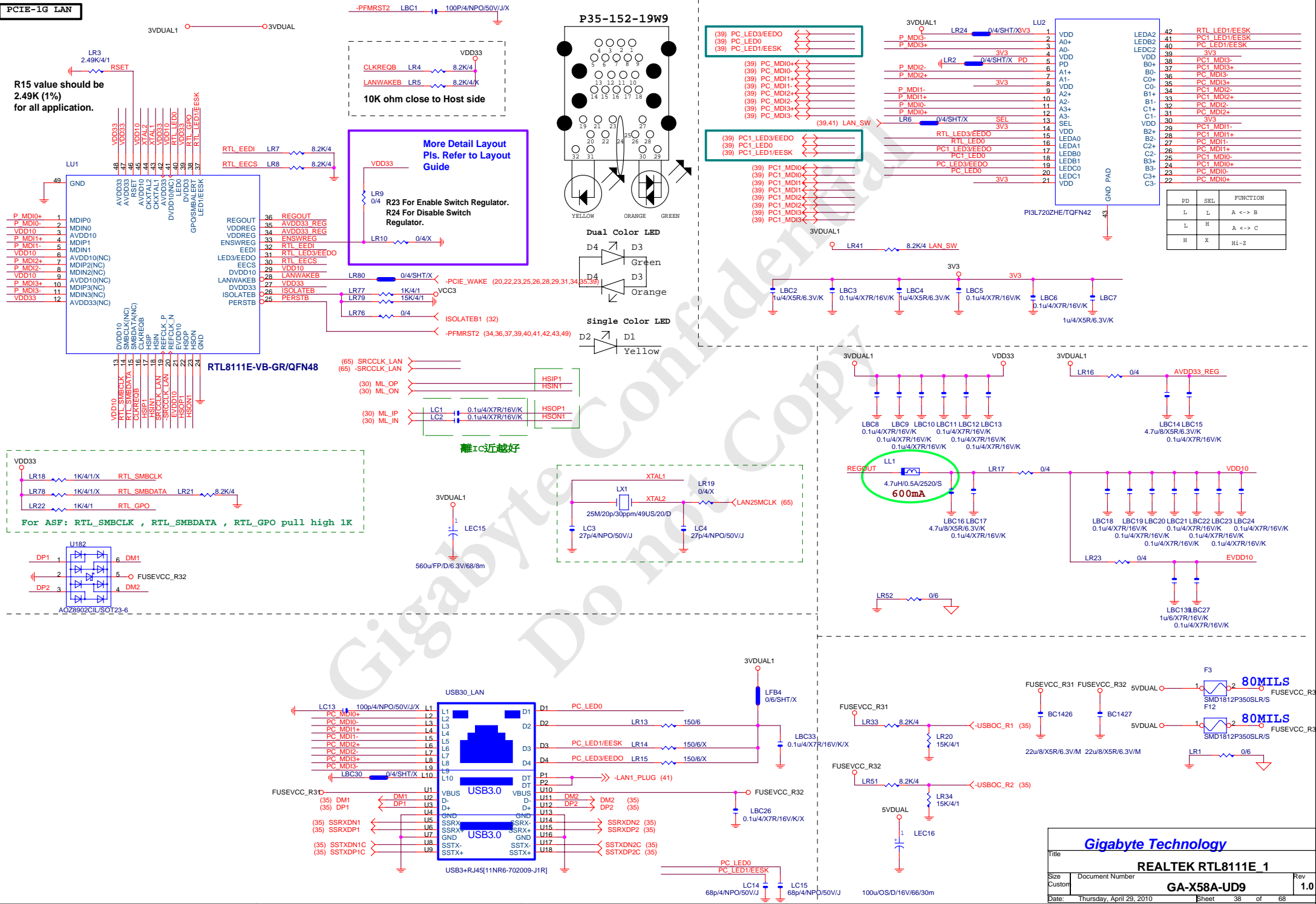


close connector



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JMB362			
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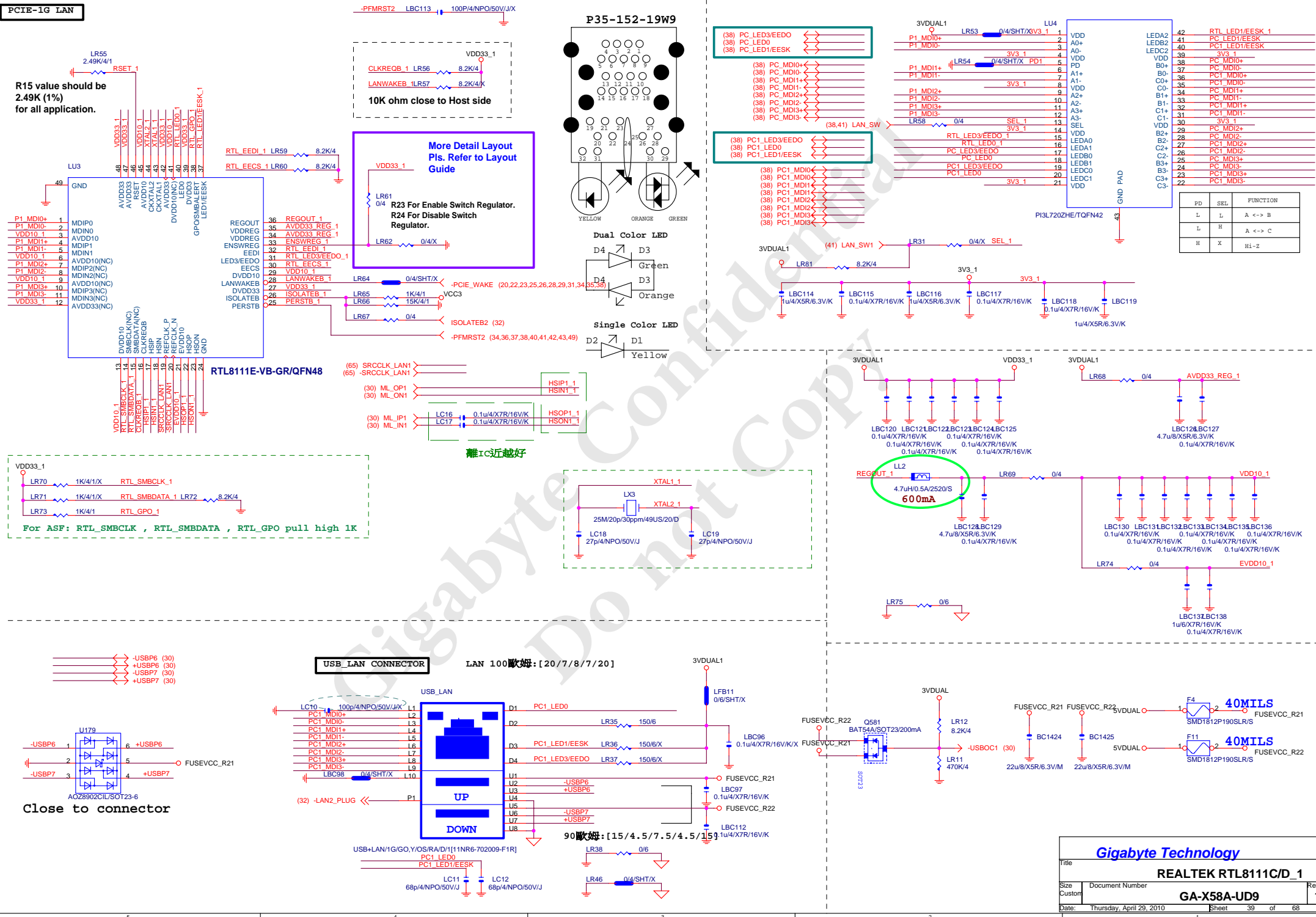
## PCIE-1G LAN



PD	SEL	FUNCTION
L	L	A $\leftrightarrow$ B
L	H	A $\leftrightarrow$ C
H	X	Hi-Z

## PCIE-1G LAN

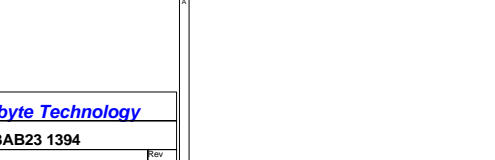
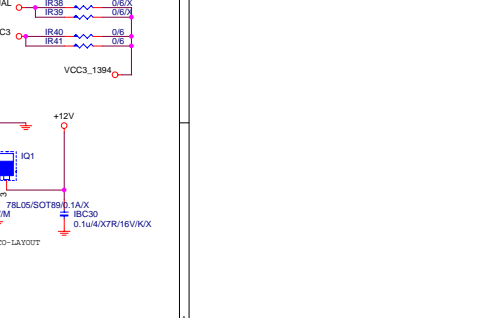
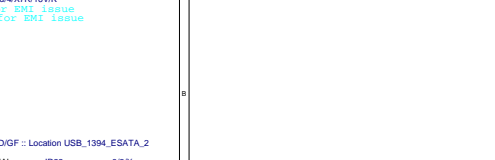
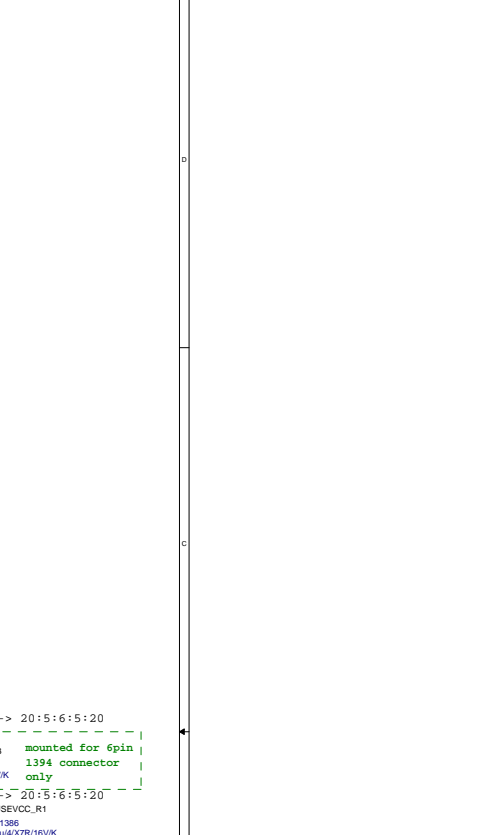
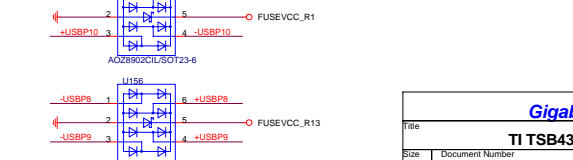
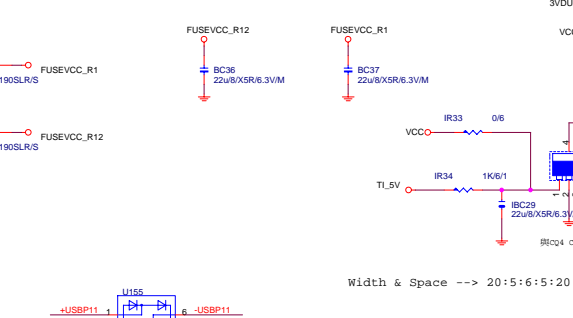
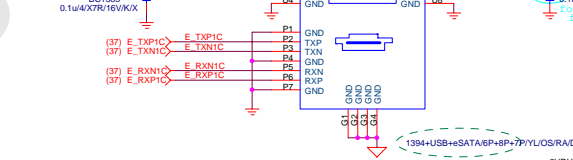
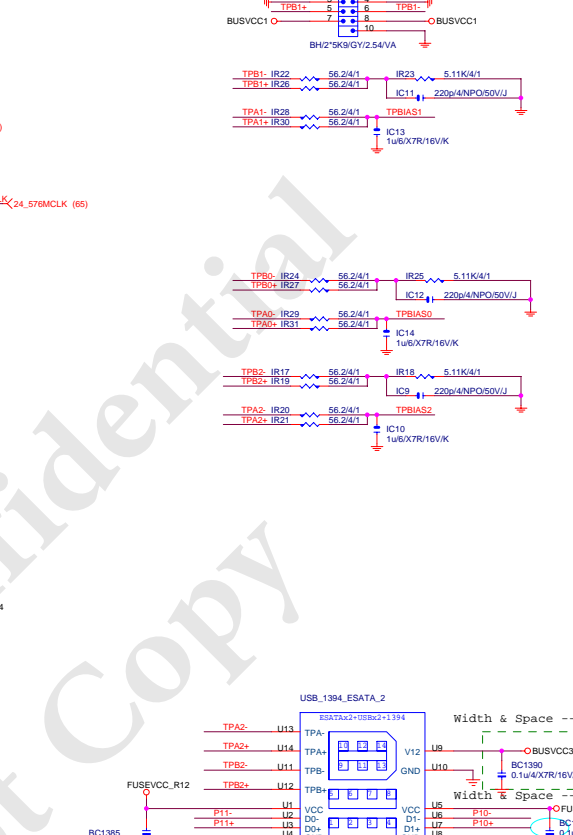
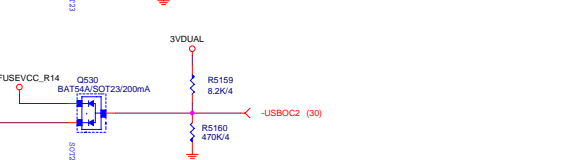
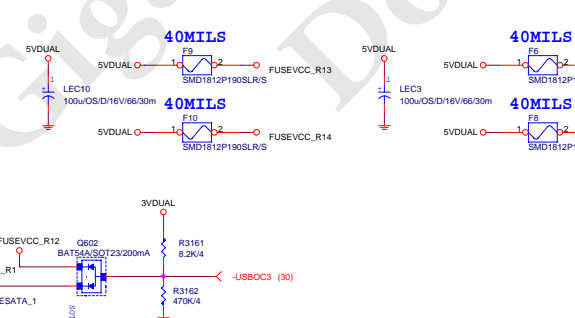
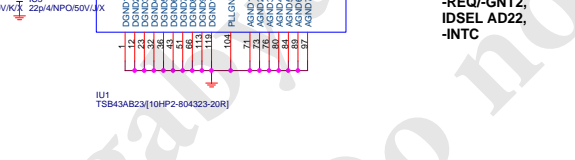
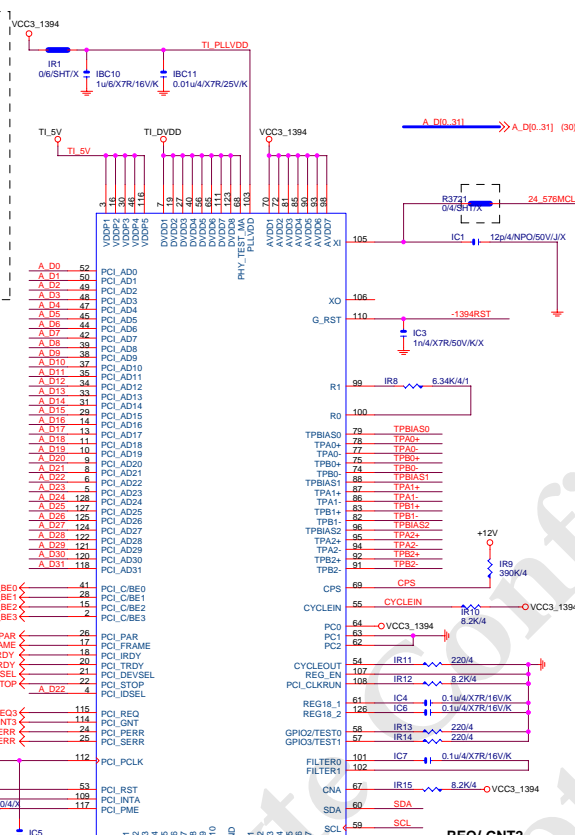
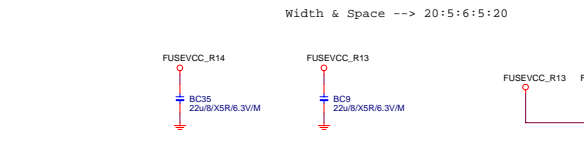
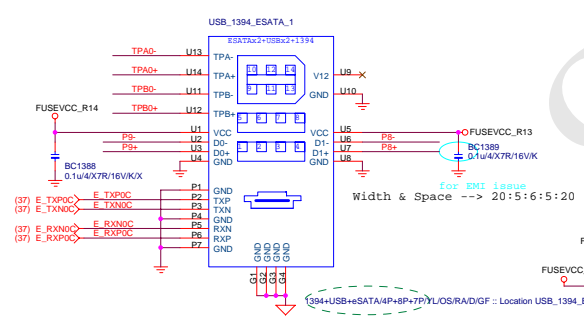
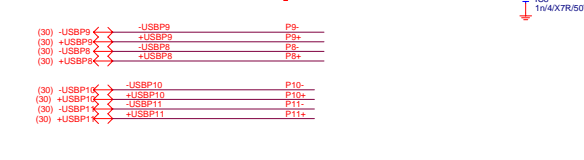
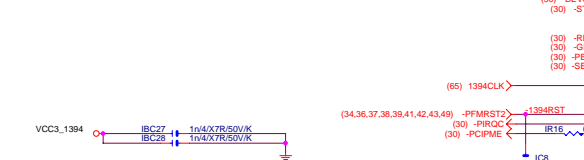
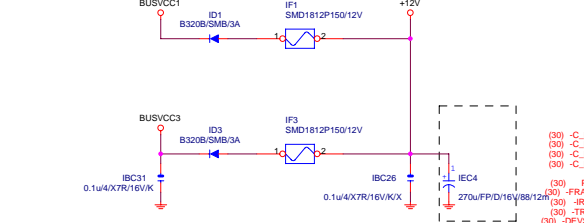
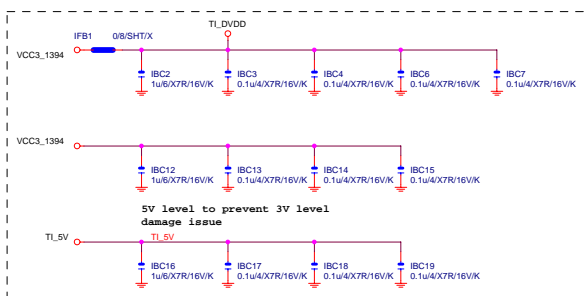
**R15 value should be  
2.49K (1%)  
for all application.**

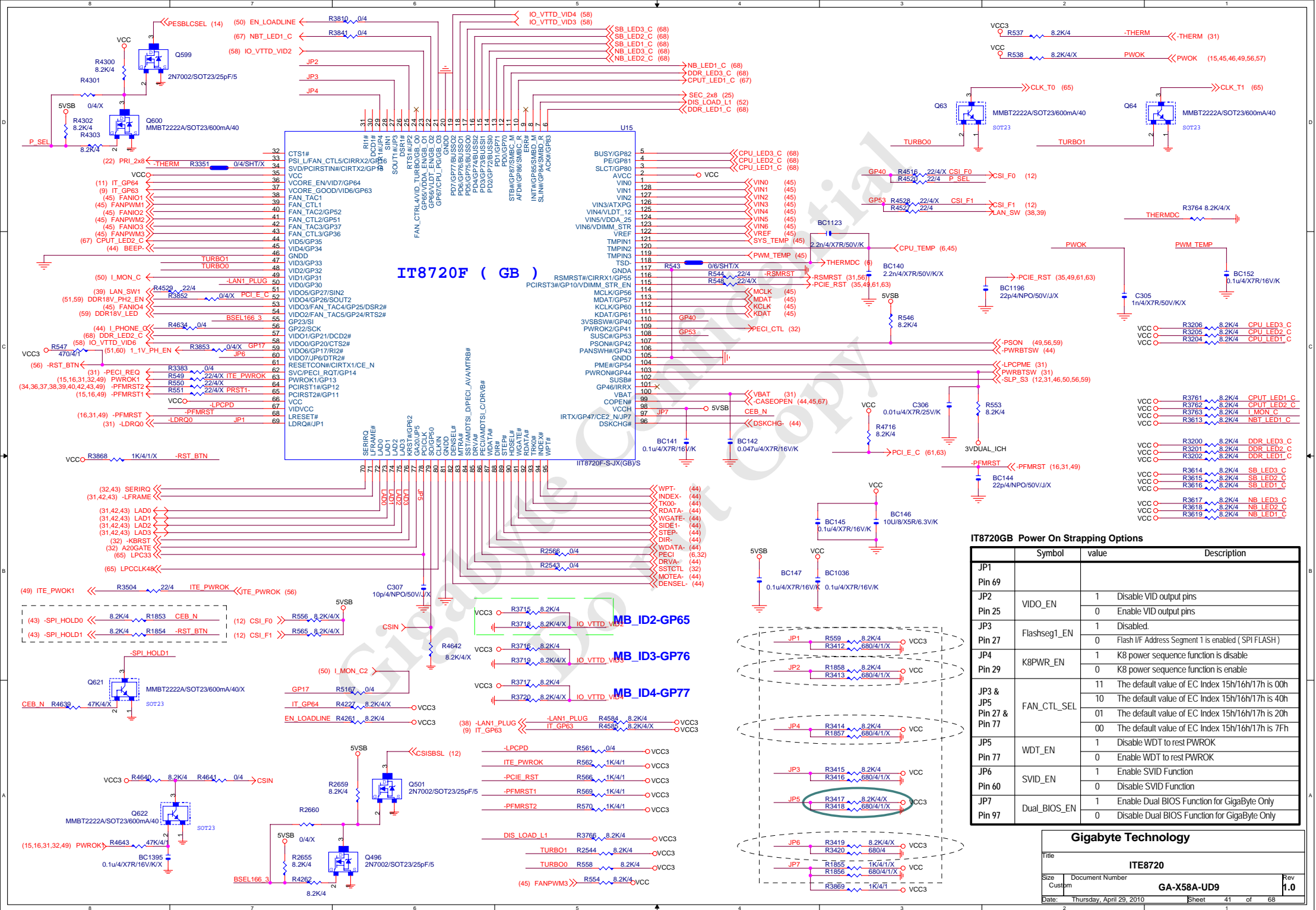


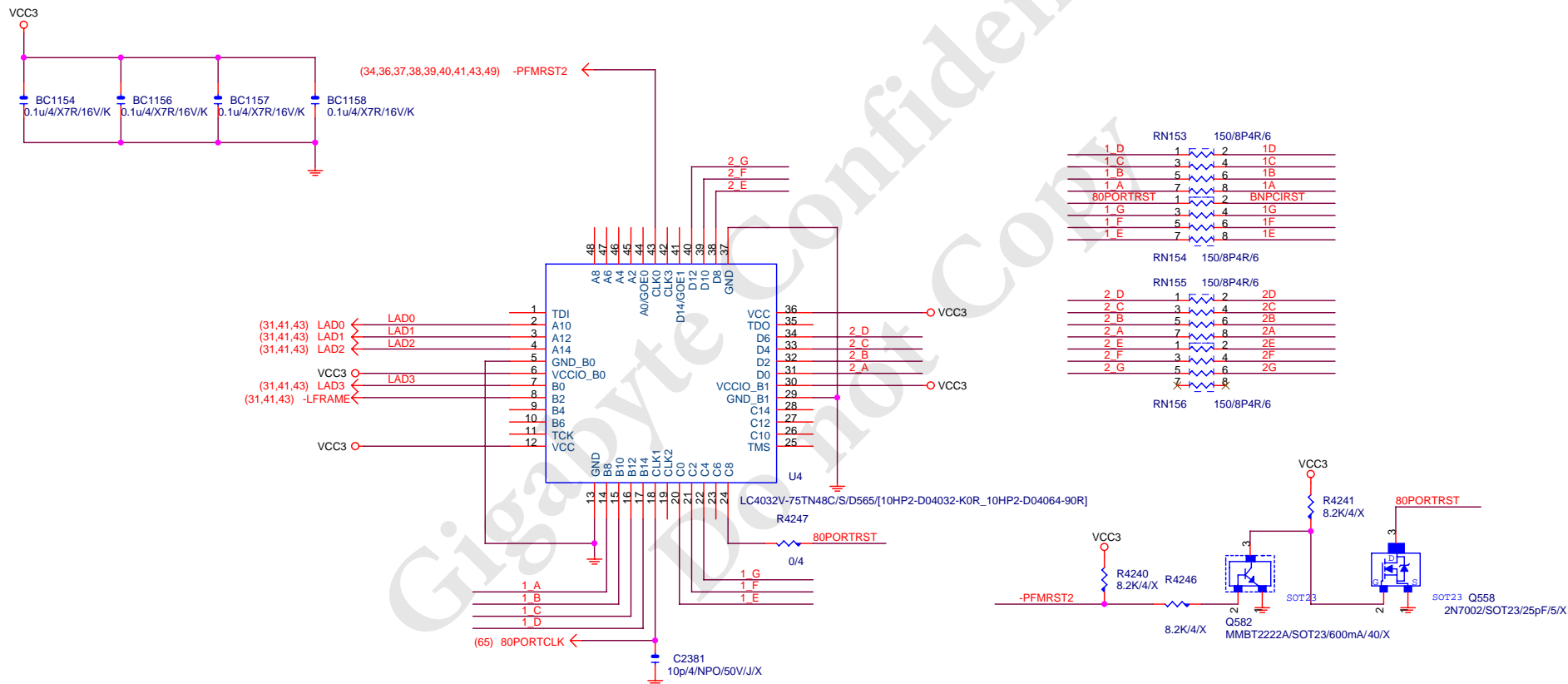
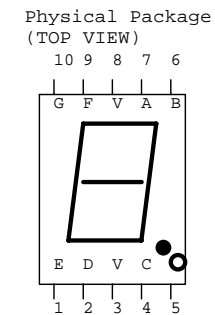
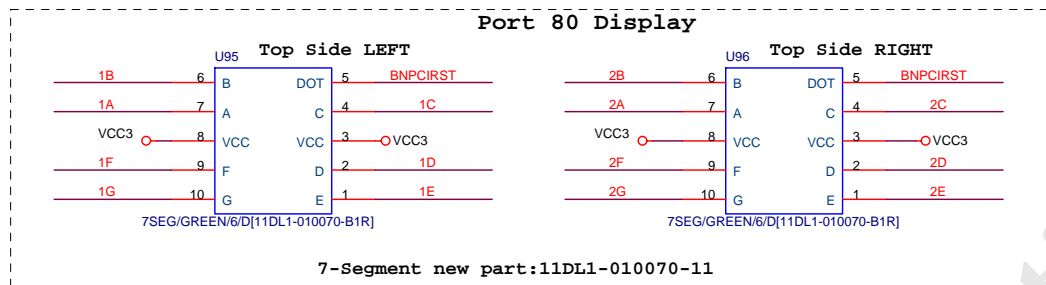
**Gigabyte Technology**

REALTEK RTL8111C/D 1

Size	Document Number	Rev
Custom	<b>GA-X58A-UD9</b>	<b>1.0</b>
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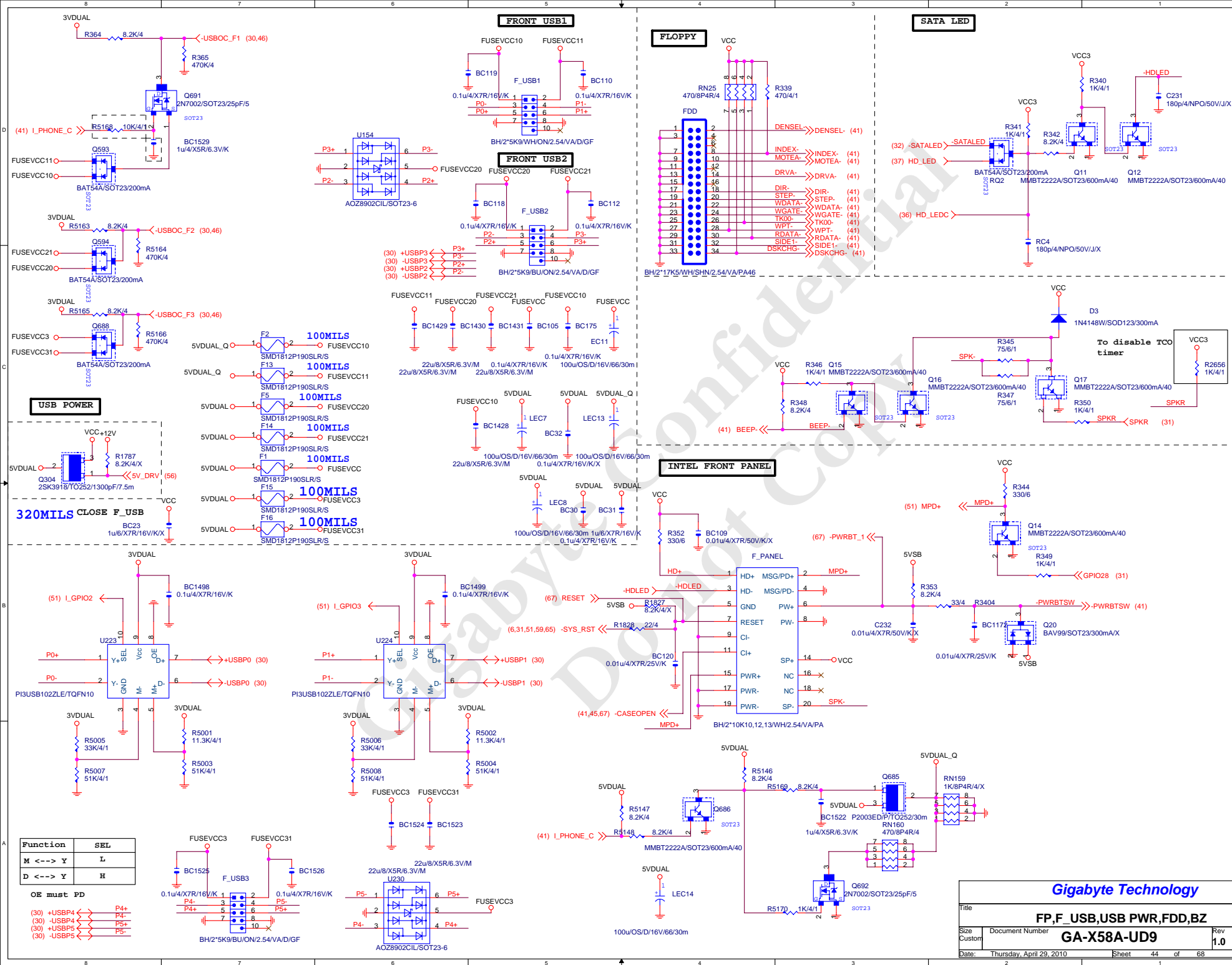
**Gigabyte Technology**

Title		
80 PORT		
Size B	Document Number	Rev
	GA-X58A-UD9	1.0
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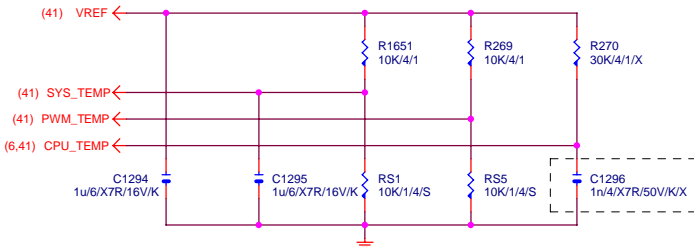




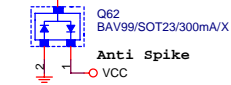
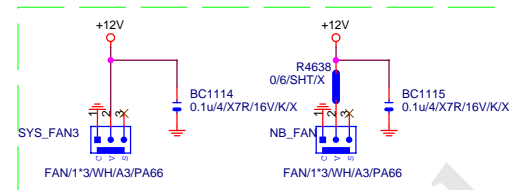
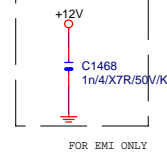




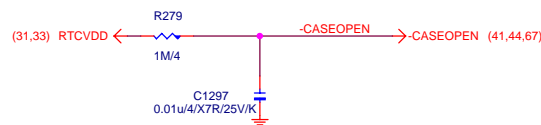
# TEMP H/W MONITOR



# CPU SMART FAN

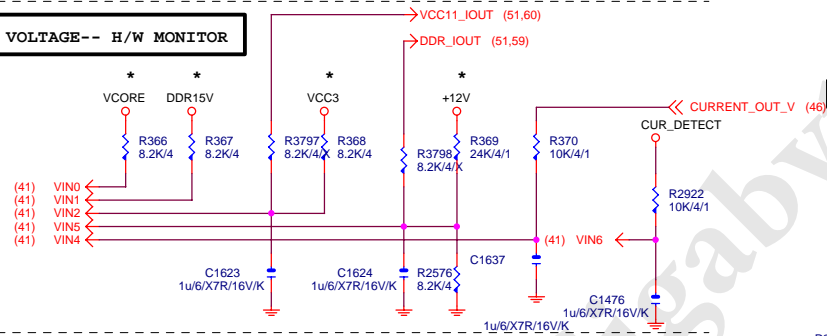


# CASE OPEN

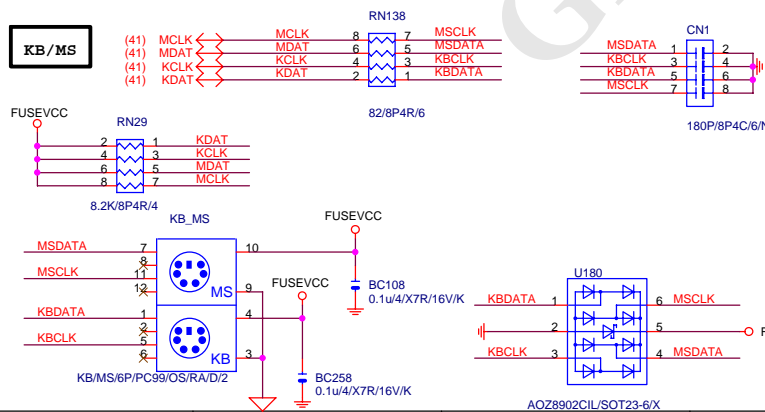


# Case Open Circuits

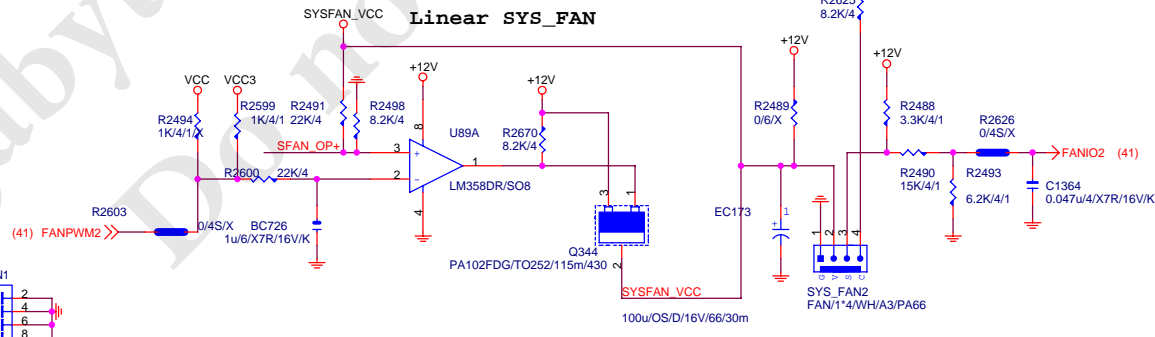
# VOLTAGE-- H/W MONITOR



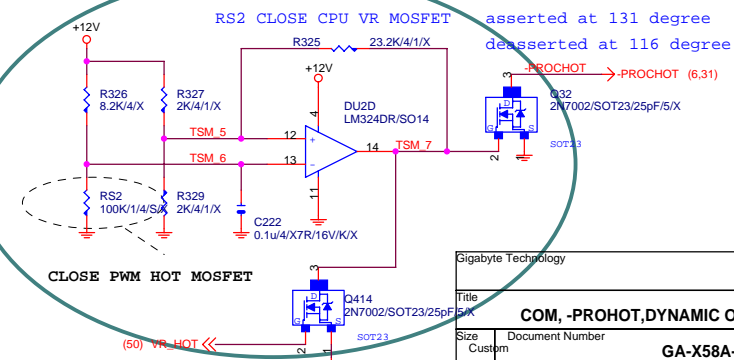
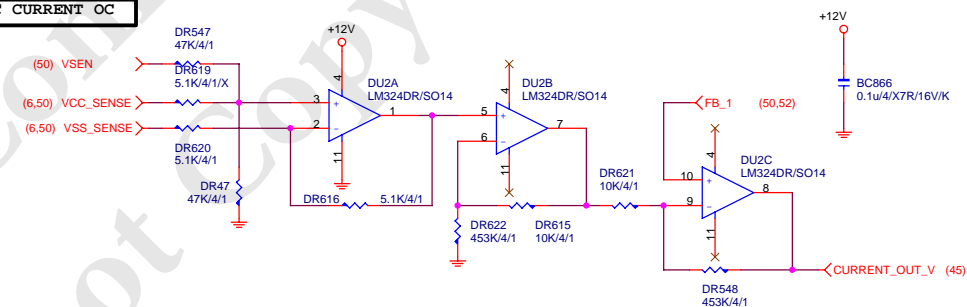
# KB/MS



# Linear SYS\_FAN

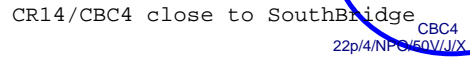


Gigabyte Technology			
Title			
HWM,KB/MS, FAN CTRL			
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Gigabyte Technology			
Title			
COM, -PROHOT,DYNAMIC OC +12V 保護線路			
Size	Document Number	Rev	
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layout

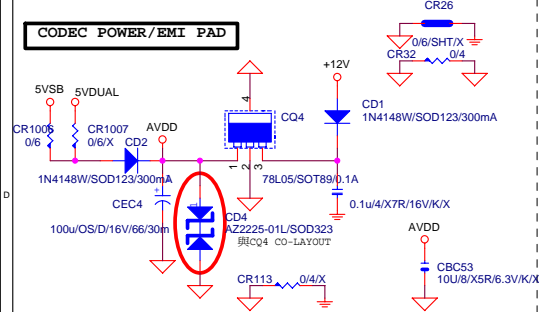


## Analog Area

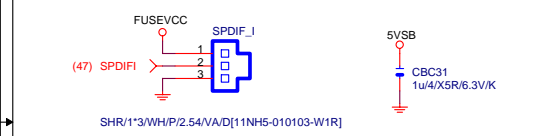
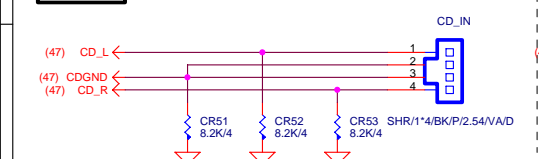
JD resistors close to pin13 of CODEC

Can Support Amp Out

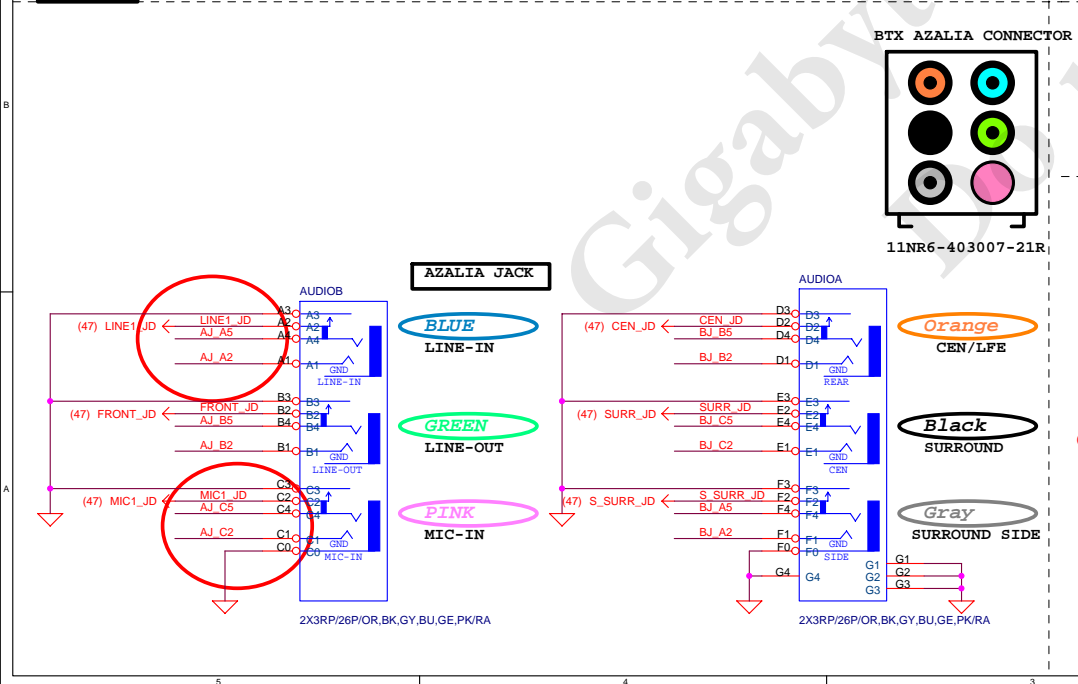
## CODEC POWER/EMI PAD



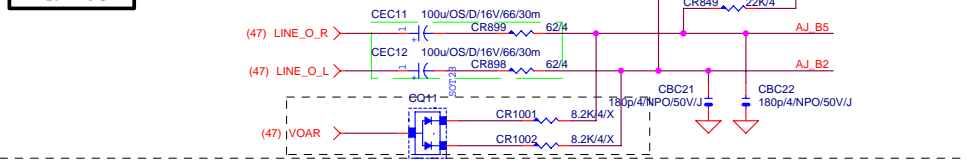
## CD IN



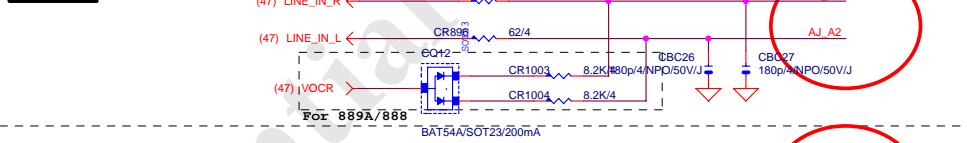
## SPDIF\_IN



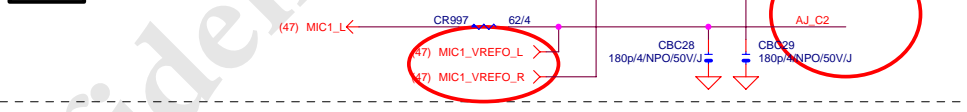
## LINE-OUT



## LINE-IN



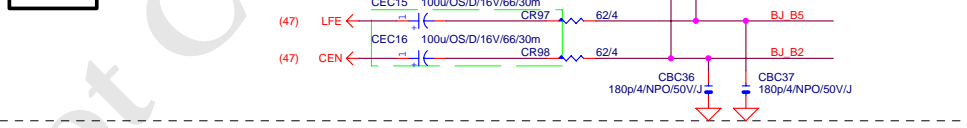
## MIC-IN



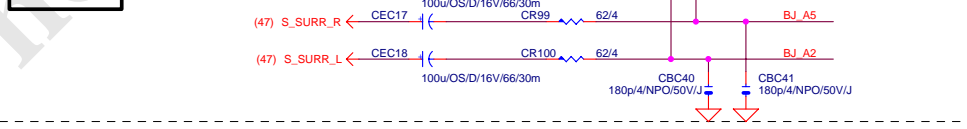
## SURROUND



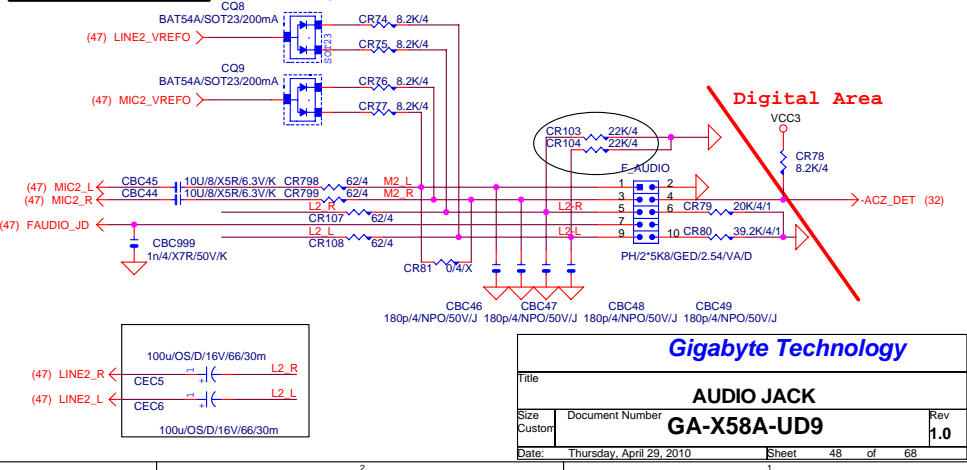
## CEN/LFE



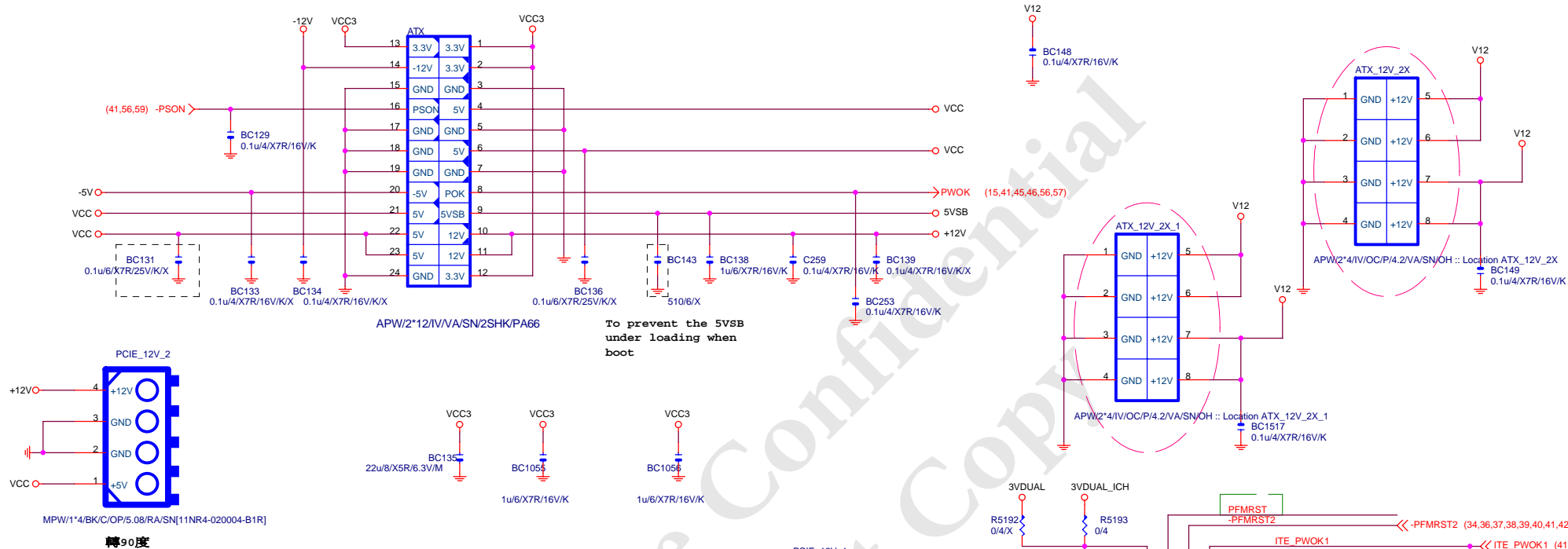
## SURR BACK



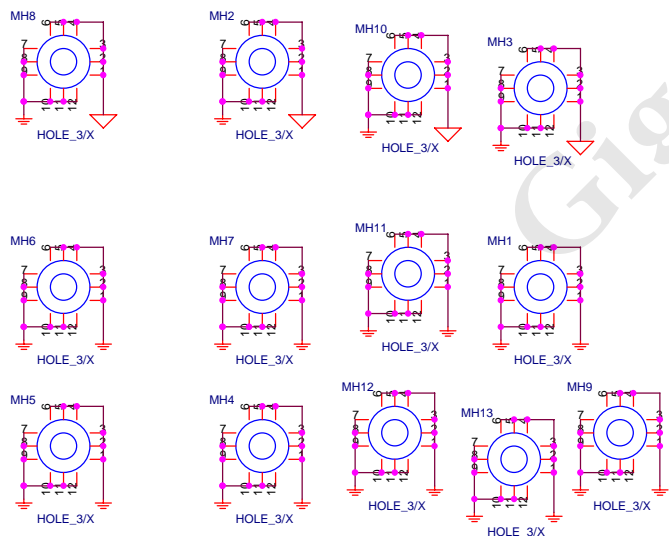
## AZALIA FRONT PANEL



# ATX POWER CONNECTOR

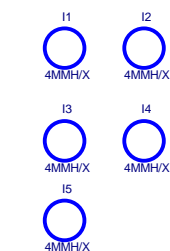
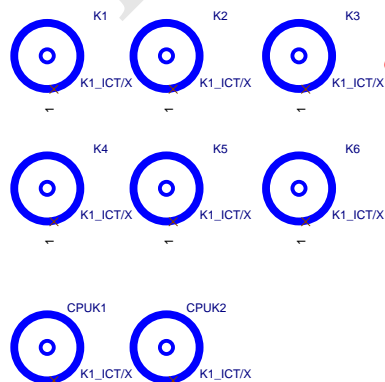


## PCB 螺絲孔位置(Footprint不同)

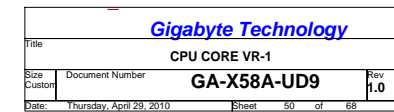


180

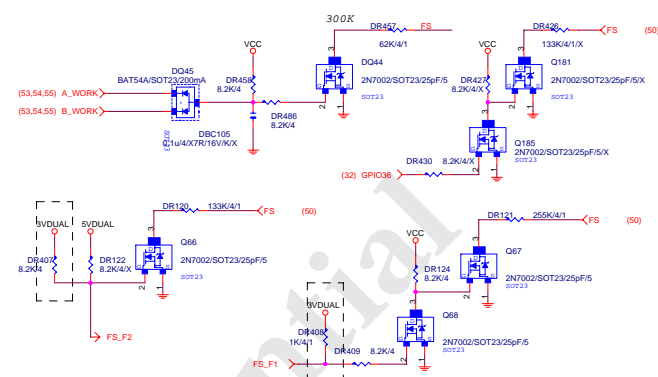
180



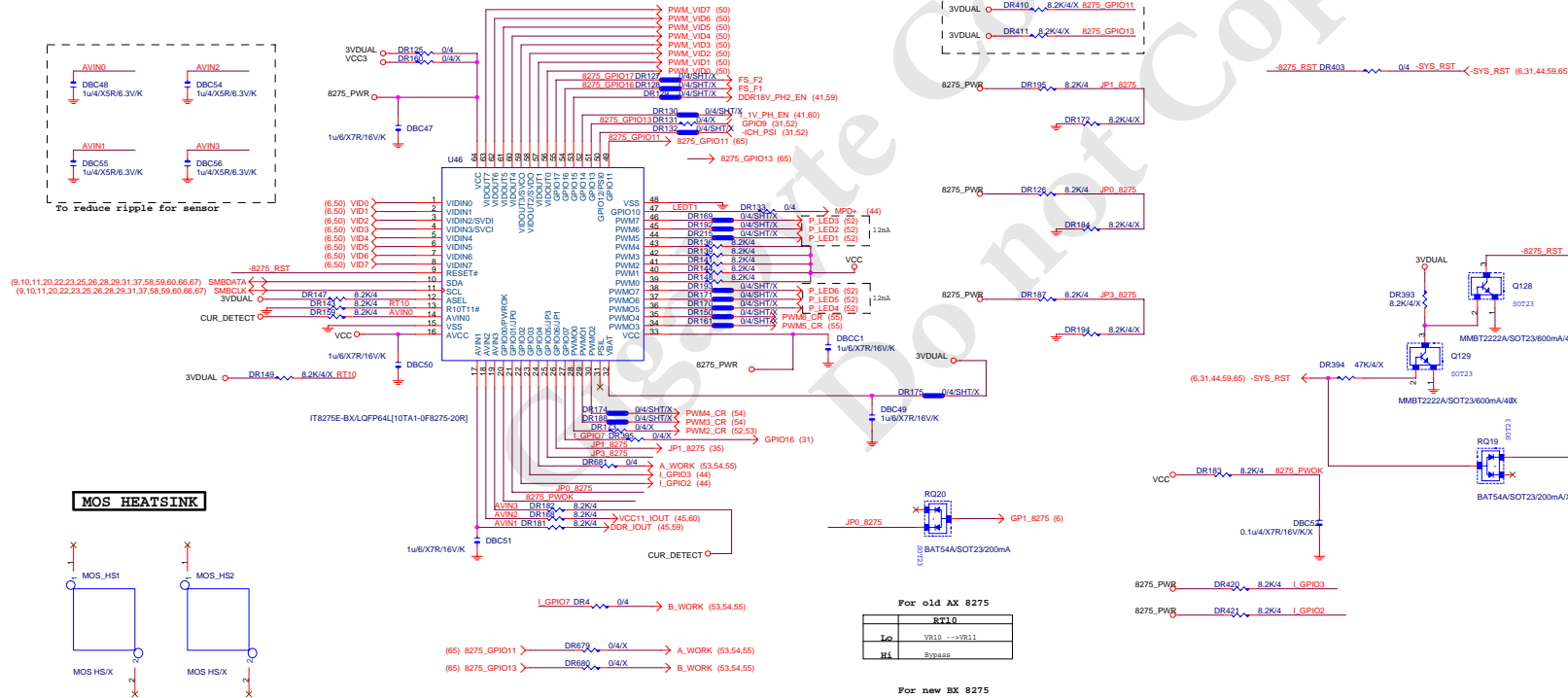
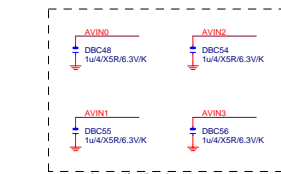
Gigabyte Technology			
Title			
ATX POWER CONNECTOR			
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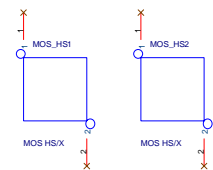




PWM FREQUENCY (400K-1MHz)				
		IT8275 GP17 GP16	ICH GP36	
Default	400K	L	X	X
	500K	L	L	X
	600K	X	X	X
	700K	X	L	X
	1MHz	X	L	L



## MOS HEATSINK



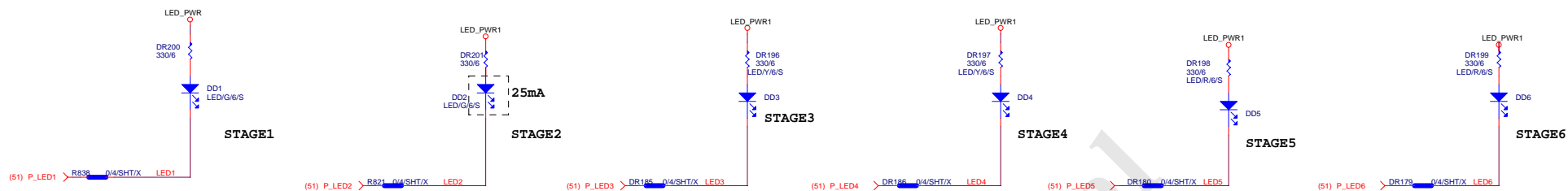
For old AX 8275

	RT10
Lo	VR10 --> VR11
uri	Remain

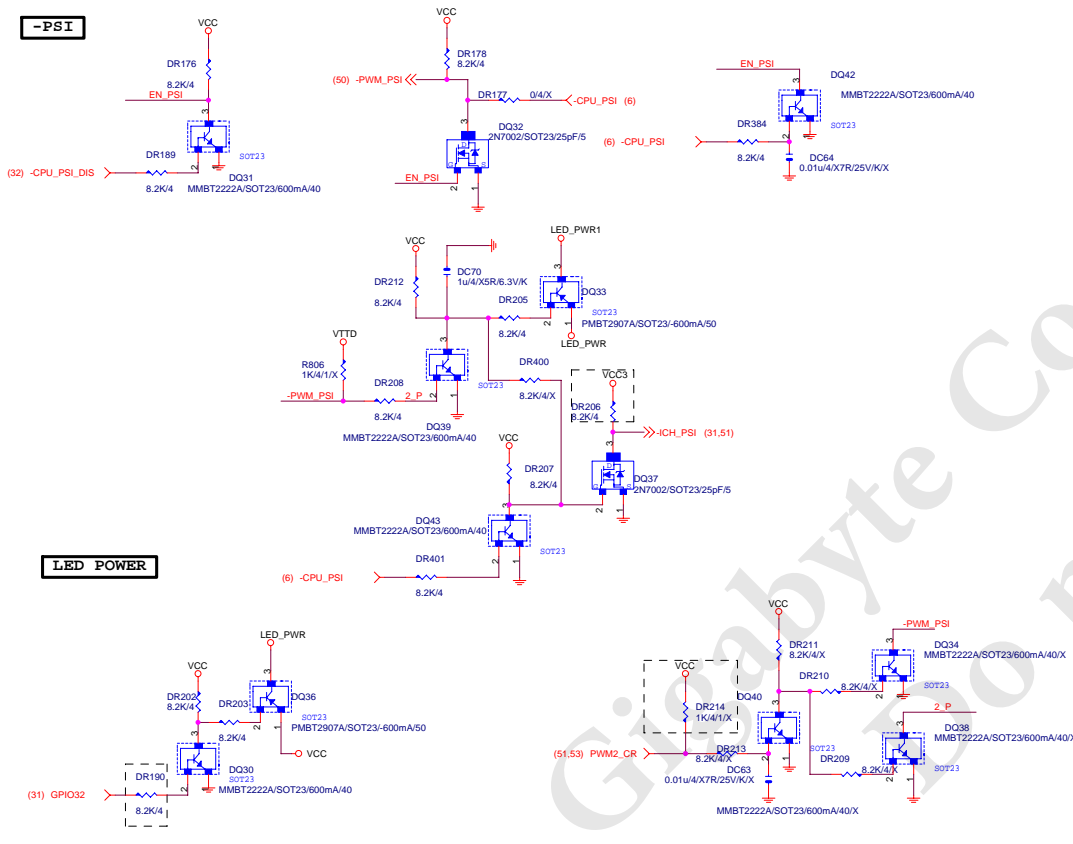
For new BX 8275

	<b>RT10</b>
<b>Lo</b>	PWM0 0-4 control by FW
<b>Hi</b>	PWM0-4 bypass to PWM00-

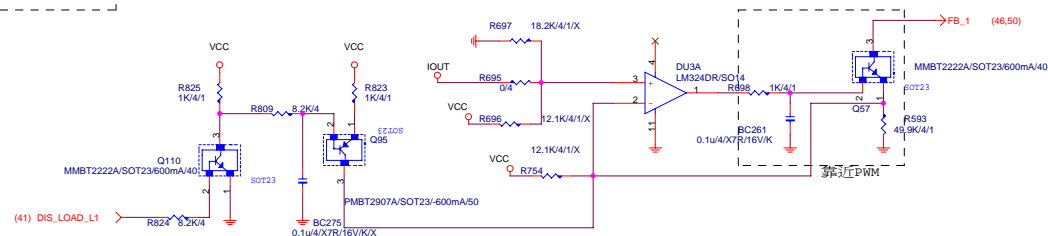
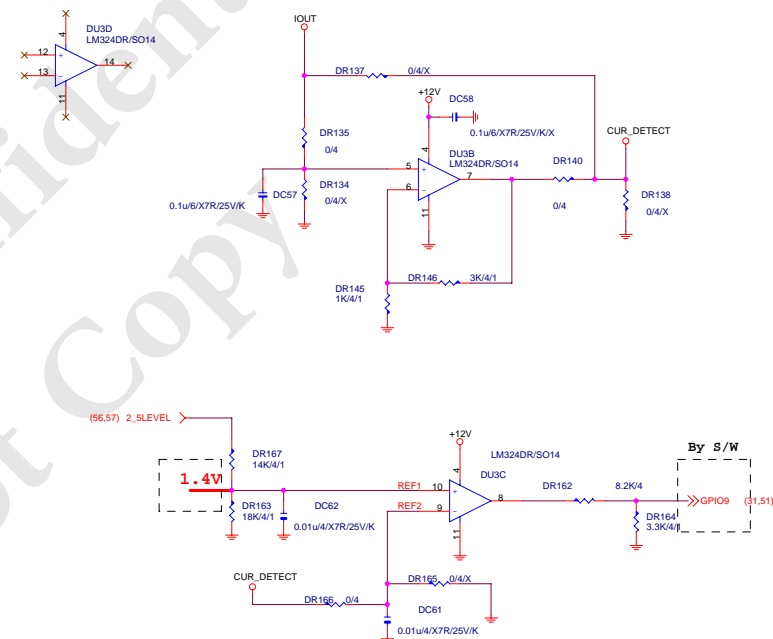
PHASE LED



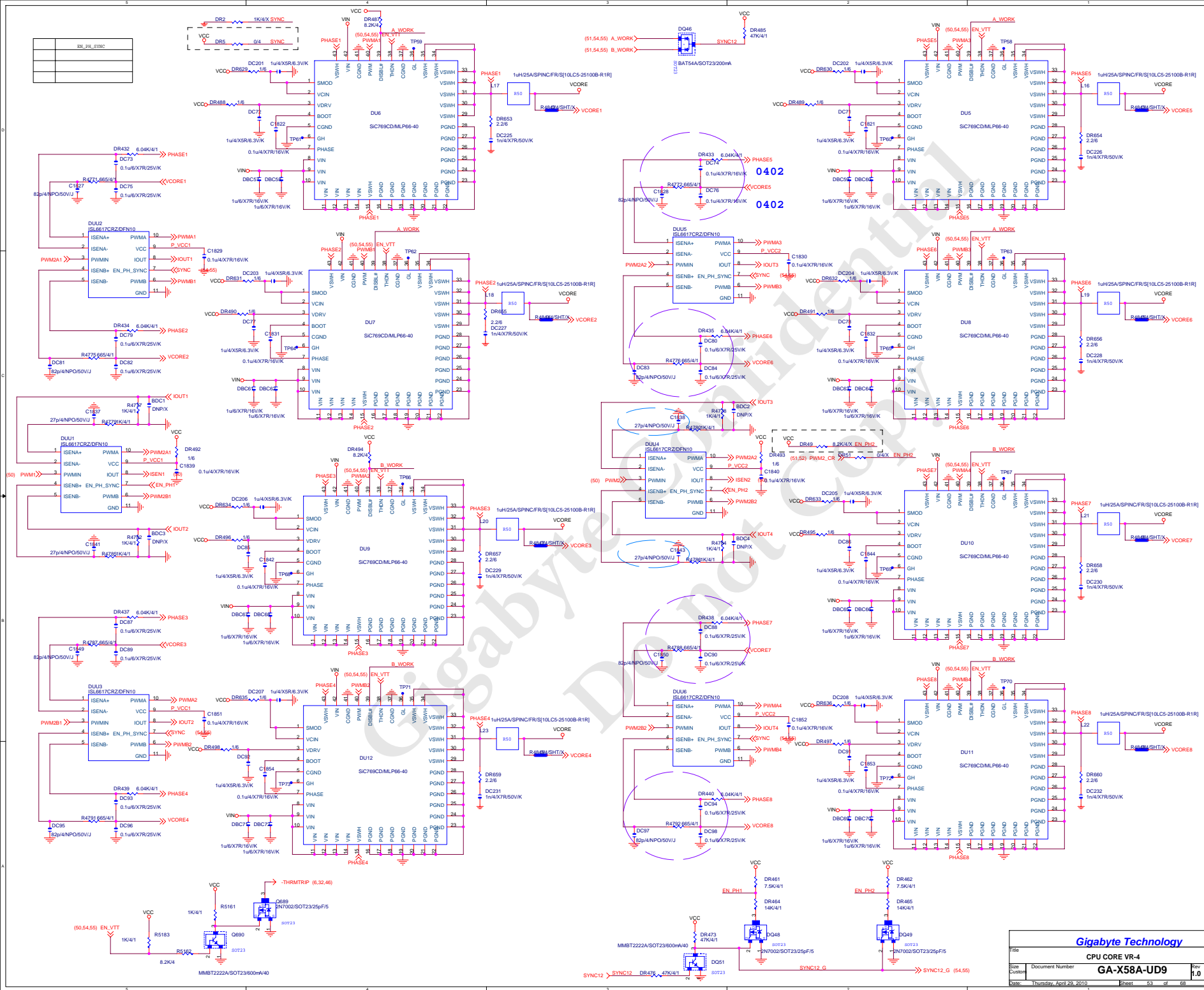
**-PSI**

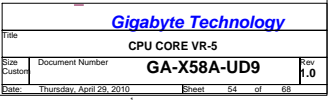


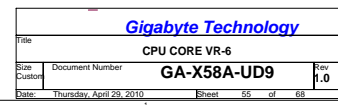
LED POWER

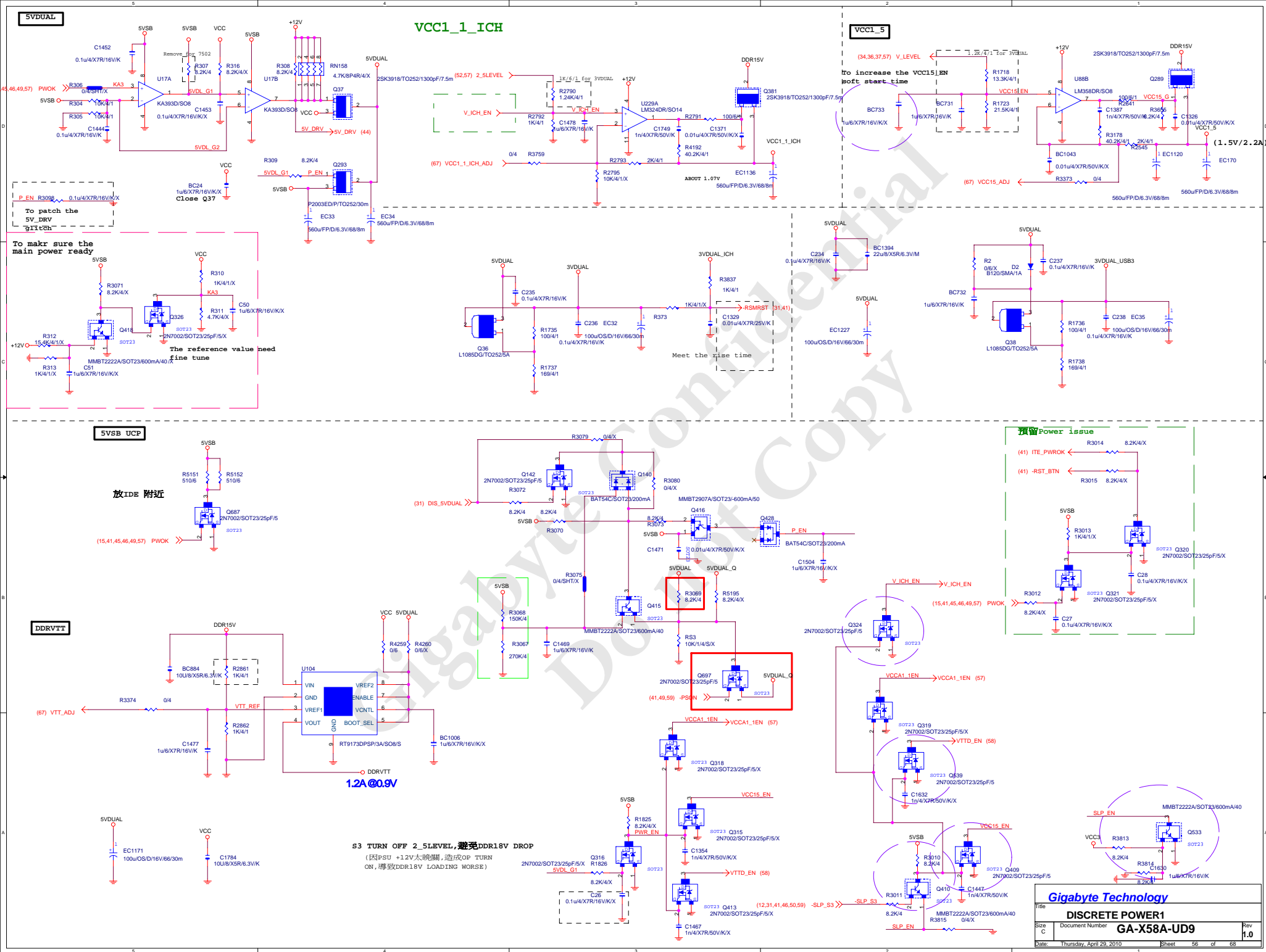


		EN_PH_SYNC



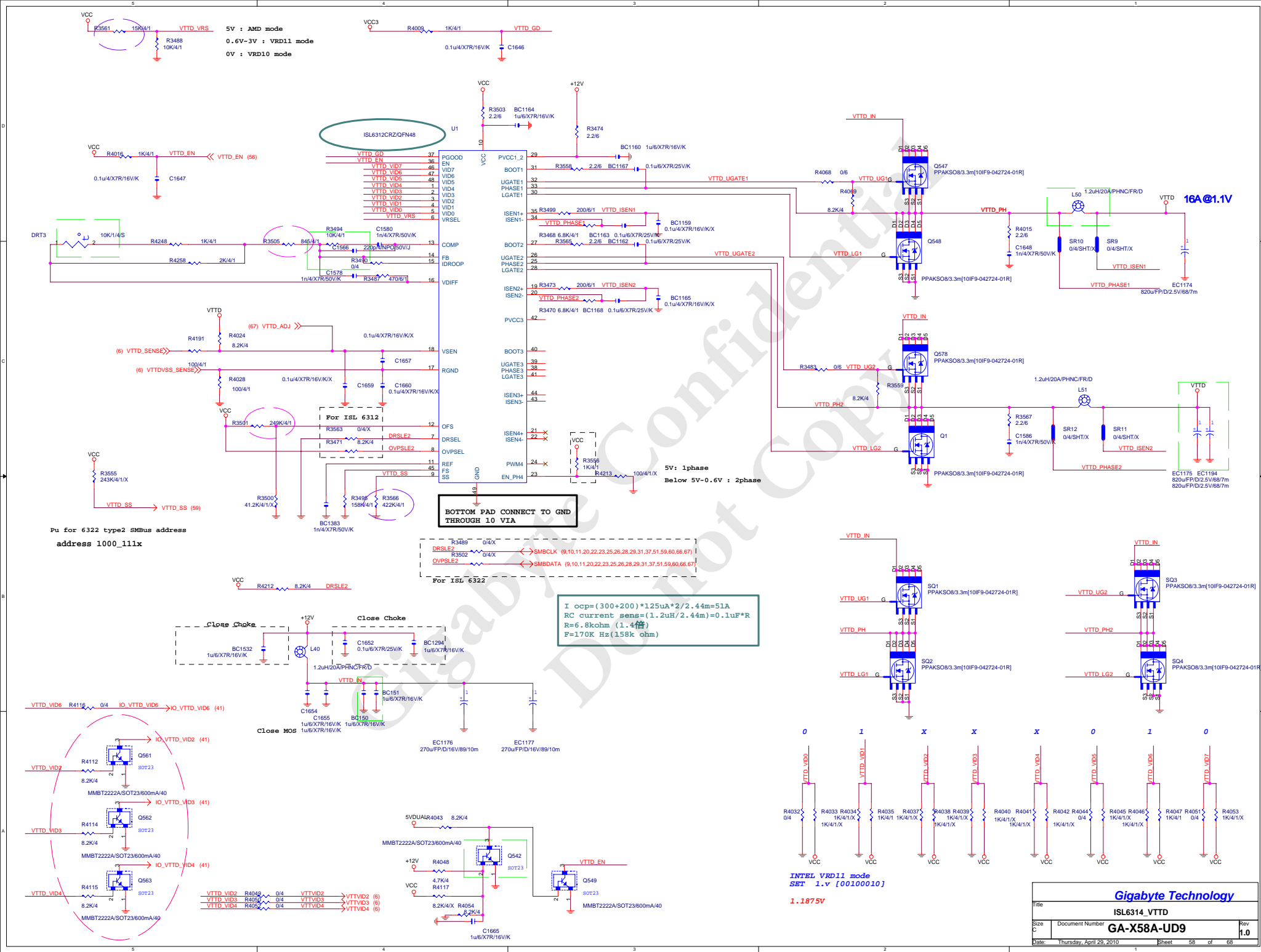


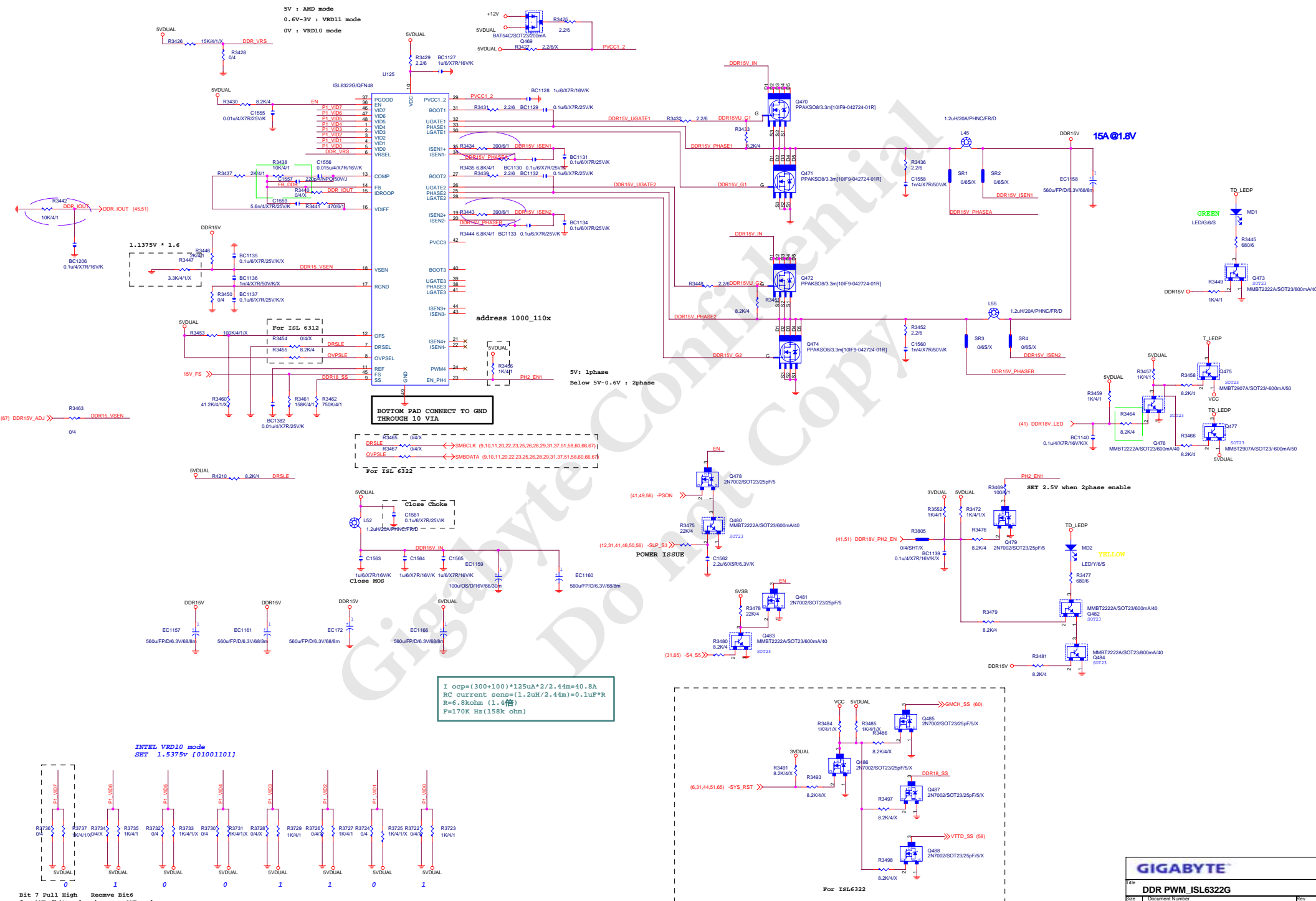




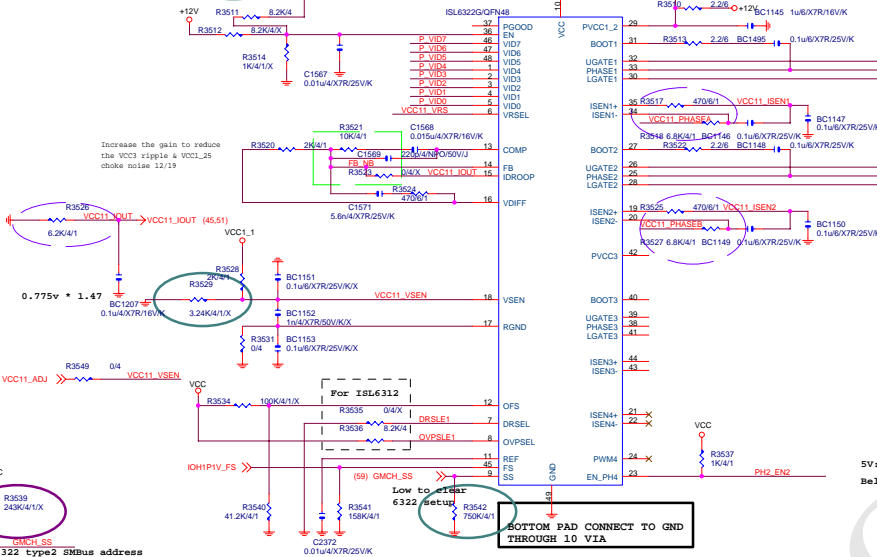
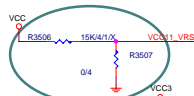








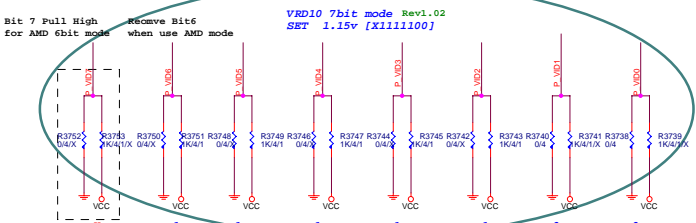
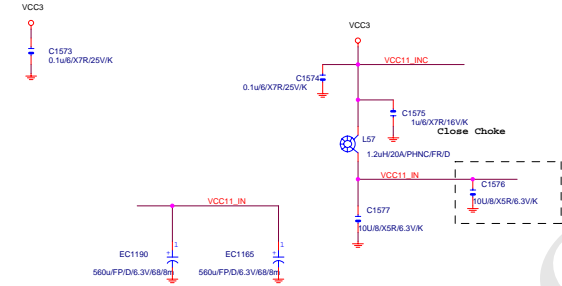
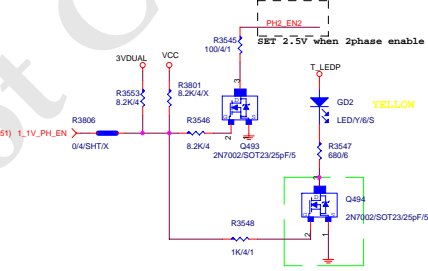
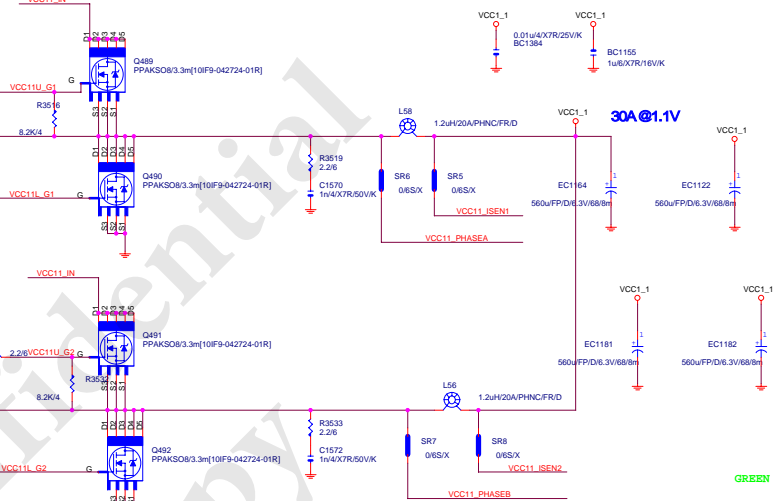
5V : AMD mode  
0.6V-3V : VRD11 mode  
0V : VRD10 mode

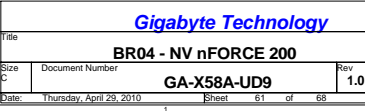


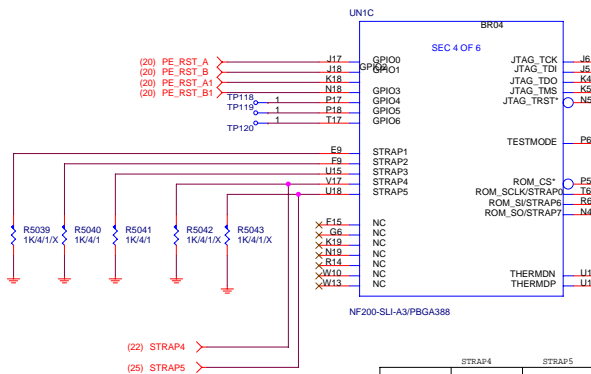
5V: 1phase  
Below 5V-0.6V : 2phase

For ISL6322

$I_{ocp} = (300 + 100) * 1.25 \mu A * 2 / 2.44 m = 40.8 A$   
RC current sense =  $(1.2 \mu H / 2.44 m) = 0.1 \mu P * R$   
 $R = 6.8 k \Omega$  (1.44)  
 $P = 1.70 K$  Hz (1.58k ohm)

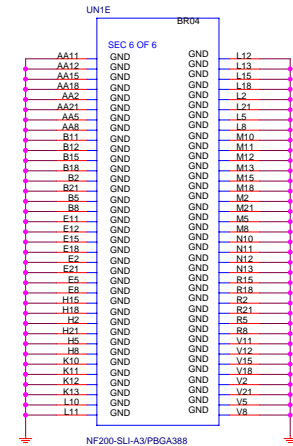
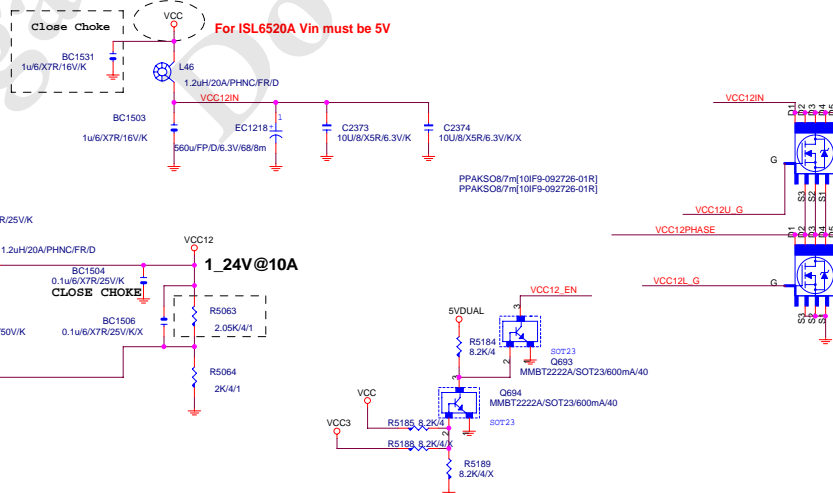
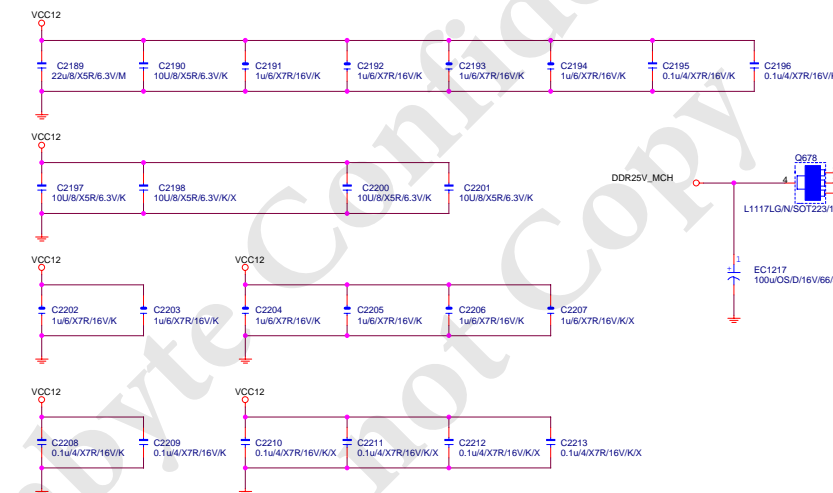
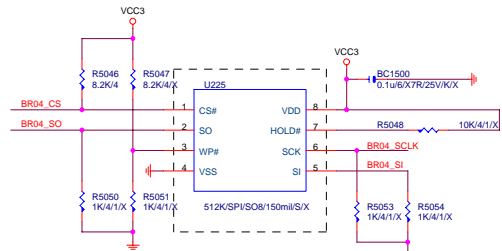




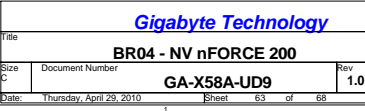


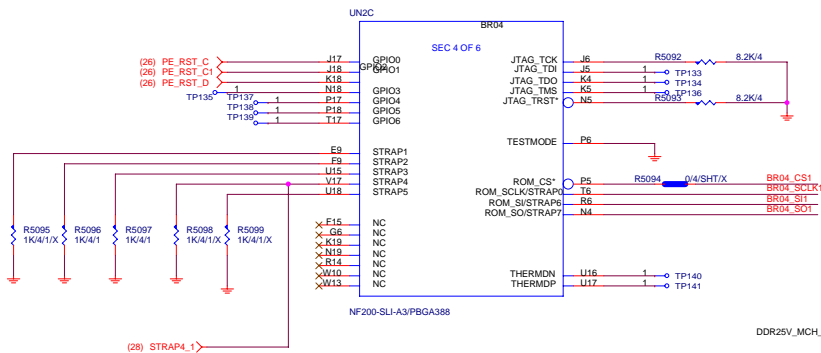
STRAP4	STRAP5
(STRAP5, 4)	PEXA
(H, H)	1X16
(H, L)	2X8
(L, H)	1X16
(L, L)	2X8

\* Default internal PU



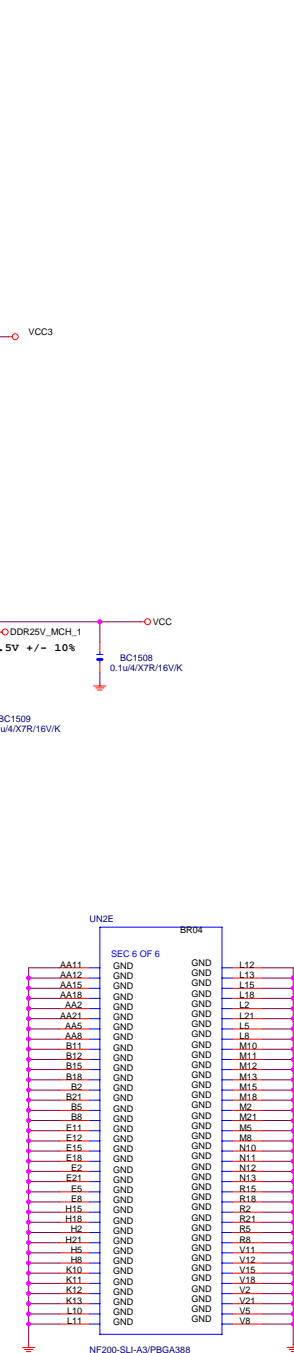
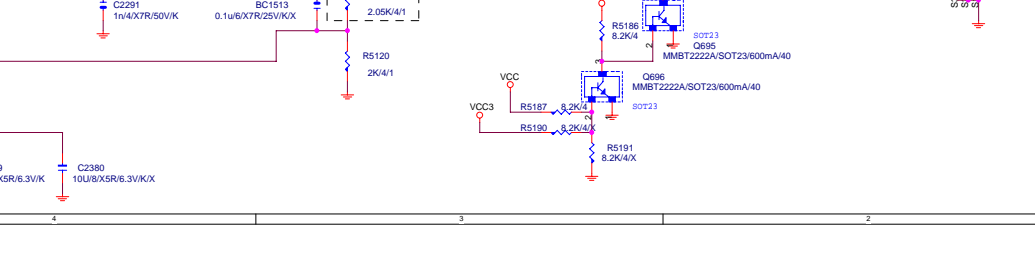
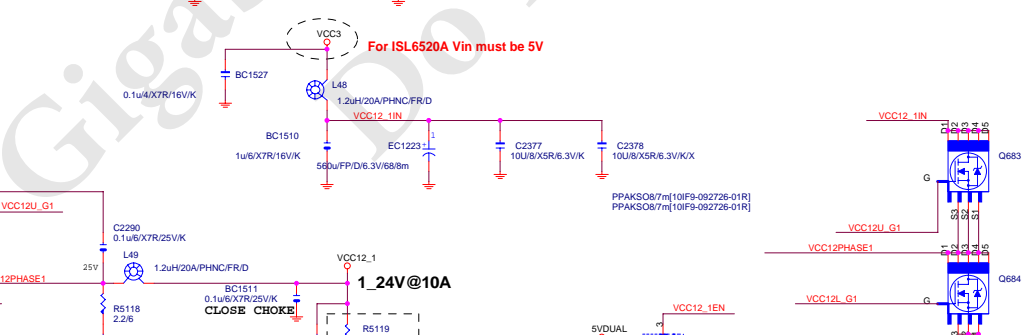
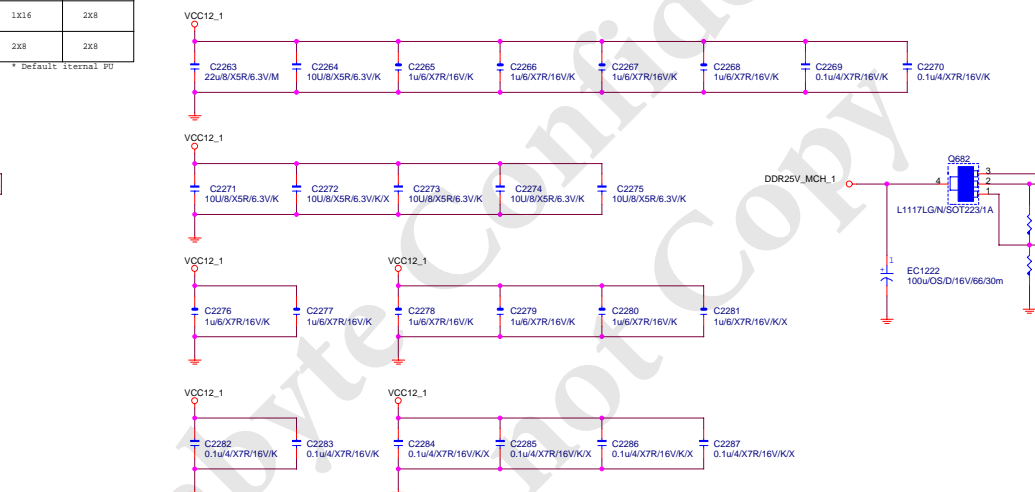
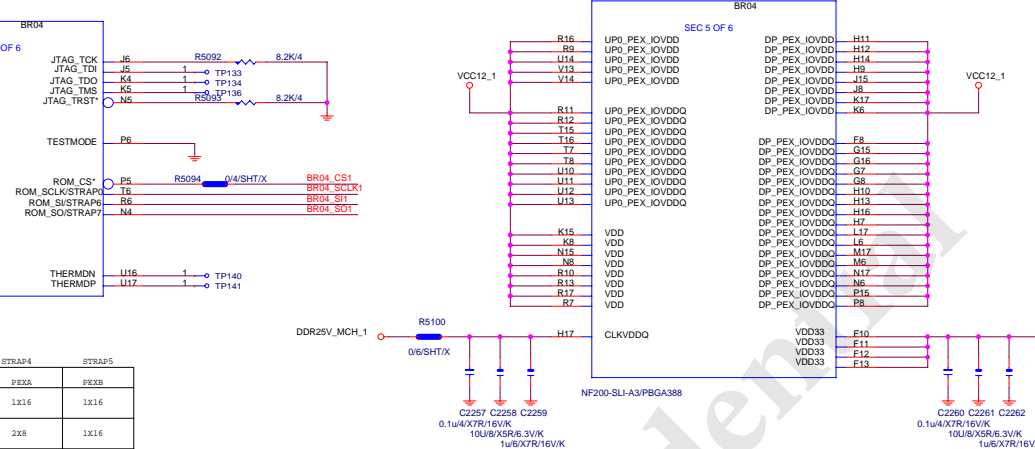
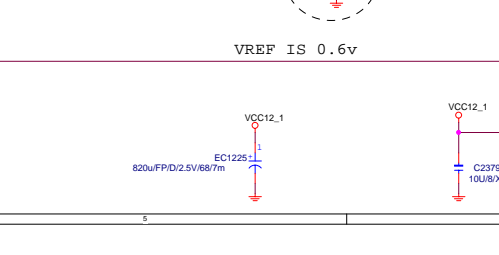
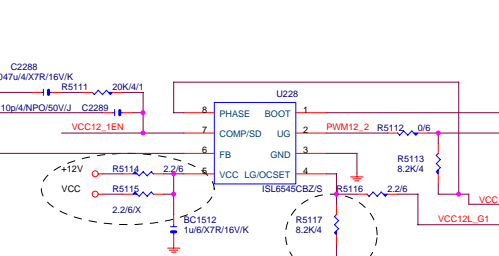
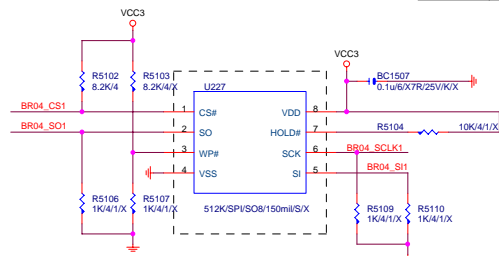






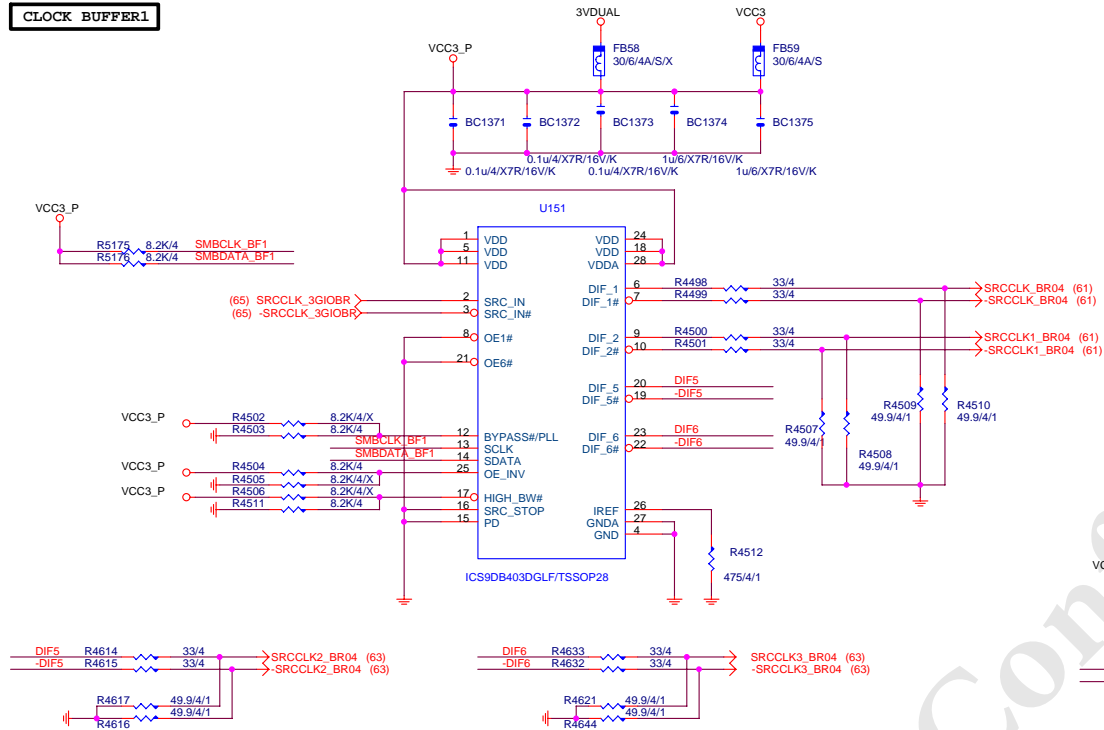
STRAP4	STRAP5
(STRAP5_4)	PEX8
(H,H)	1X16
(H,L)	2X8
(L,H)	1X16
(L,L)	2X8

\* Default internal PU

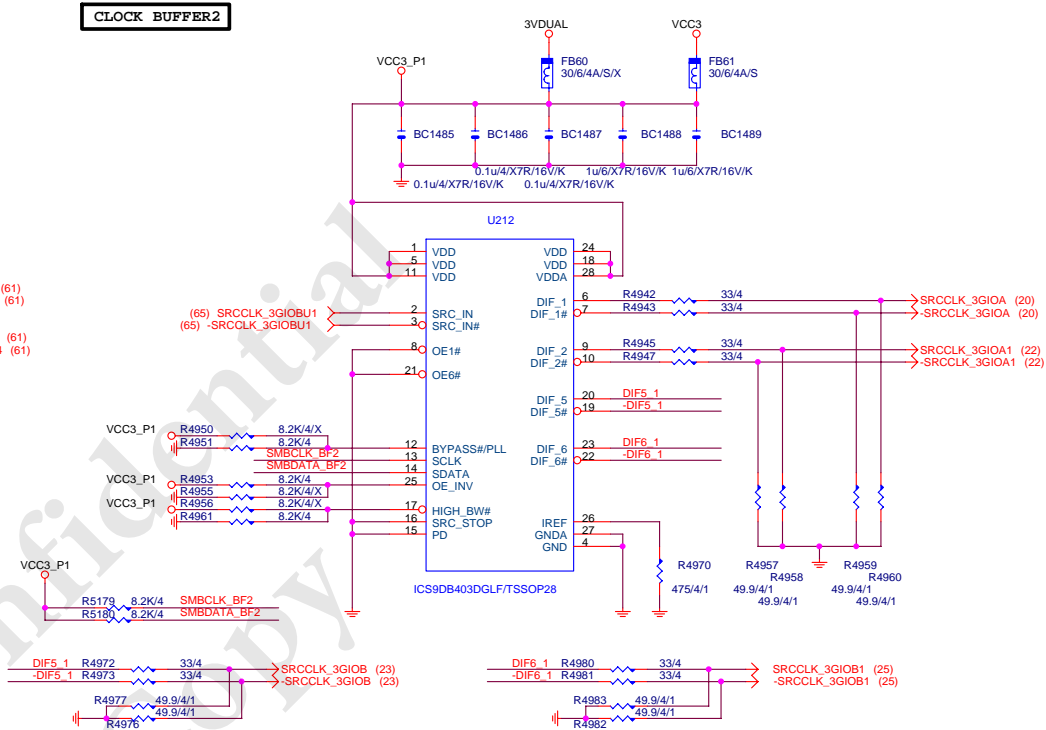




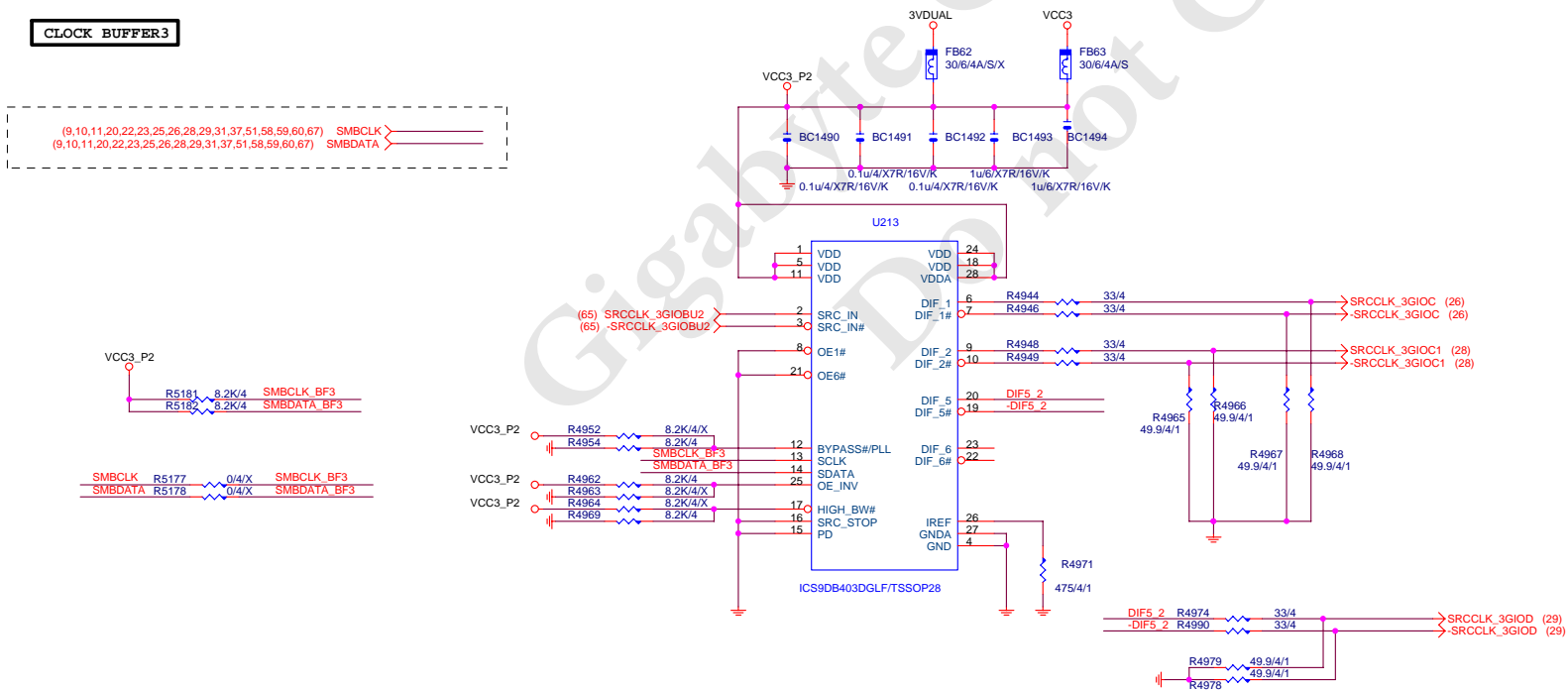
## CLOCK BUFFER1



## CLOCK BUFFER2

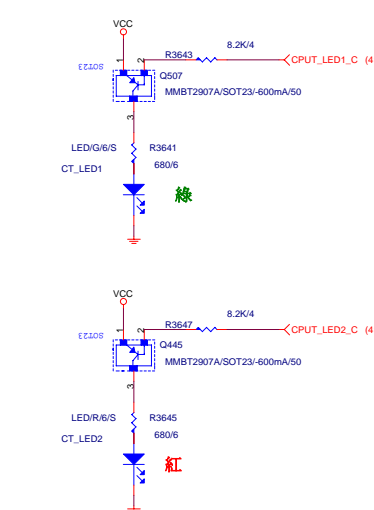


## CLOCK BUFFER3



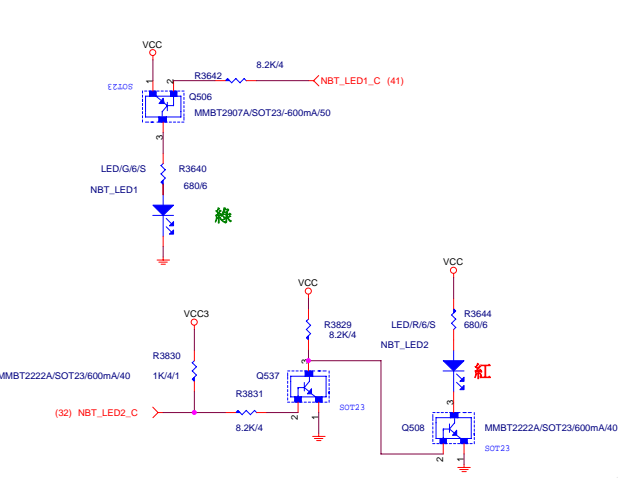
CPU溫度顯示

	I/O	Thermal
CPUT_LED1	GP63	60℃以上
CPUT_LED2	GP35	70℃以上



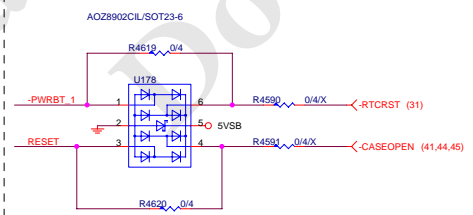
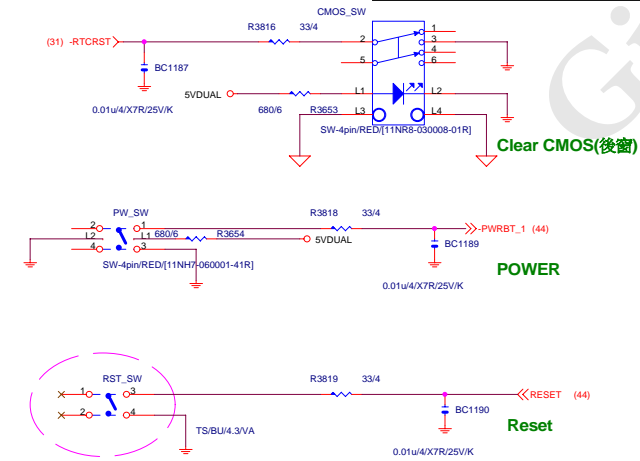
北橋(MCH)溫度顯示

	I/O	Thermal
NBT_LED1	GP30	60℃以上
NBT_LED2	GP31	70℃以上



Switch 部分

Clear CMOS 90℃料號:11NR8-030008-01R.  
Clear CMOS 180℃料號:11NH7-060001-11R.  
Power 180℃料號:11NH7-030001-21R.  
Reset 180℃料號:11NH7-060001-51R.



UPI6262(NCT393) Table

up6262(NCT393)	0X60(0x20)U123	0X62(0x22)U116	0X6A(0x2A)U122	0X66(0x26)U124	0X68(0x28)U115	0X64(0x24)U117
VREF1	CHA_ADJ	VCCA18_PLL_ADJ	VTTD_ADJ	VCORE_ADJ	CHCC_ADJ	VTTD_ADJ
VREF2	DDR18V_ADJ	VCCA1_1_ADJ	CHAC_ADJ	VCC15_ADJ	CHC_ADJ	VCC1_1_I_CH_ADJ
VREF3	CHB_ADJ	VCC11_ADJ	CHBC_ADJ	VCCA1_5_ADJ	MCH_RAMVREF_ADJ	VCC1_9_ADJ

